Set 6 - Single Instruction Multiple Data

cat /proc/cpuinfo

Xeon E3-1245 v5

MMX instructions

SSE / Streaming SIMD Extensions

SSE2 / Streaming SIMD Extensions 2

SSE3 / Streaming SIMD Extensions 3

SSSE3 / Supplemental Streaming SIMD Extensions 3

SSE4 / SSE4.1 + SSE4.2 / Streaming SIMD Extensions 4 ?

AES / Advanced Encryption Standard instructions

AVX / Advanced Vector Extensions

AVX2 / Advanced Vector Extensions 2.0

BMI / BMI1 + BMI2 / Bit Manipulation instructions

F16C / 16-bit Floating-Point conversion instructions

FMA3 / 3-operand Fused Multiply-Add instructions

EM64T / Extended Memory 64 technology / Intel 64 ?

NX / XD / Execute disable bit ?

HT / Hyper-Threading technology ?

VT-x / Virtualization technology ?

VT-d / Virtualization for directed I/O

TBT 2.0 / Turbo Boost technology 2.0 ?

SGX / Software Guard Extensions

TXT / Trusted Execution technology

TSX / Transactional Synchronization Extensions