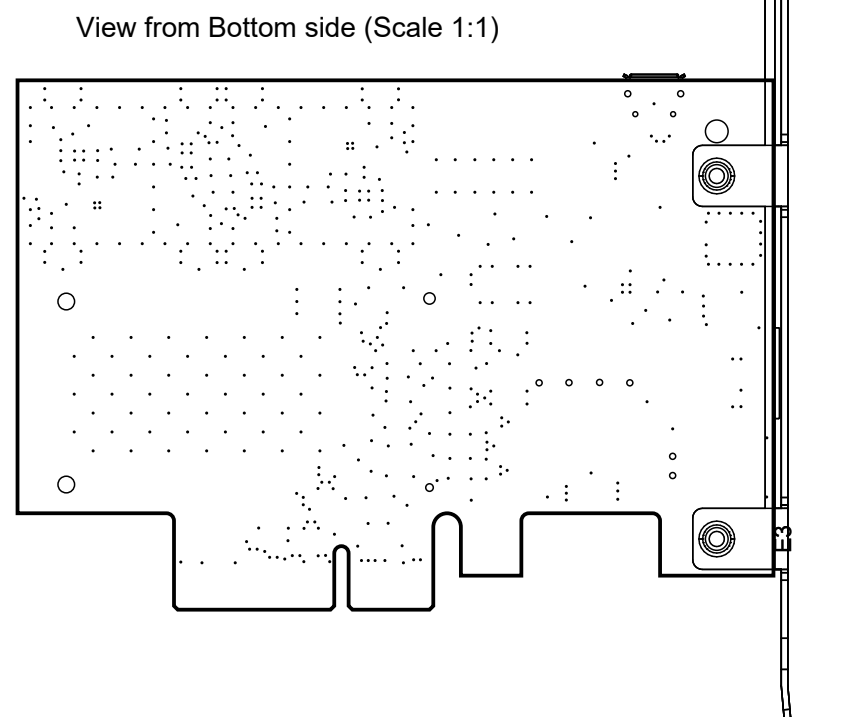


Count	Hole Size	Plated	Drill Layer Pair
8	0.20mm	Plated	Top Layer - Bottom Layer
490	0.40mm	Plated	Top Layer - Bottom Layer
2	0.85mm	Plated	Top Layer - Bottom Layer
8	1.00mm	Plated	Top Layer - Bottom Layer
1	1.20mm	Non-Plated	Top Layer - Bottom Layer
1	1.70mm	Non-Plated	Top Layer - Bottom Layer
2	2.40mm	Plated	Top Layer - Bottom Layer
3	3.20mm	Plated	Top Layer - Bottom Layer
515 Total			



Layer Stack Legend		3	3.20mm	Plated	Top Layer - Bottom Layer	
Material	Layer	515 Total	Thickness	Dielectric Material	Type	Gerber
	Top Overlay				Legend	GTO
	Surface Material					
	Top Solder	0.01mm	Solder Resist	Solder Mask	GTS	
	Copper	0.04mm		Signal	GTL	
	Core	0.32mm	FR-4	Dielectric		
	Copper	0.04mm		Signal	G1	
	Prepreg	0.70mm	FR-4	Dielectric		
	Copper	0.04mm		Signal	G2	
	Core	0.32mm	FR-4	Dielectric		
	Copper	0.04mm		Signal	GBL	
	Surface Material	0.01mm	Solder Resist	Solder Mask	GBS	
	Bottom Solder					
	Bottom Overlay				Legend	GBO

UNLESS OTHERWISE SPECIFIED: DIMENSIONS ARE IN MILLIMETERS SURFACE FINISH: TOLERANCES:				FINISH: Gold plating, thickness of 30 micro-inches minimum for Edge Connector finger tabs and 4 micro-inches for entire board. Facets – 20° +/- 20° x 1.4 mm for edge connector.				DEBURR AND BREAK SHARP EDGES				DO NOT SCALE DRAWING				REVISION 2			
	NAME			SIGNATURE		DATA						TITLE: LimeFEA-mPCIe 1v0							
DRAW						2024-04													
CHK'D																			
APPV'D																			
MFG																			
QA						MATERIAL: PCB is four layer FR4 1.5 ±0.1 mm. Custom-made PCIe bracket is Nickel plated or Stainless steel sheet 0.86mm.						DWG NO.						A3	
						WEIGHT:						SCALE 1:1				SHEET 1 OF 1			