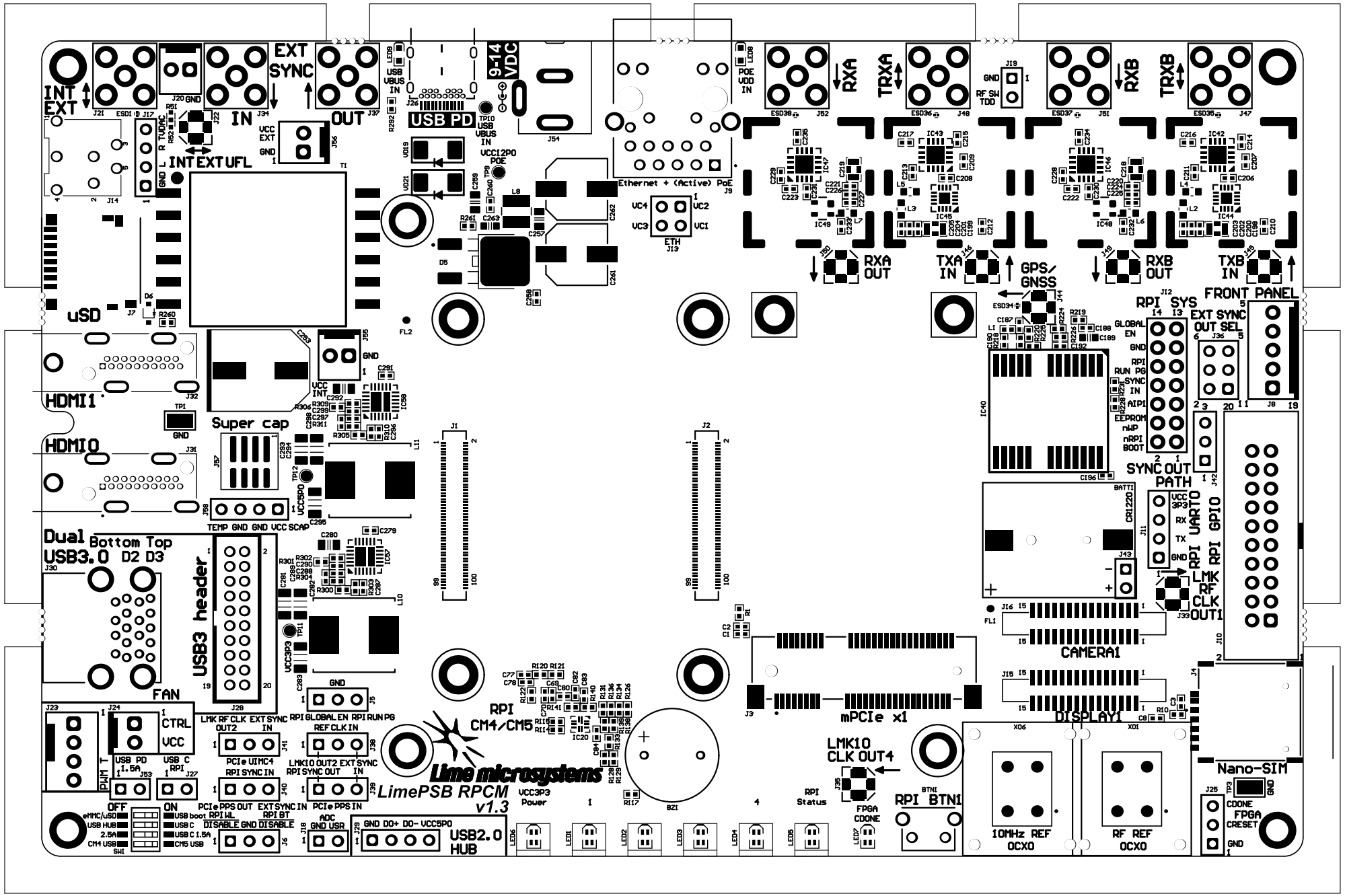
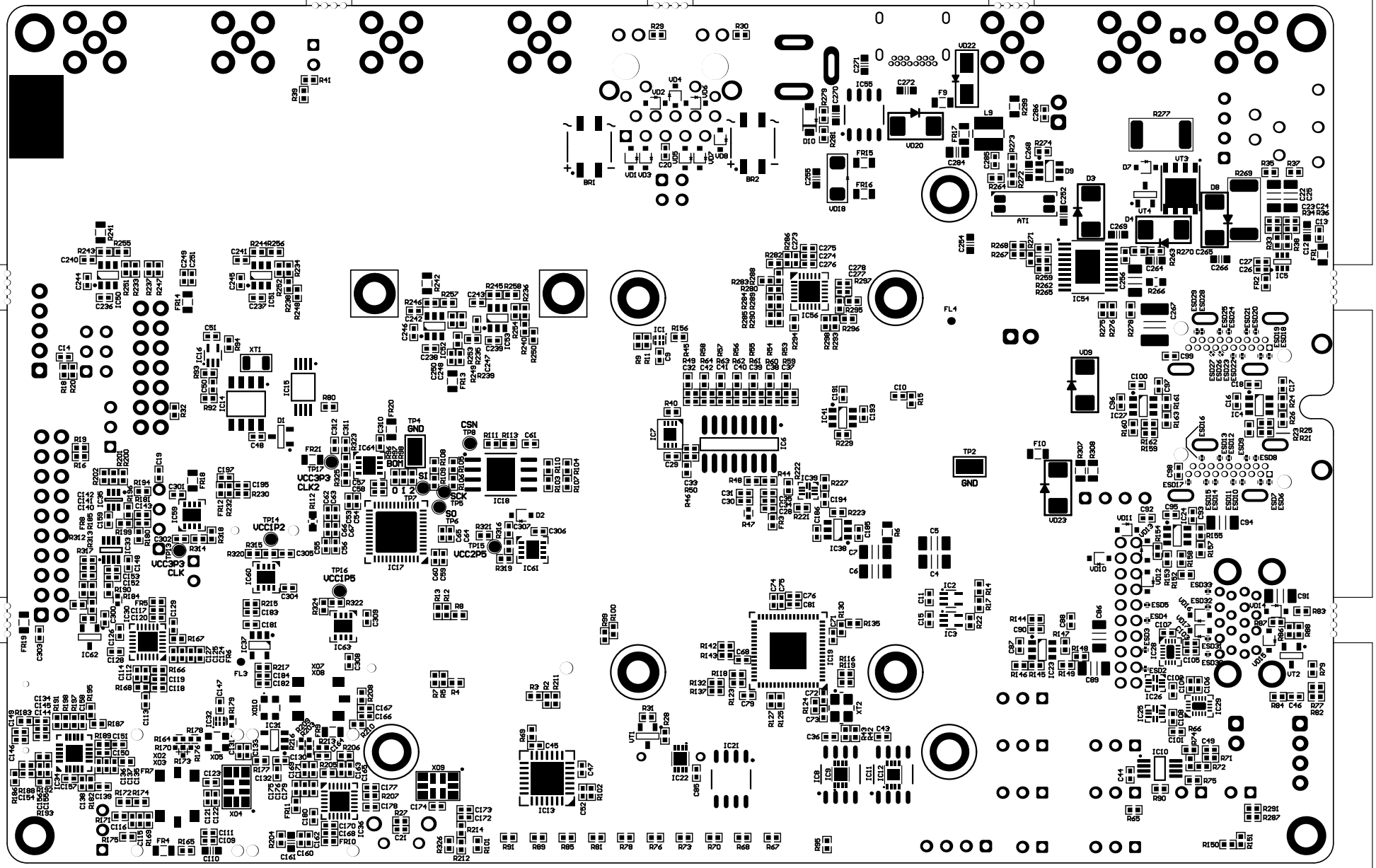
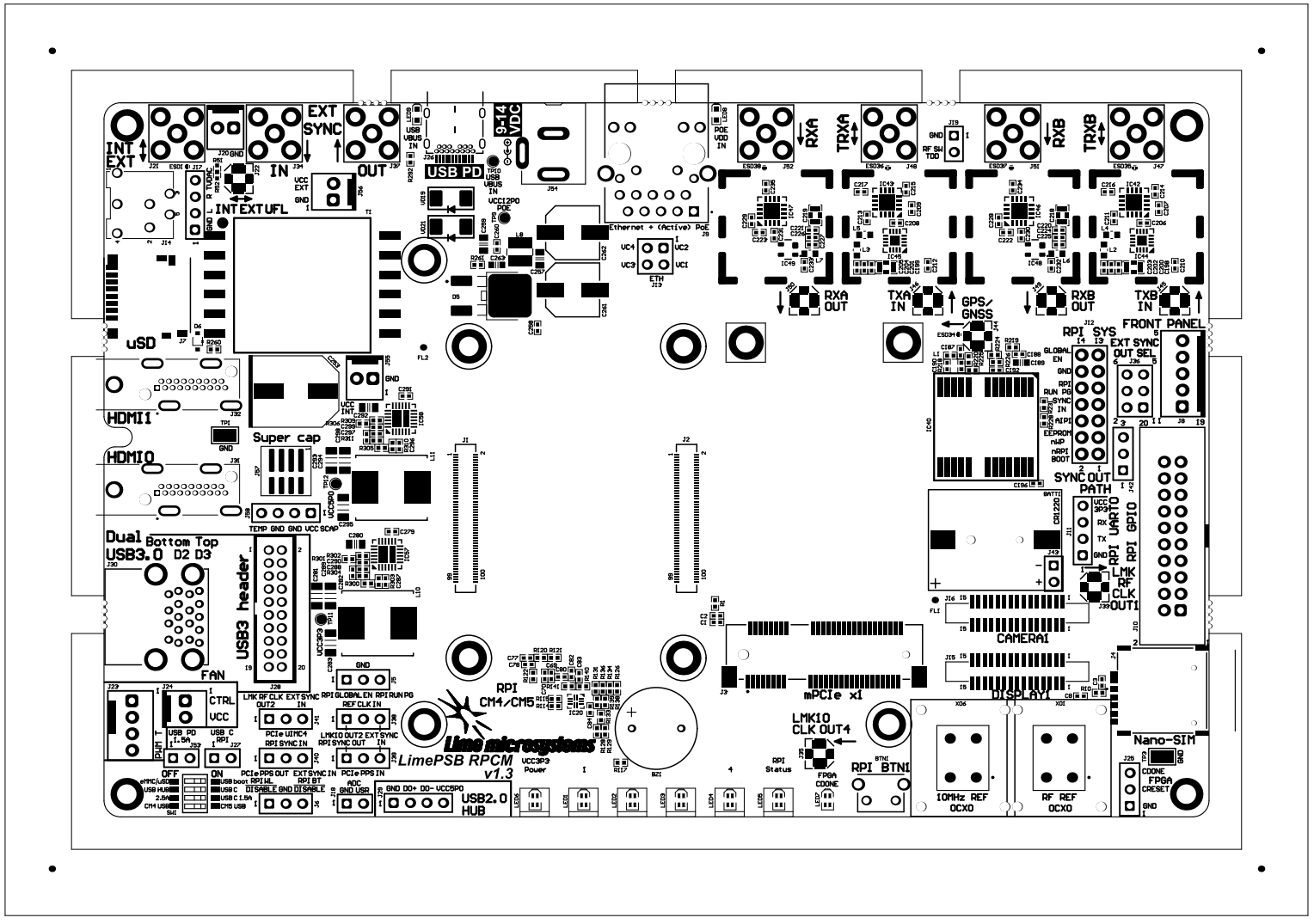

INT
EXT
EXT SYNC
IN
OUT
9-14 VDC
USB PD
USB VBUS IN
VCC12P0 POE
Ethernet + (Active) PoE
ETH
RXA
TRXA
RXB
TRXB
RF SW TDD
POE VDD IN
RXA OUT
TXA IN
GPS/ GNSS
RXB OUT
TXB IN
FRONT PANEL
RPI SYS
EXT SYNC OUT SEL
GLOBAL EN
GND
RPI RUN PG
SYNC IN
AIPI
EEPROM nWP
nRPI BOOT
SYNC OUT PATH
RPI UART0
RPI GPIO
LMK RF CLK OUT1
CAMERA1
DISPLAY1
uSD
HDMI1
HDMI0
Super cap
Dual USB3.0
Bottom Top
D2 D3
USB3 header
FAN
CTRL
VCC
LMK RF CLK OUT2
EXT SYNC IN
RPI GLOBAL EN
RPI RUN PG
REF CLK IN
USB PD 1.5A
USB C RPI
PCIe UIMC4 RPI SYNC IN
LMK10 OUT2 EXT SYNC RPI SYNC OUT
IN
OFF
ON
eMMC/uSD
USB HUB
2.5A
CM4 USB
USB boot
USB C
USB C 1.5A
CM5 USB
PCIe PPS OUT
EXT SYNC IN
PCIe PPS IN
RPI WL DISABLE
RPI BT DISABLE
GND
ADC GND USR
GND DO+ DO- VCC5P0
USB2.0 HUB
RPI CM4/CM5
Lime microsystems
LimePSB RPCM v1.3
VCC3P3 Power
RPI Status
mPCIe x1
LMK10 CLK OUT4
FPGA CDONE
RPI BTN1
10MHz REF OCXO
RF REF OCXO
Nano-SIM
CDONE
FPGA CRESET
GND


CSN
SI
SCK
SO
GND
VCC3P3
CLK2
VCC3P3
CLK
VCCIP2
VCCIP5
VCC2P5
GND


INT
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USB C
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LMK RF CLK OUT2
EXT SYNC IN
RPI GLOBAL EN
RPI RUN PG
REF CLK IN
PCIe UTMC4
RPI SYNC IN
LMK10 OUT2
EXT SYNC IN
RPI SYNC OUT
PCIe PPS OUT
EXT SYNC IN
PCIe PPS IN
RPI BT
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GND USR
GND D0+ D0- VCC5P0
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VCCIP2
VCC3P3
CLK
VCCIP5
VCC2P5
GND