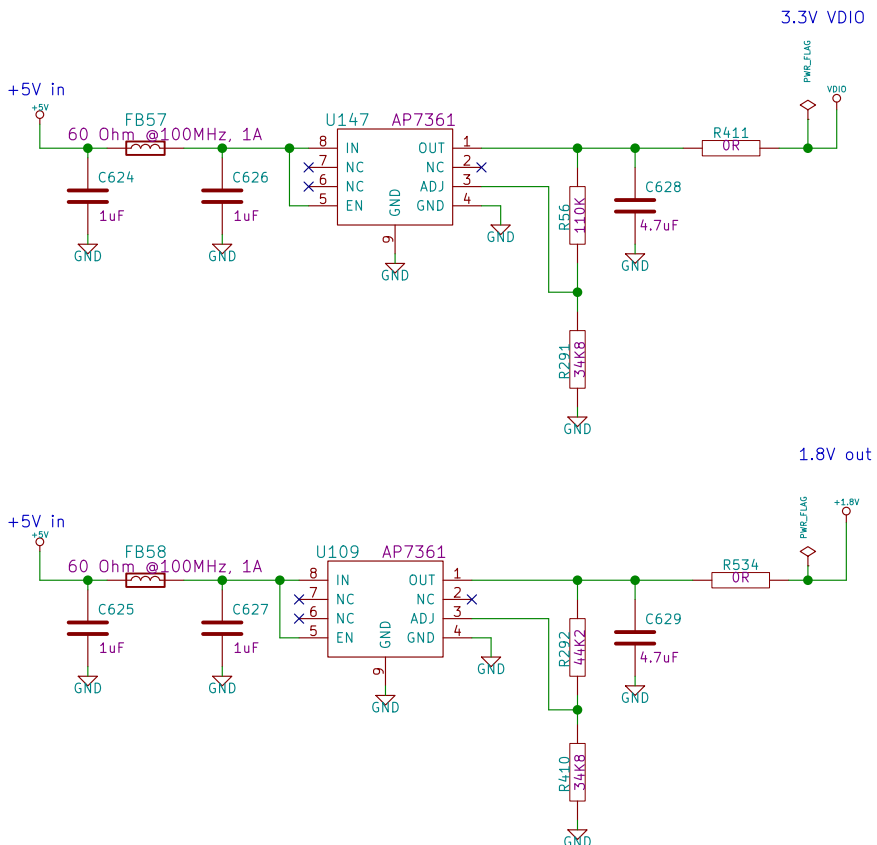
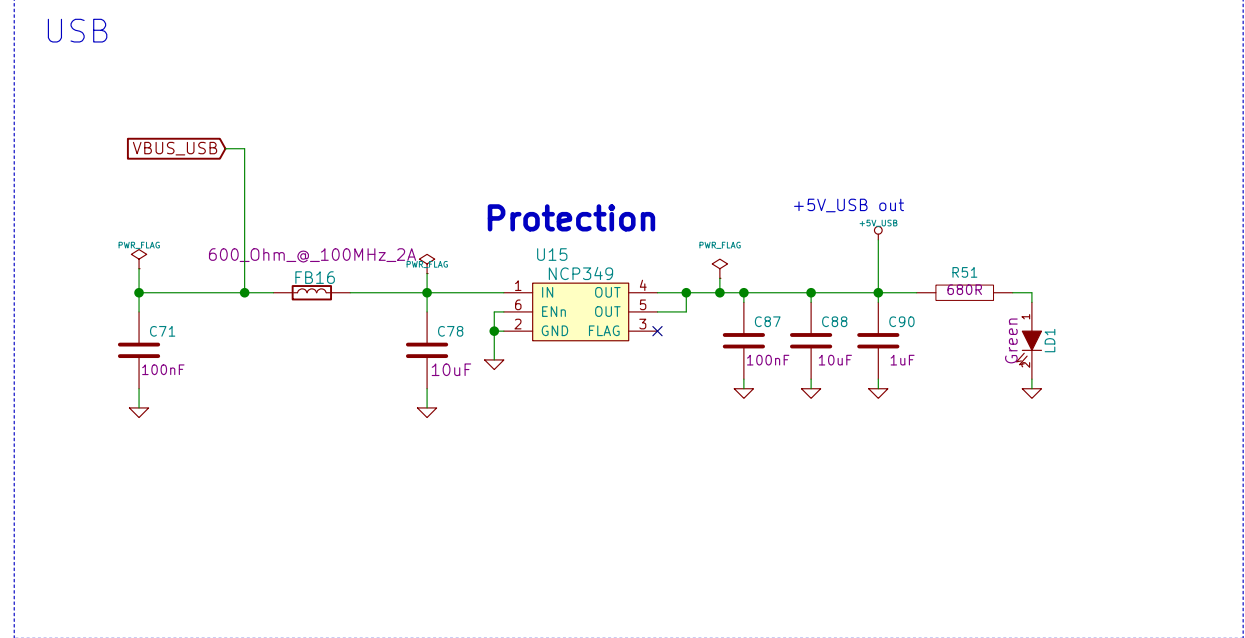
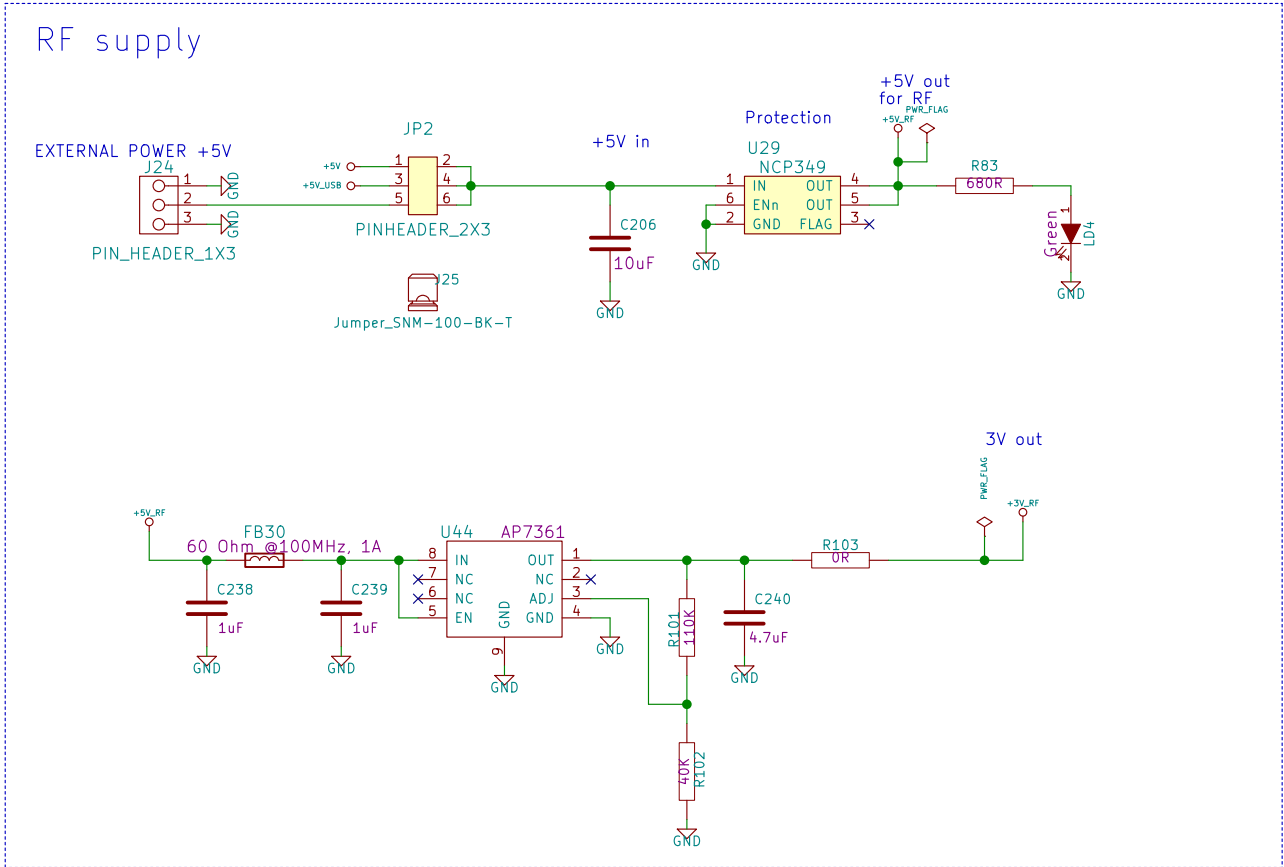
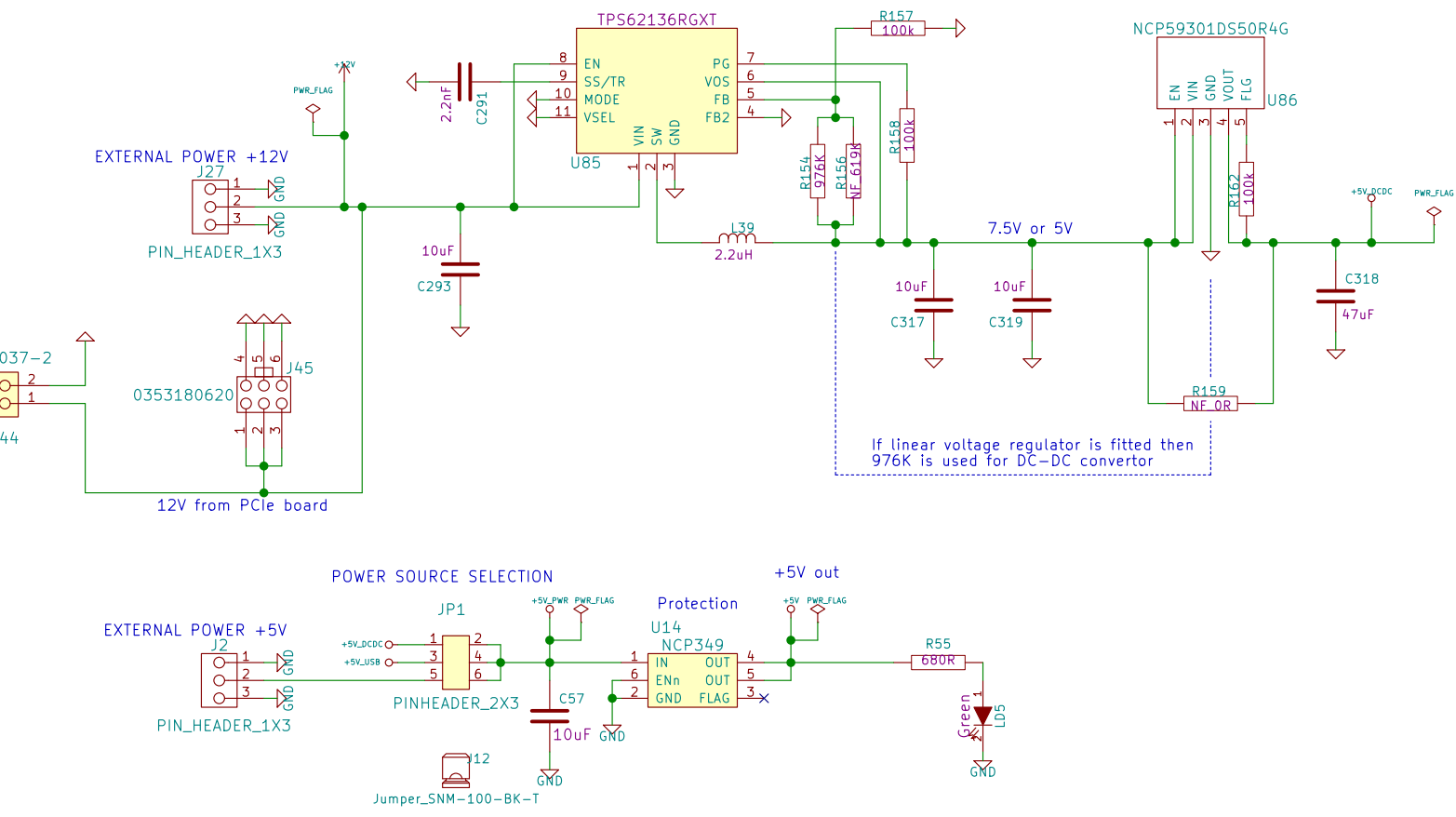


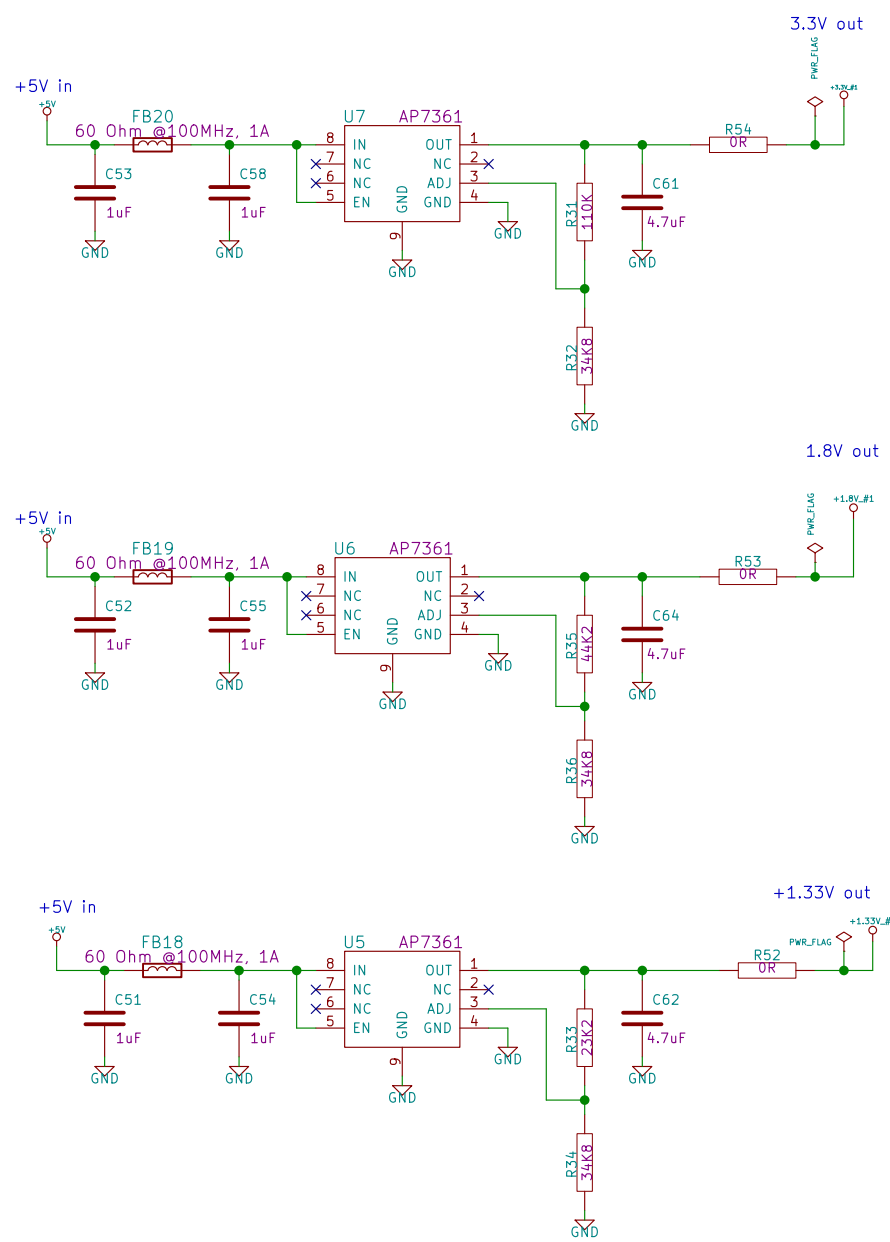
LMS8001 #1

LMS8001 #2

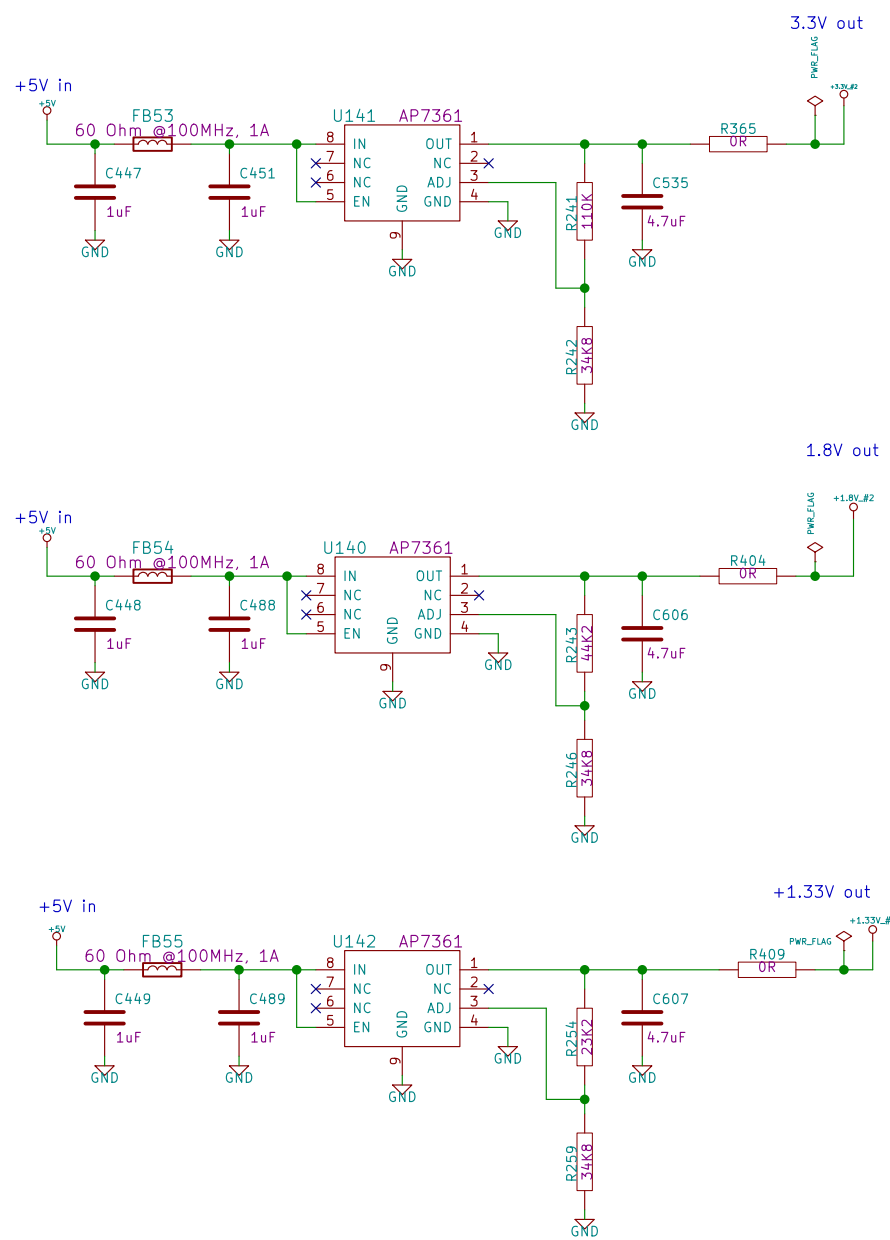
For DRC pass  
PWR\_FLAG



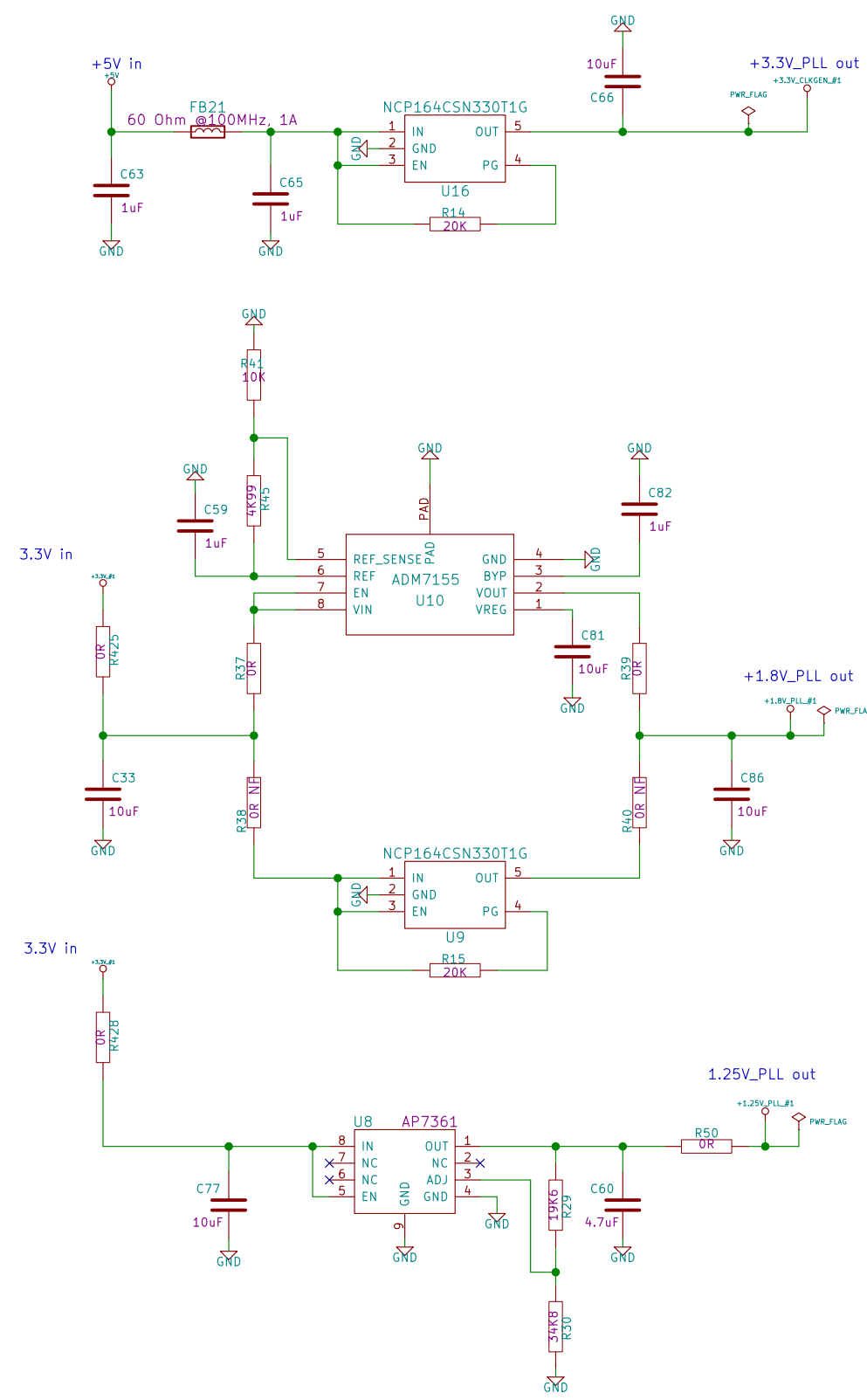
LMS8001\_#1 supply



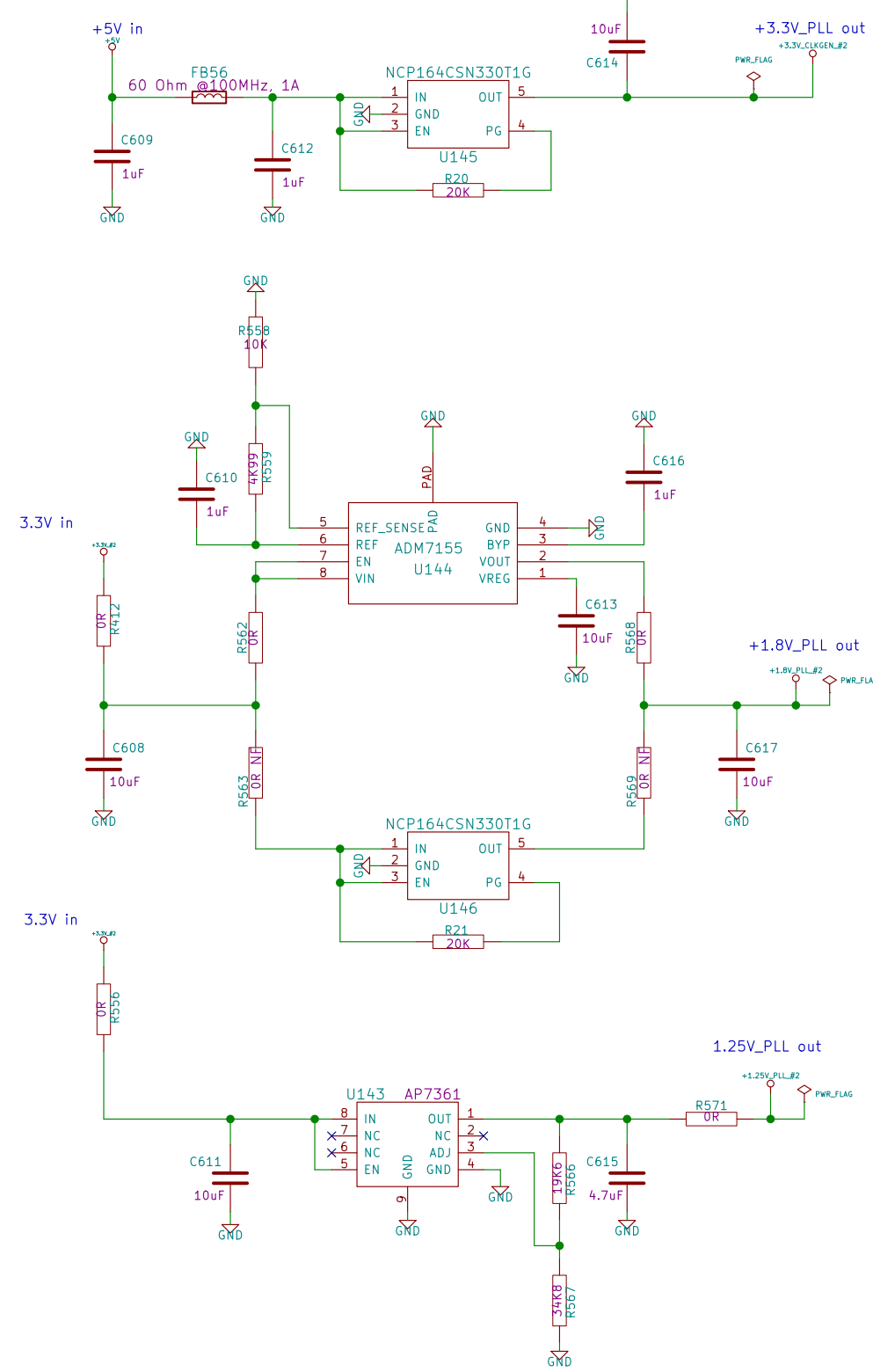
LMS8001\_#2 supply



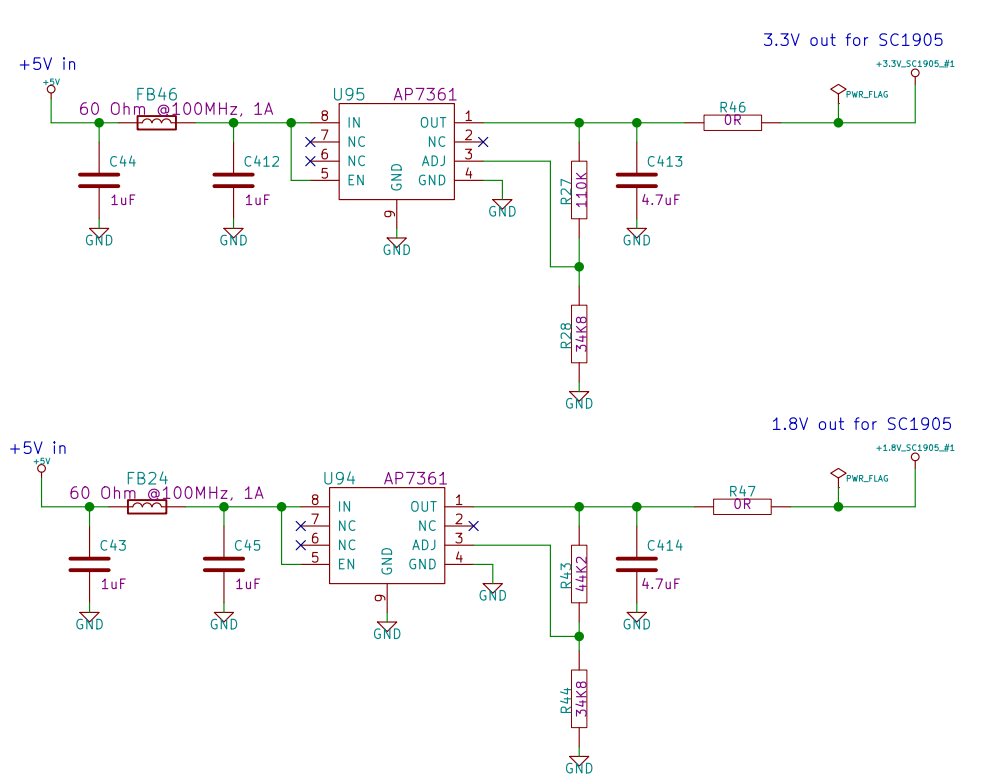
PLL Supply



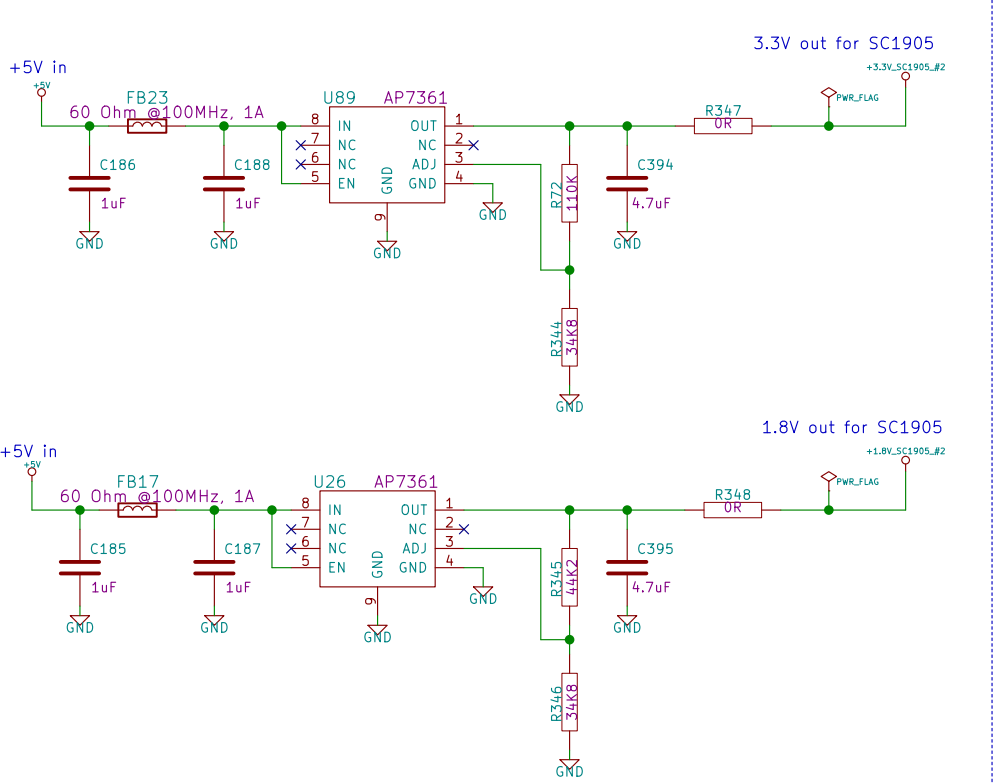
PLL Supply



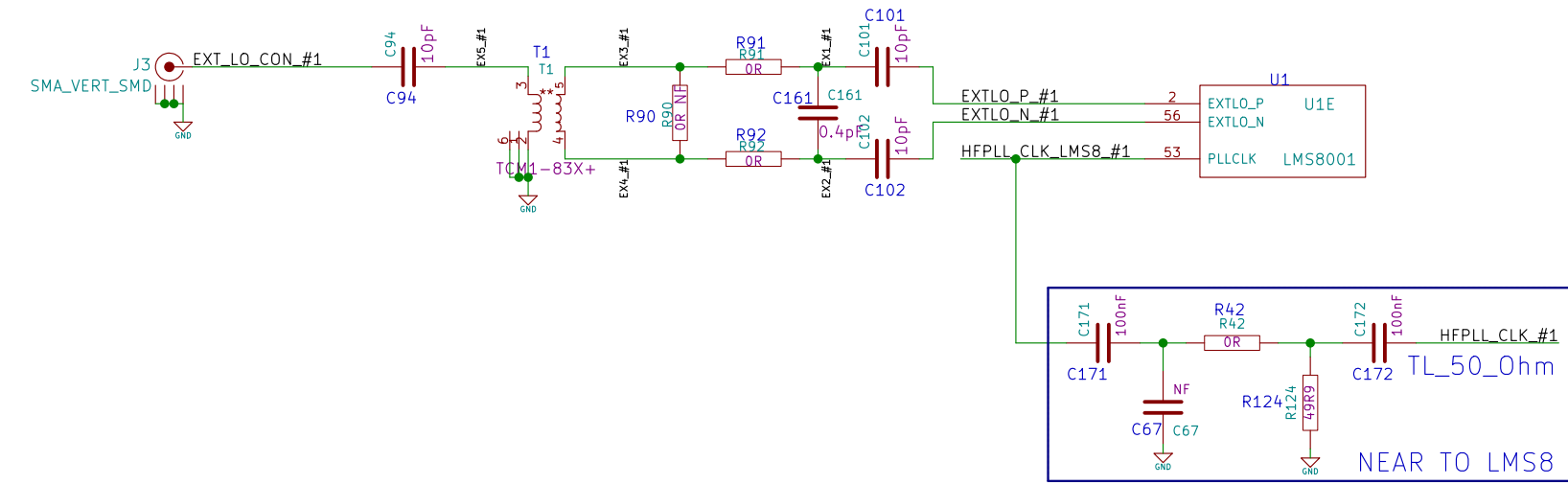
SC1905\_#1 Chips Supply



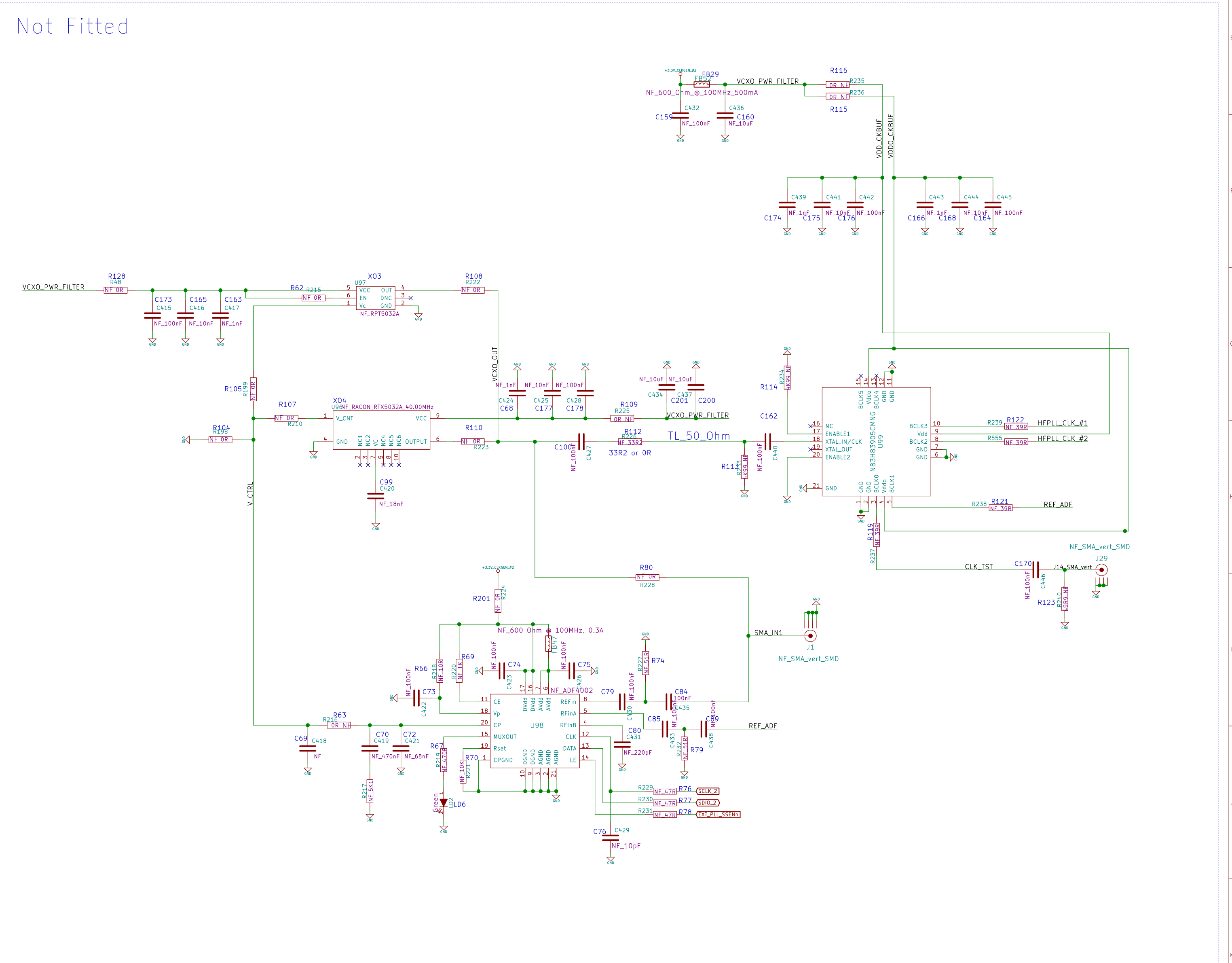
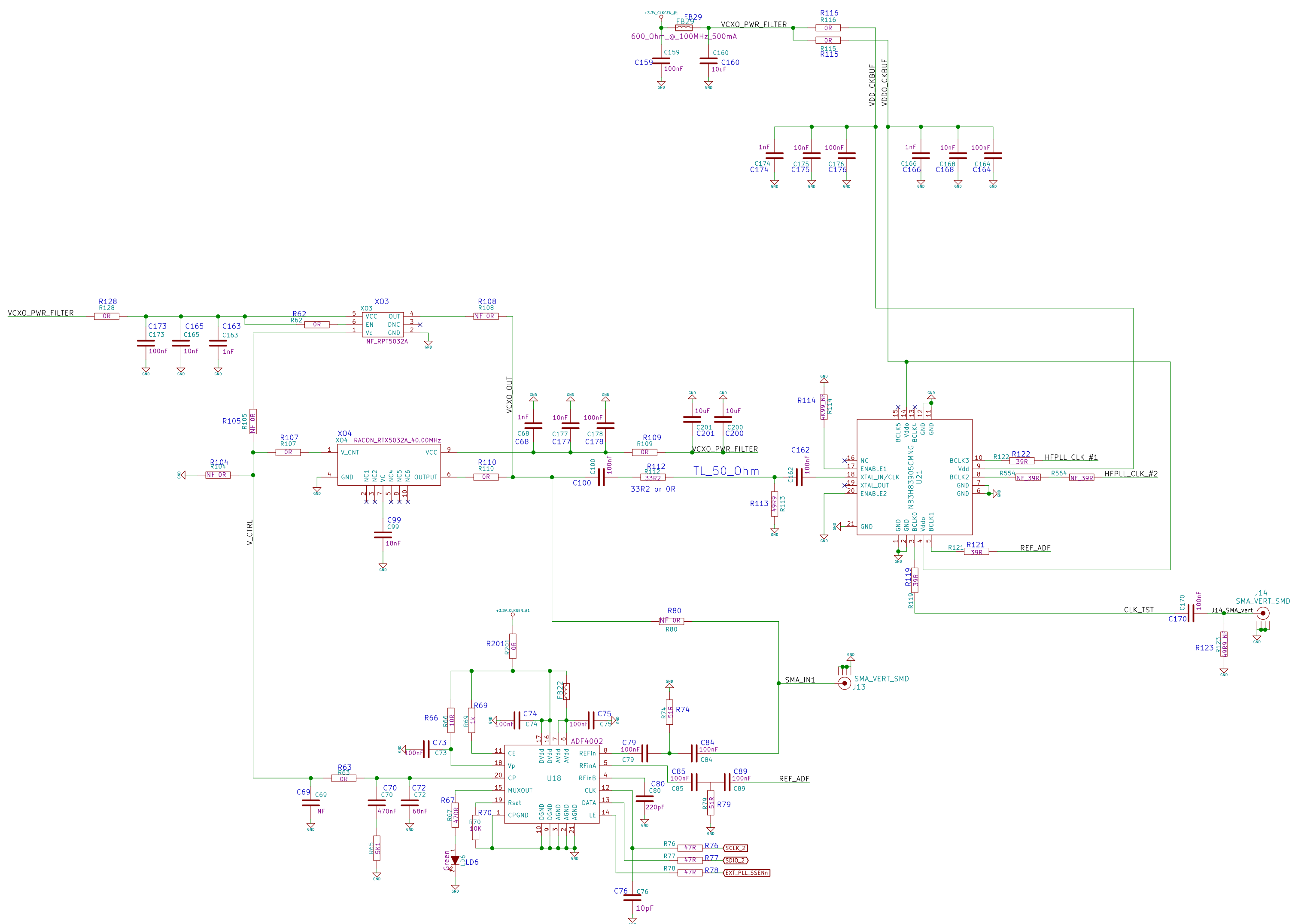
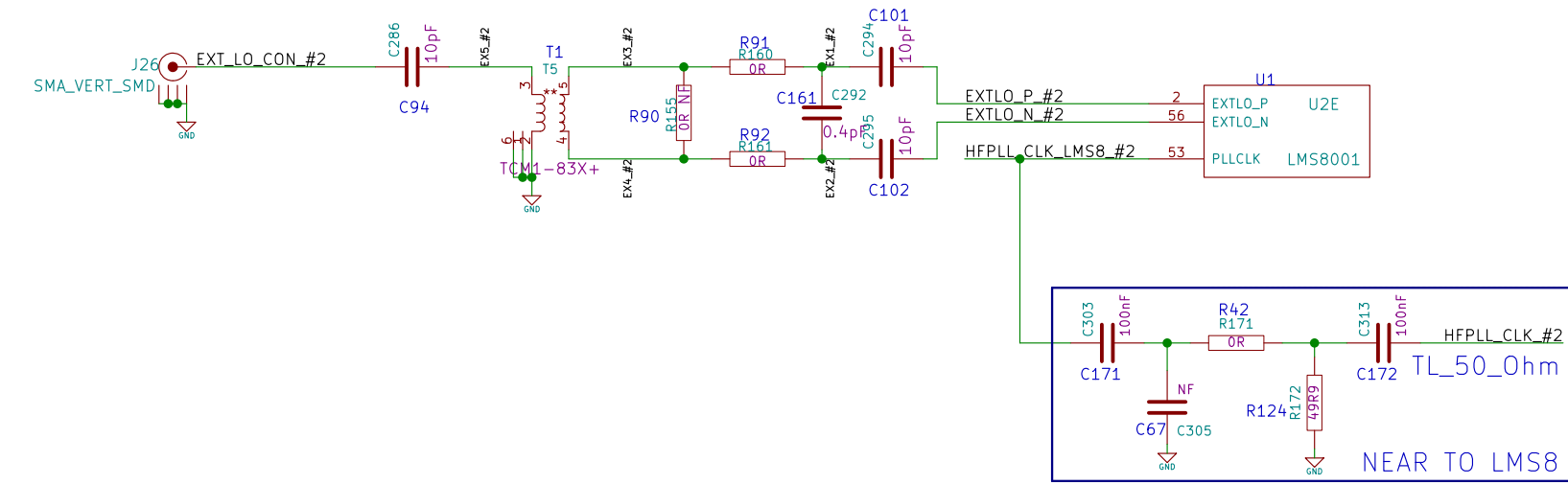
SC1905\_#2 Chips Supply



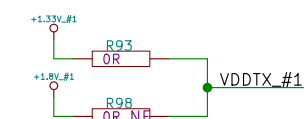
# LMS8001 #1



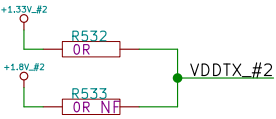
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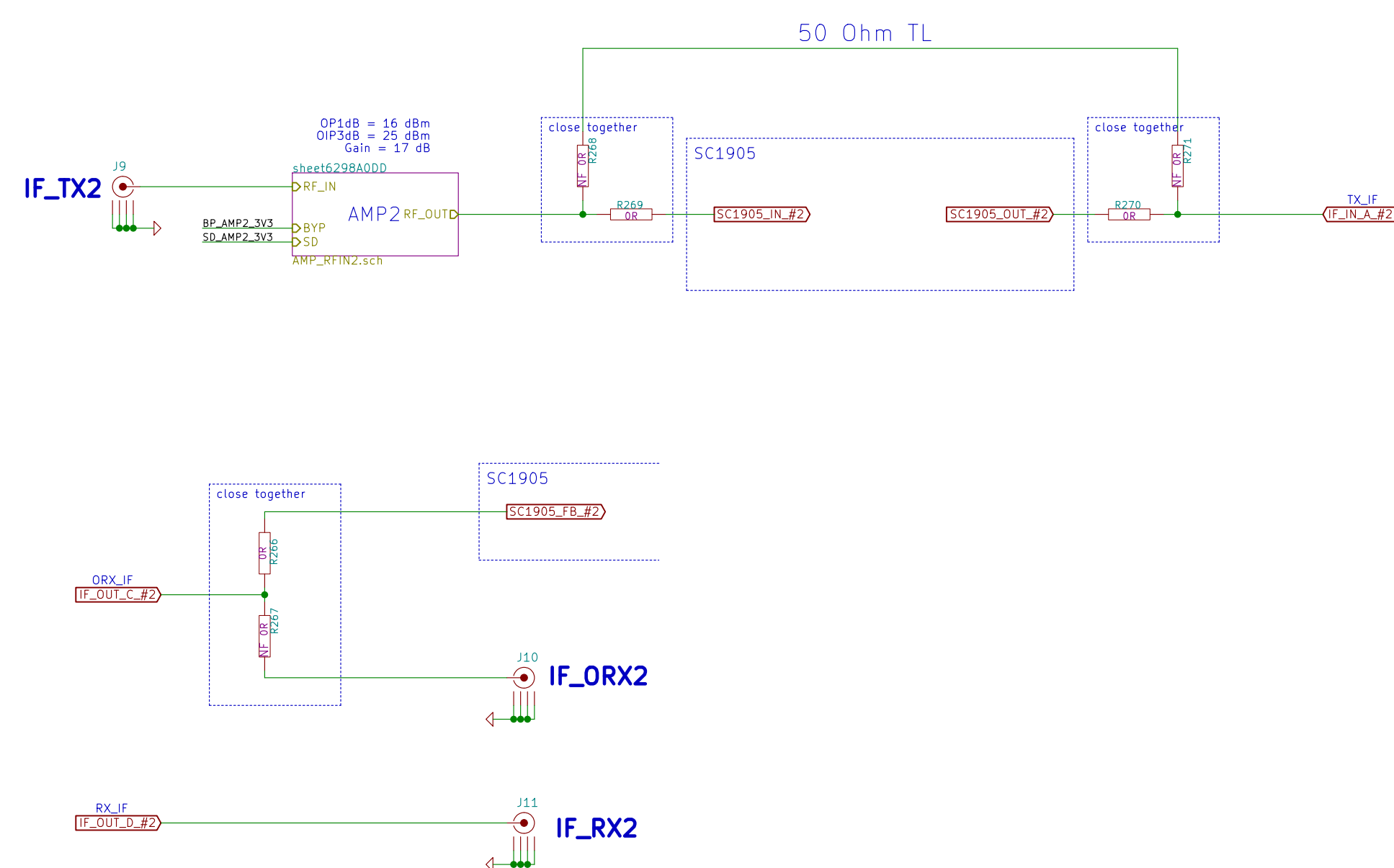


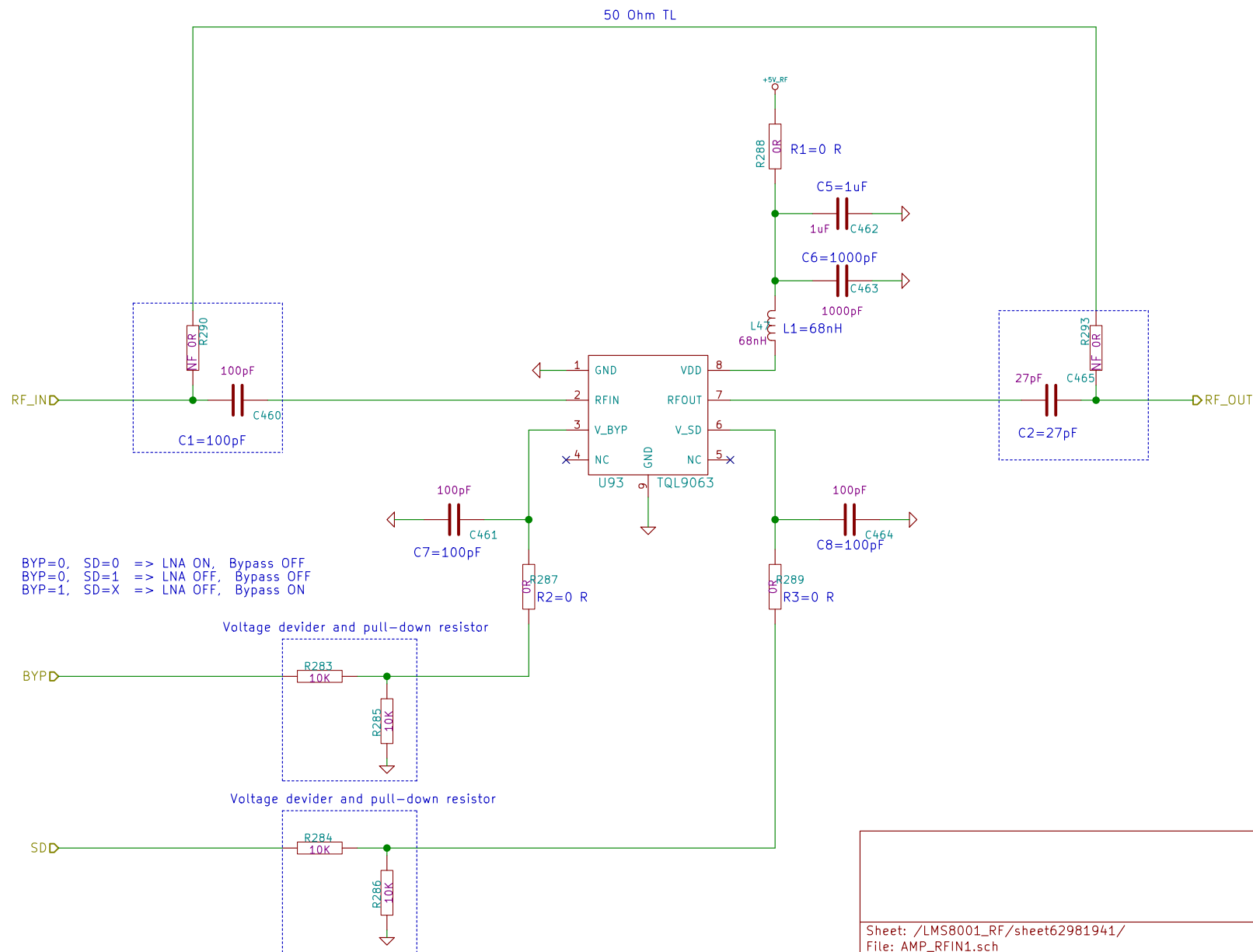


LMS8001 #2



NOT USED  
DOWN CONVERSION





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File: AMP\_RF1.sch

**Title:**

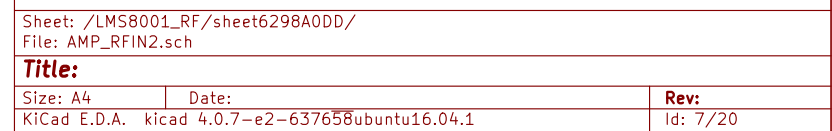
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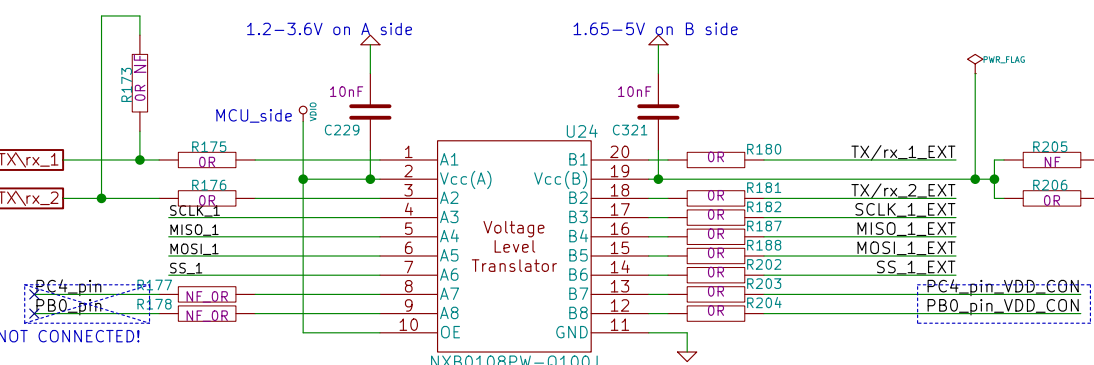
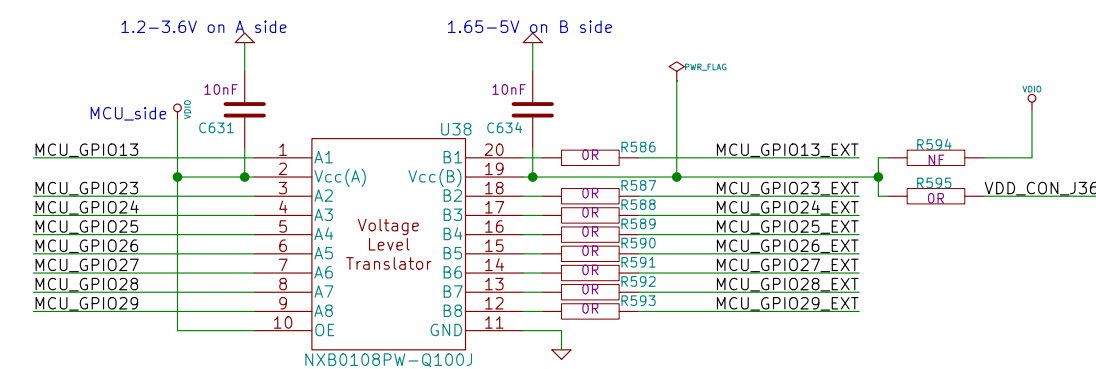
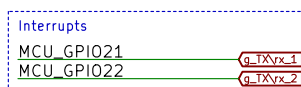
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**Rev:**

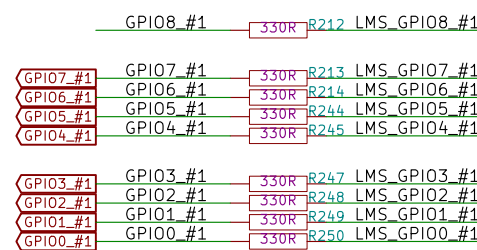
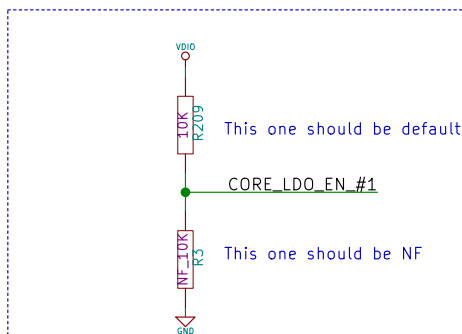
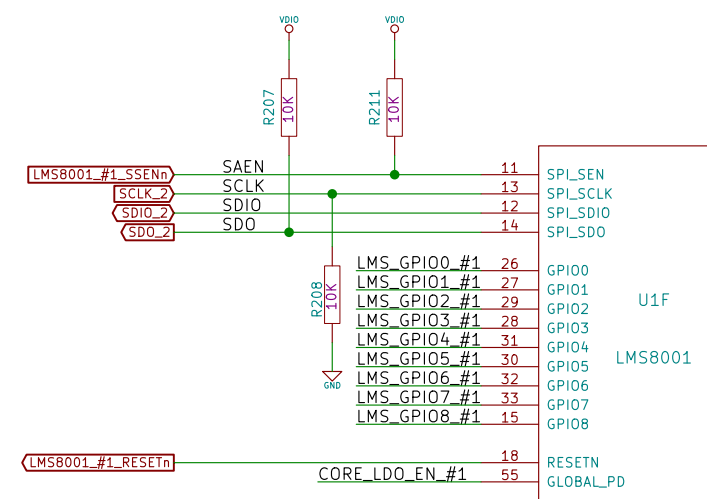
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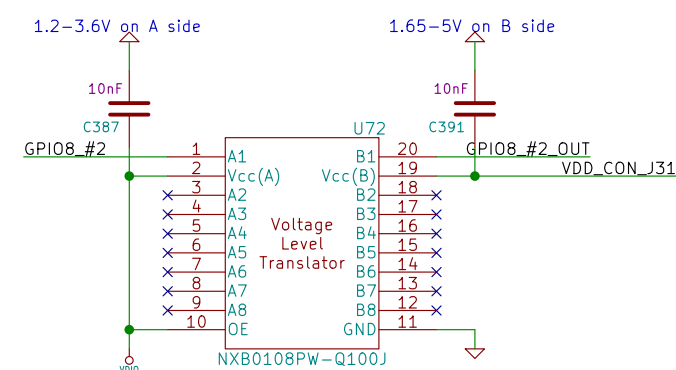
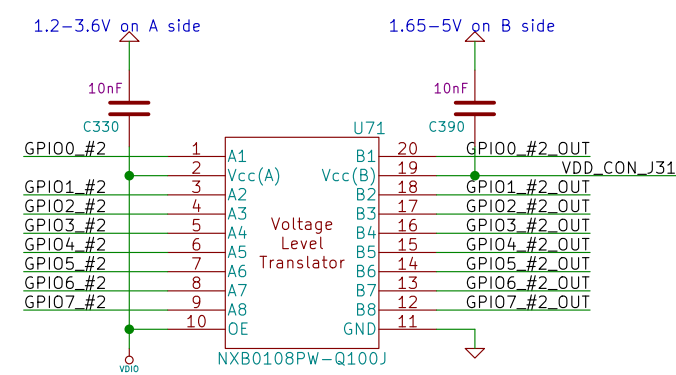
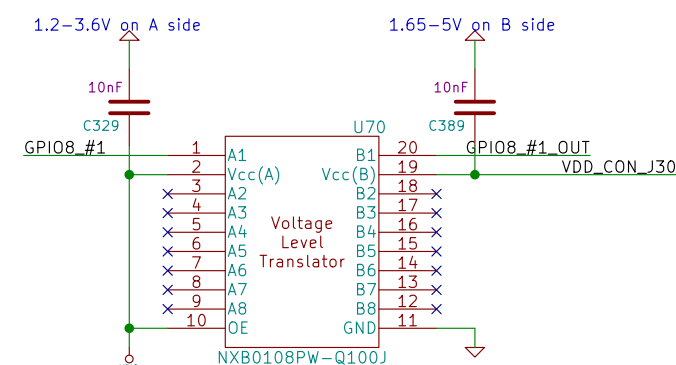
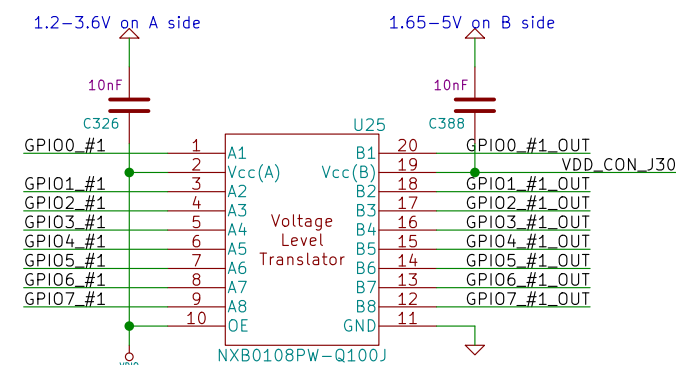
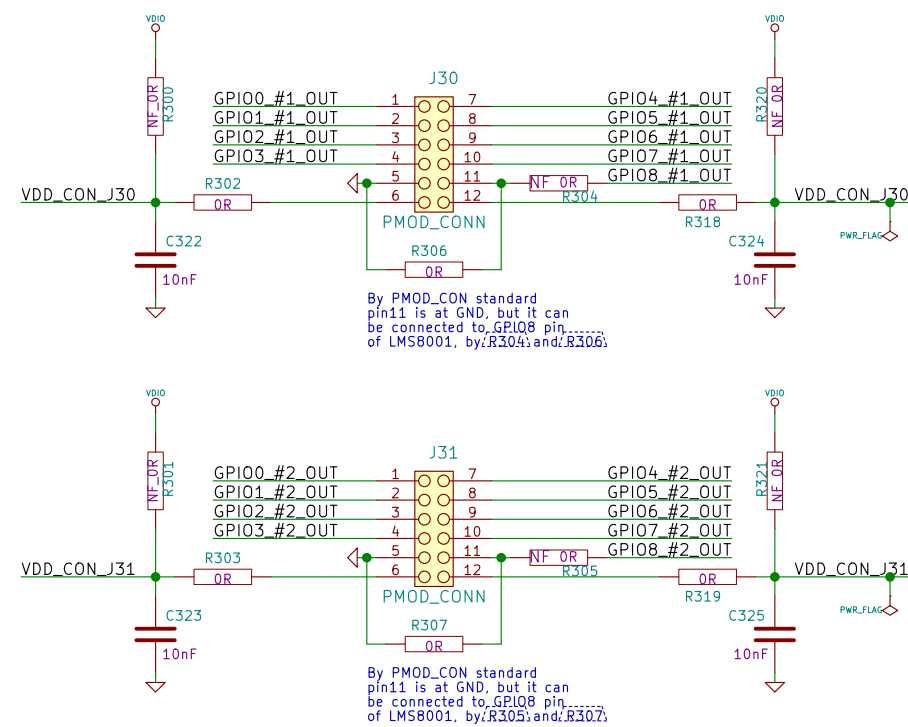
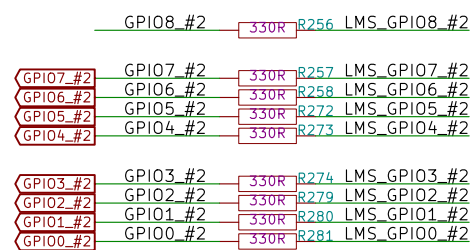
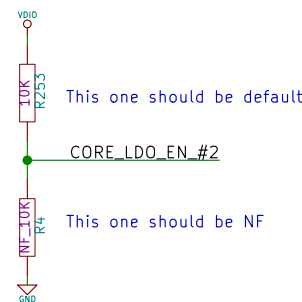
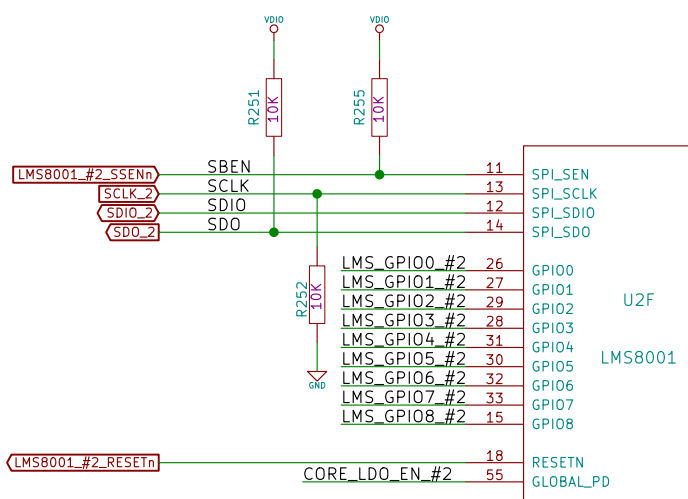




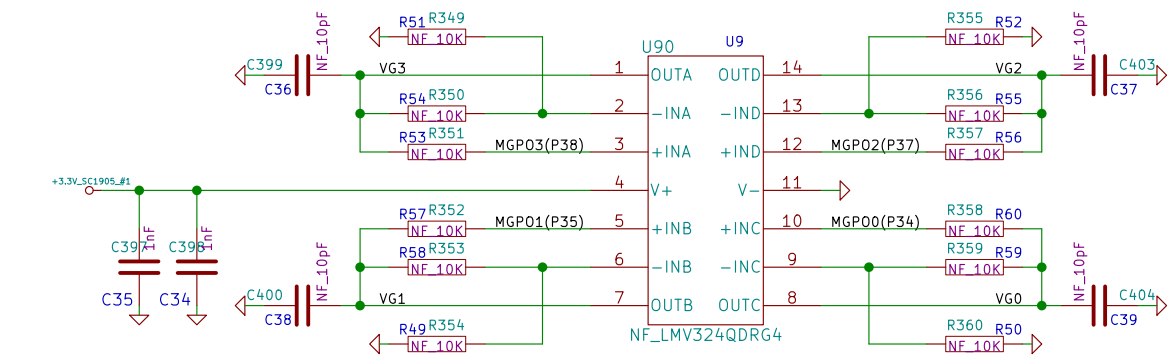
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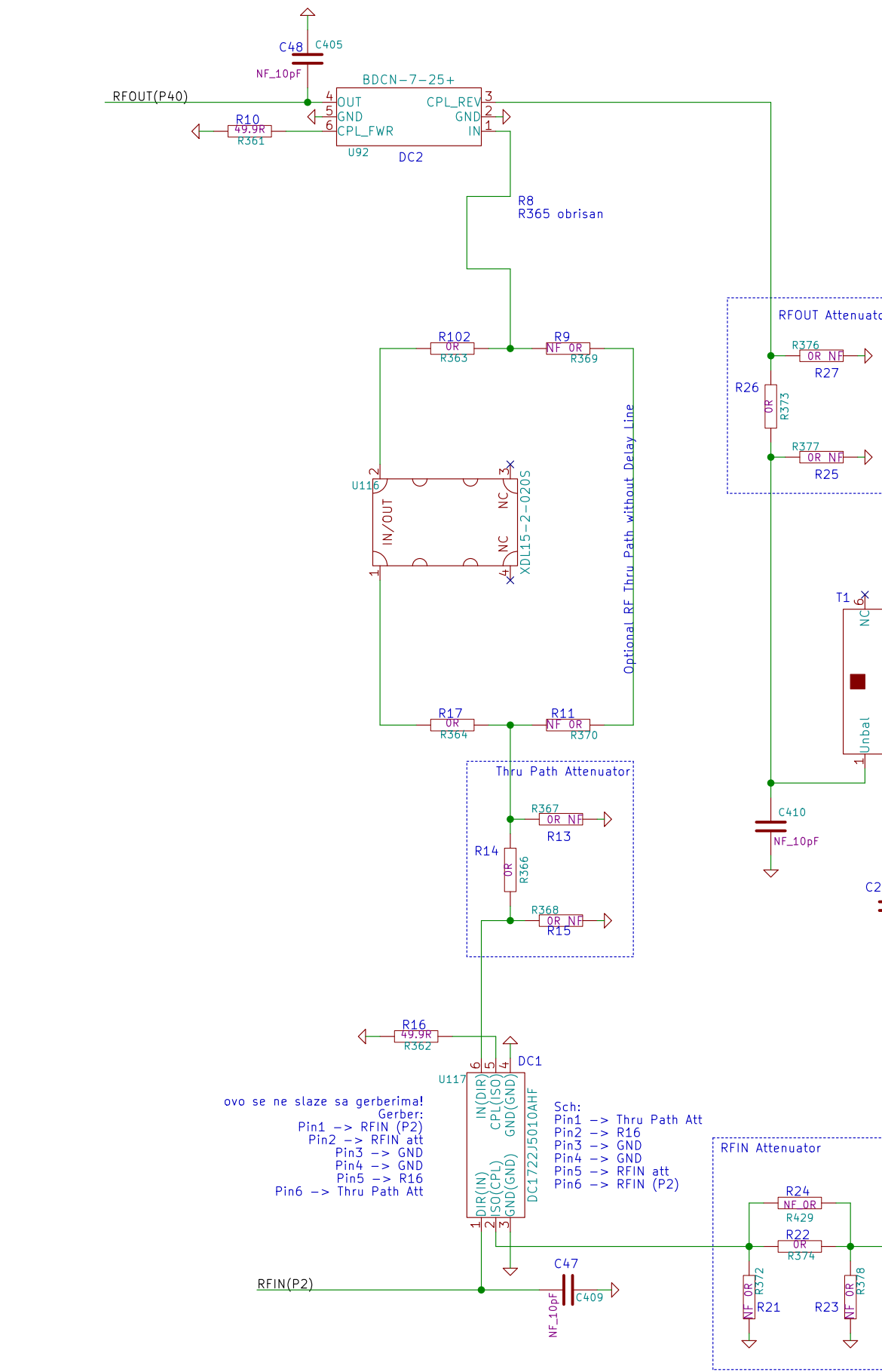
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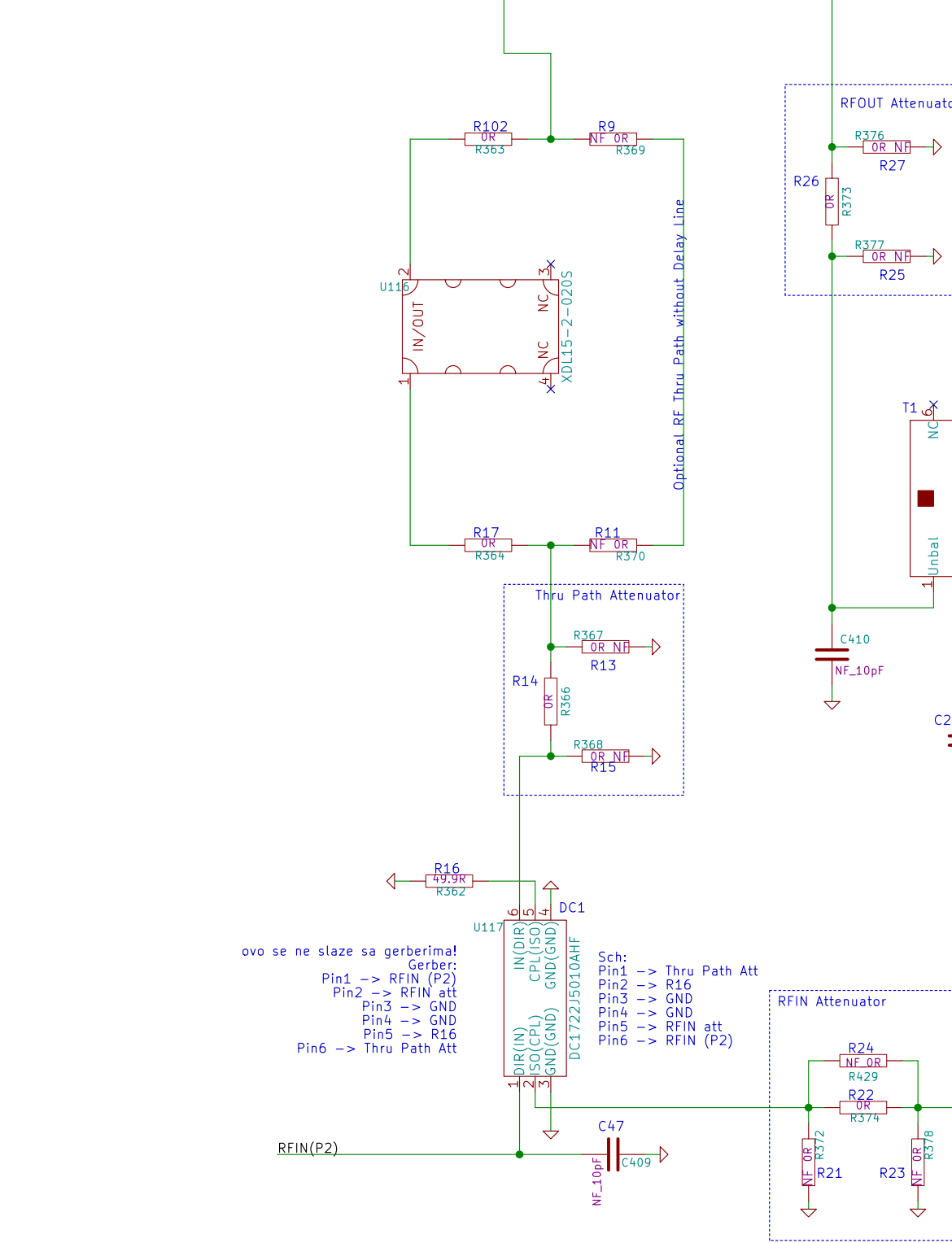
### Buffers for MGPOUT 0-3 Not Fitted



Master Board	EVK1900 board
STATO(P16)	STATO
RESETP(P25)	RESETP
SSM(P24)	SSM
SBI(P10)	SBI
SDD(P26)	SDD
SCLK(P23)	SCLK
WDTEN(P22)	WDTEN
LOADENB(P27)	LOADENB
DGPIN1(P17)	DGPIN1
DGPIN0(P18)	DGPIN0

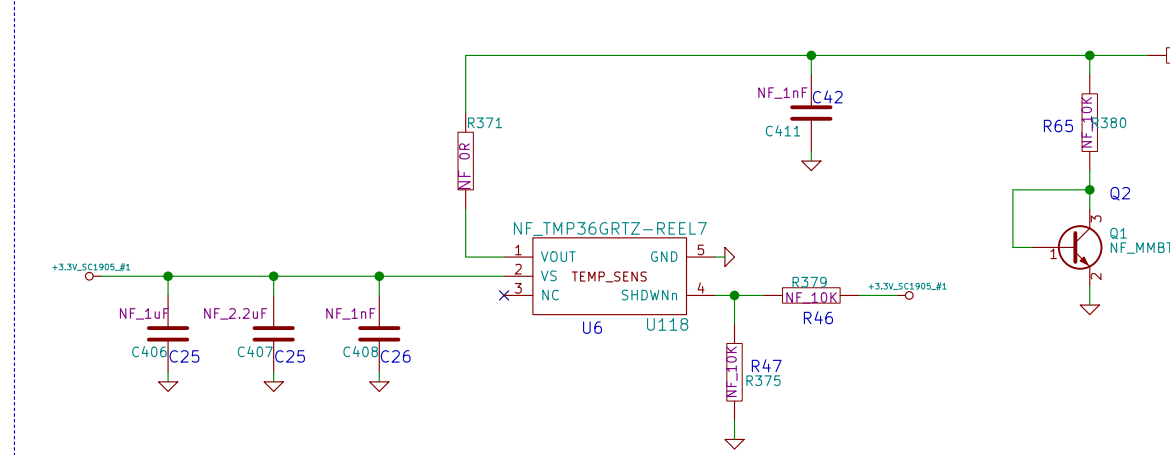


Master Board	EVK1900 board
WDTEN	WDTEN
LOADENB	LOADENB
DGPIN1	DGPIN1
DGPIN0	DGPIN0

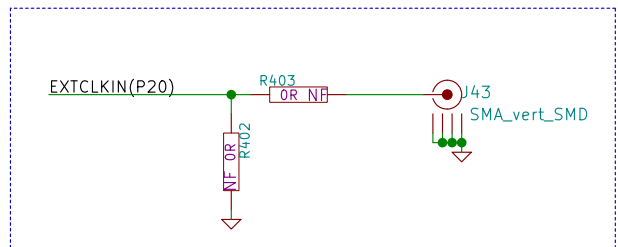
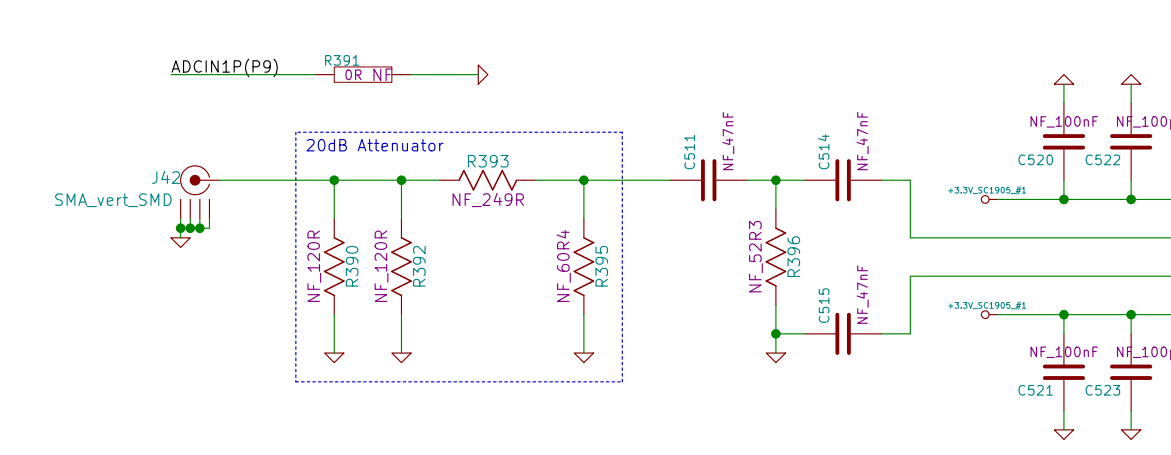


Master Board	EVK1900 board
WDTEN	WDTEN
LOADENB	LOADENB
DGPIN1	DGPIN1
DGPIN0	DGPIN0

### Temperature Sensor Not Fitted



### Power Detector Not Fitted







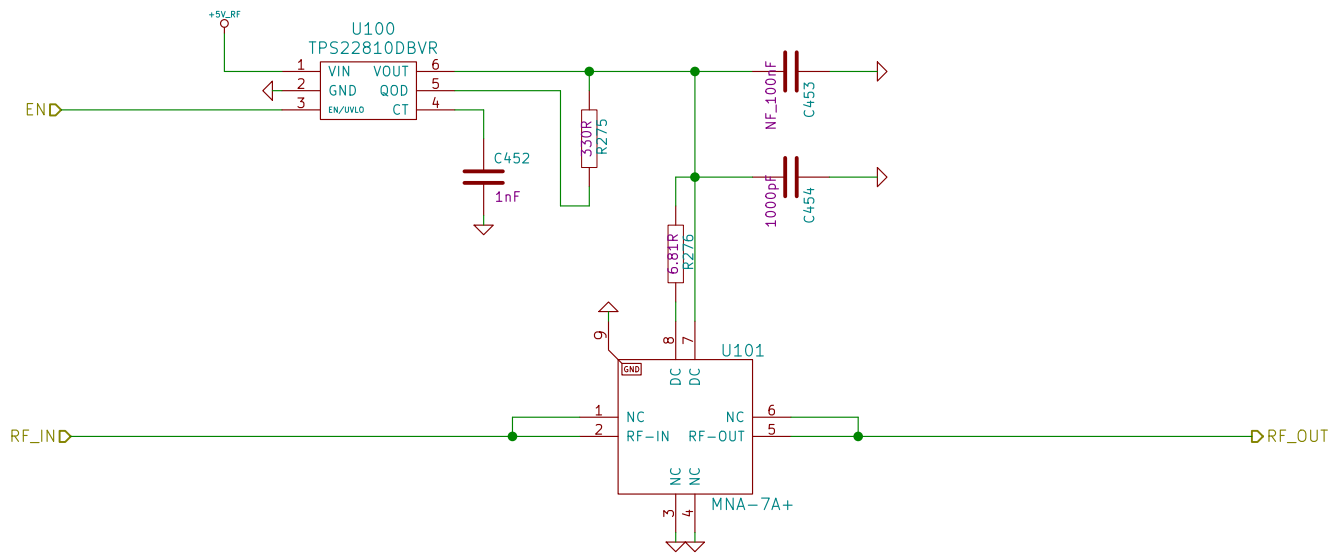






<b>Time Microsystems</b>		D
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File: LNA1.sch		
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Size: A4	Date: 10 Feb 2022	Rev: 1v0
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		Id: 13/20





**Lime Microsystems**

Sheet: /RF/DA1/

File: DA1.sch

**Title: FrontEnd\_5G**

Size: A4

Date: 10 Feb 2022

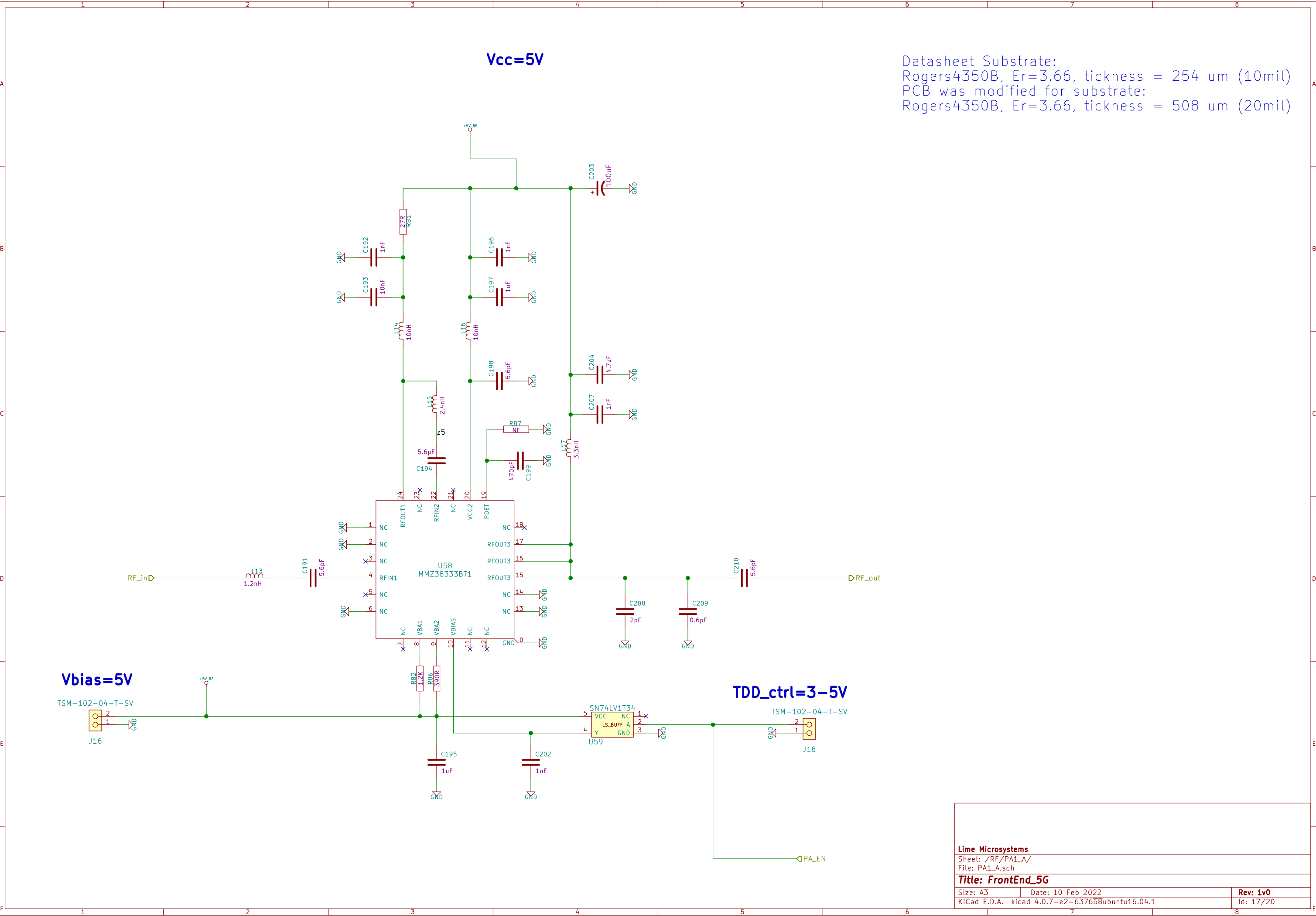
Rev: 1v0

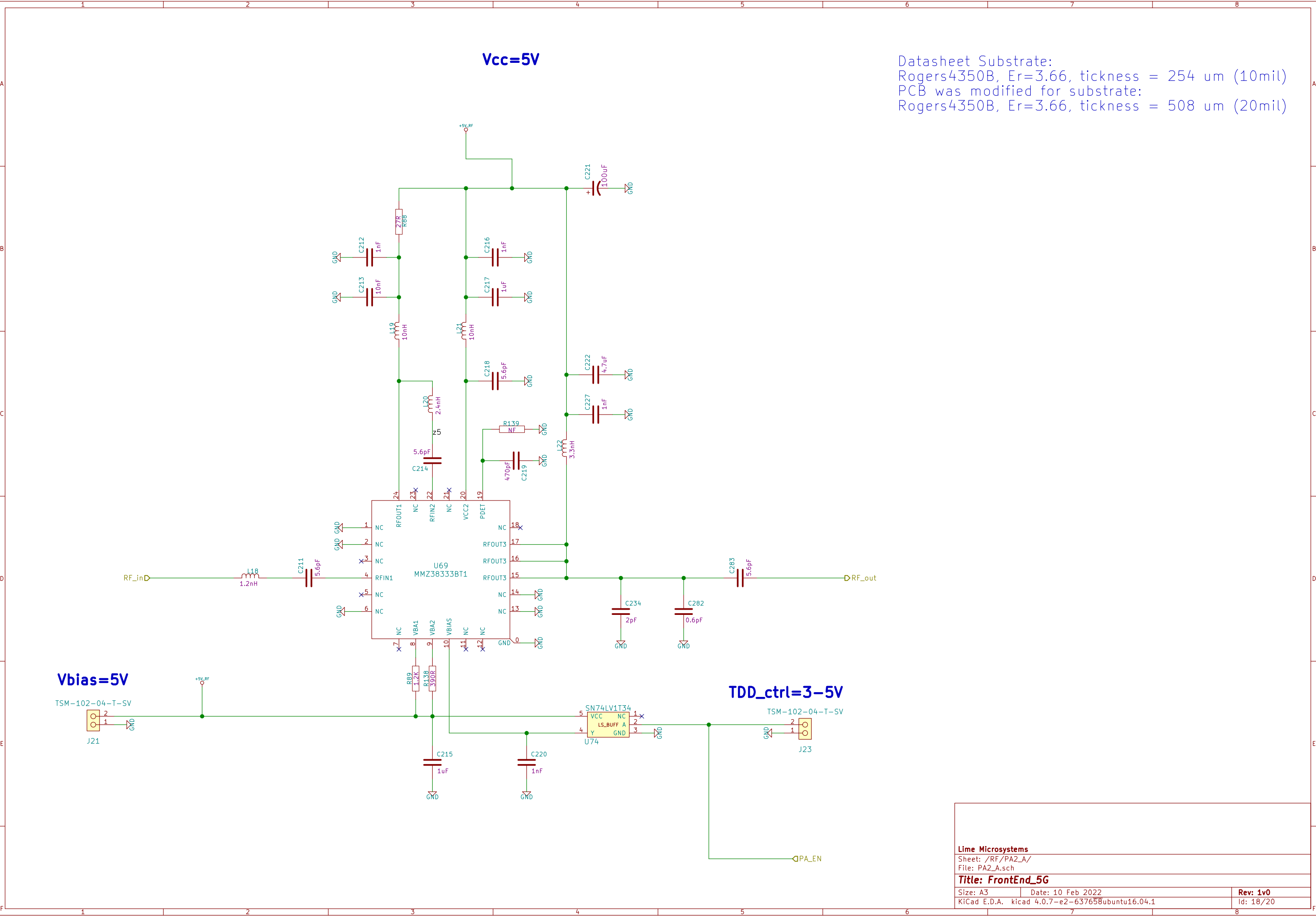
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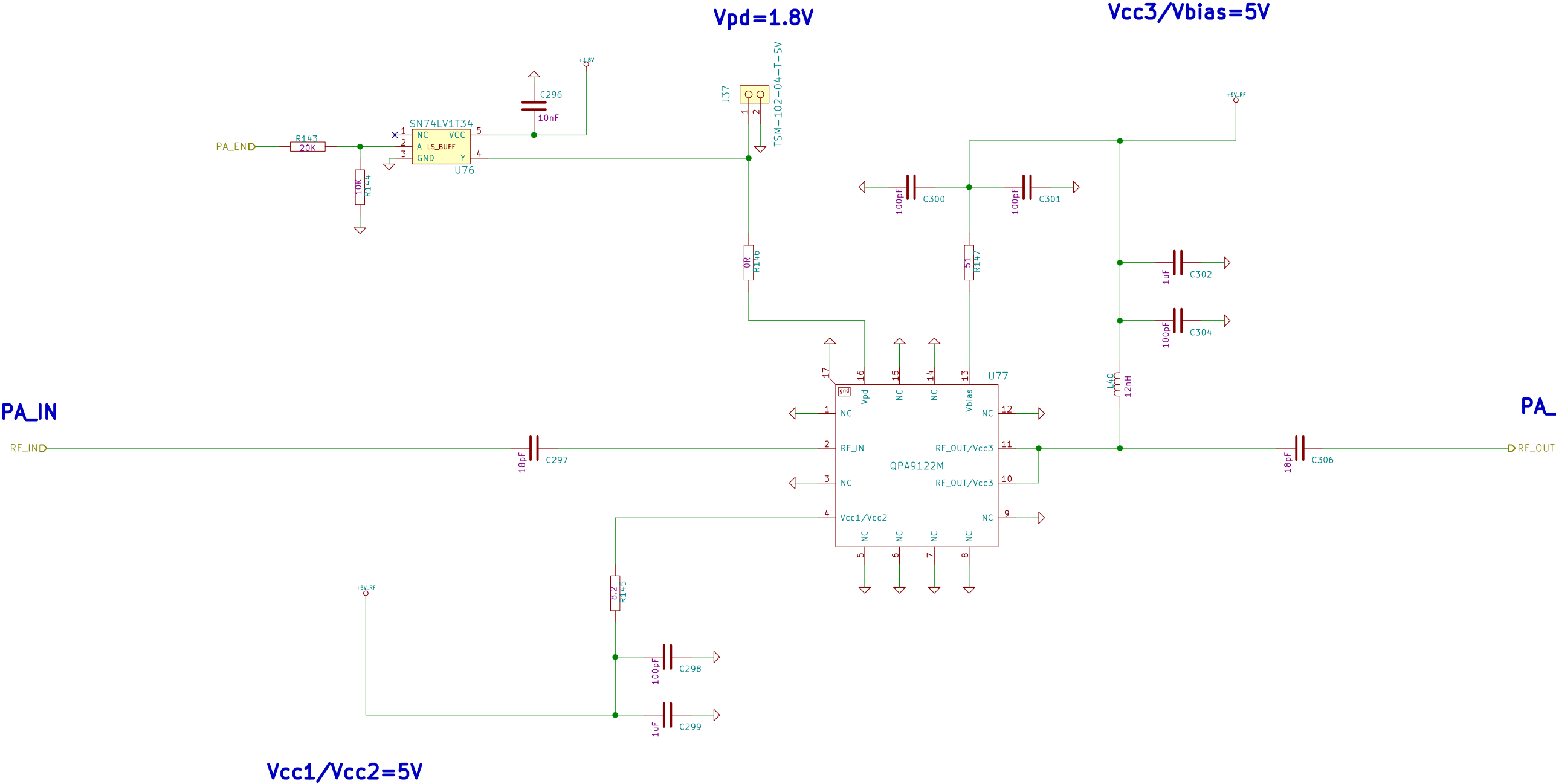
Id: 16/20







Roger 4350B, Er = 3.66, tickness = 508 um (20 mil)



Roger 4350B, Er = 3.66, tickness = 508 um (20 mil)

