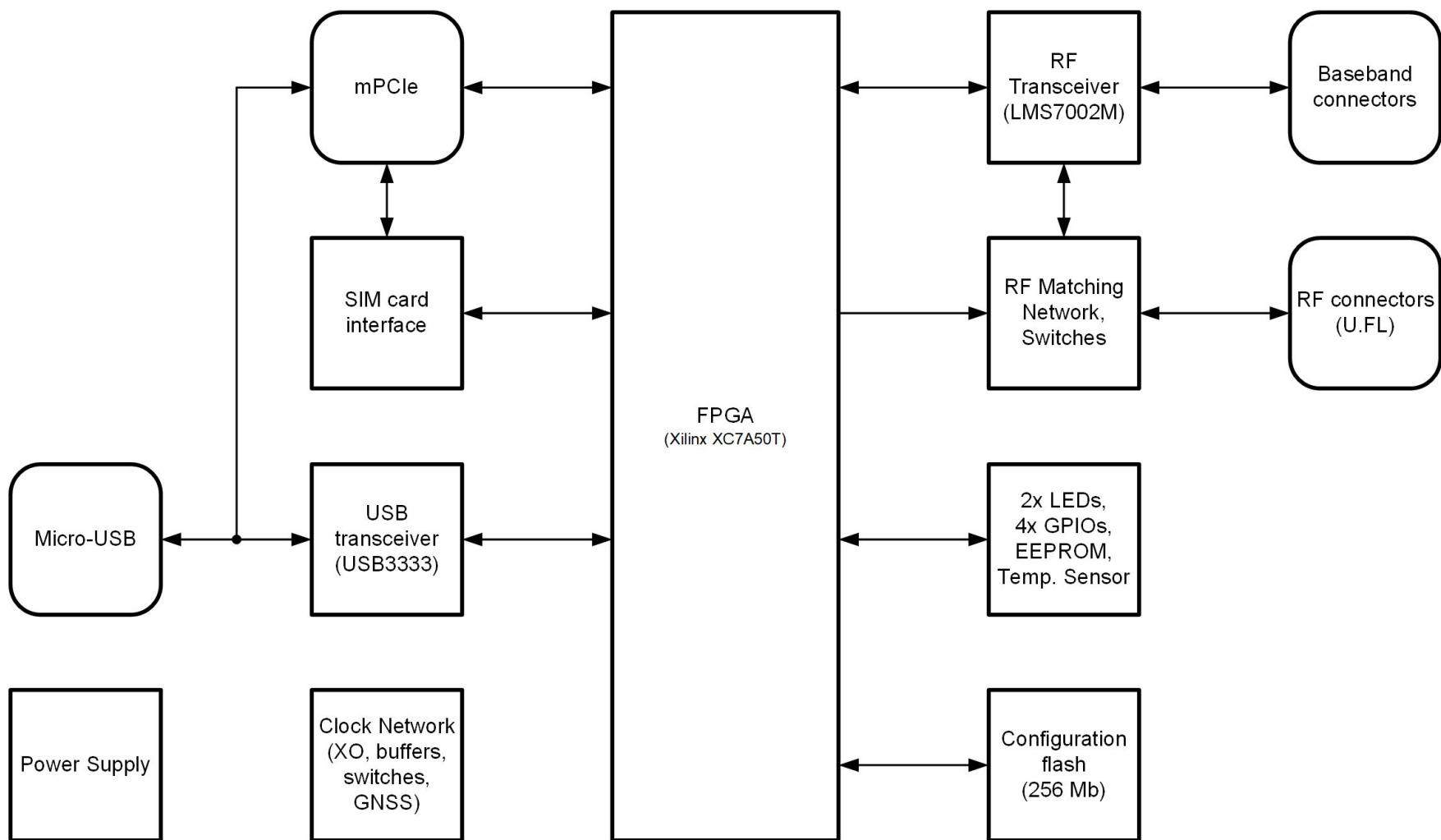


Block diagram



Project name: **LimeSDR-XTRX_1v0.PrjPcb**

Title: **Block diagram**

Size: **A4** Revision: **v1.0**

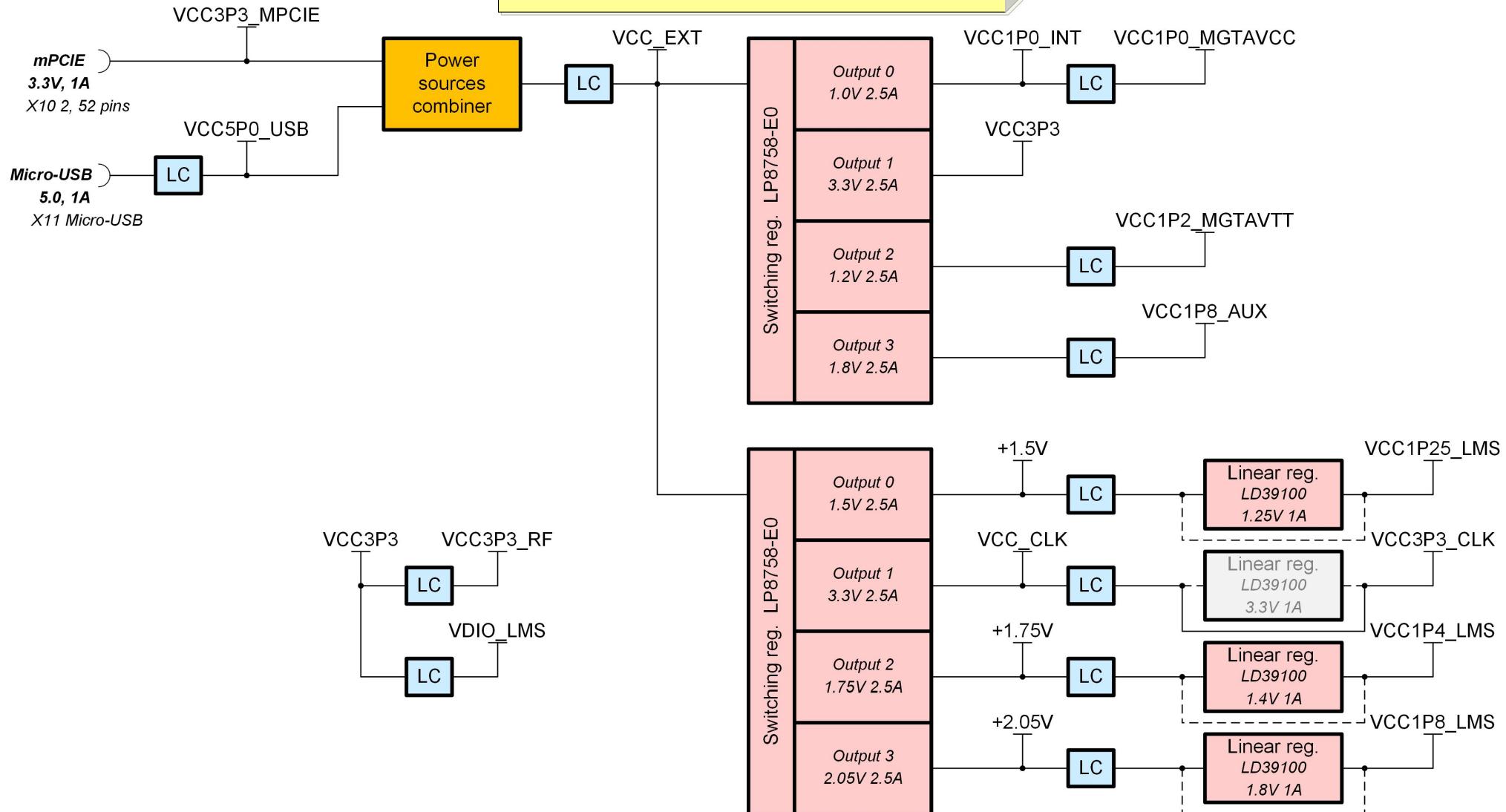
Date: **2023-01-03** Time: **12:09:13** Sheet **1** of **10**

File: **01_Block_Diagram.SchDoc**

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United Kingdom



Power diagram



Project name: **LimeSDR-XTRX_1v0.PnjPcb**

Title: **Power diagram**

Size: **A4** Revision: **v1.0**

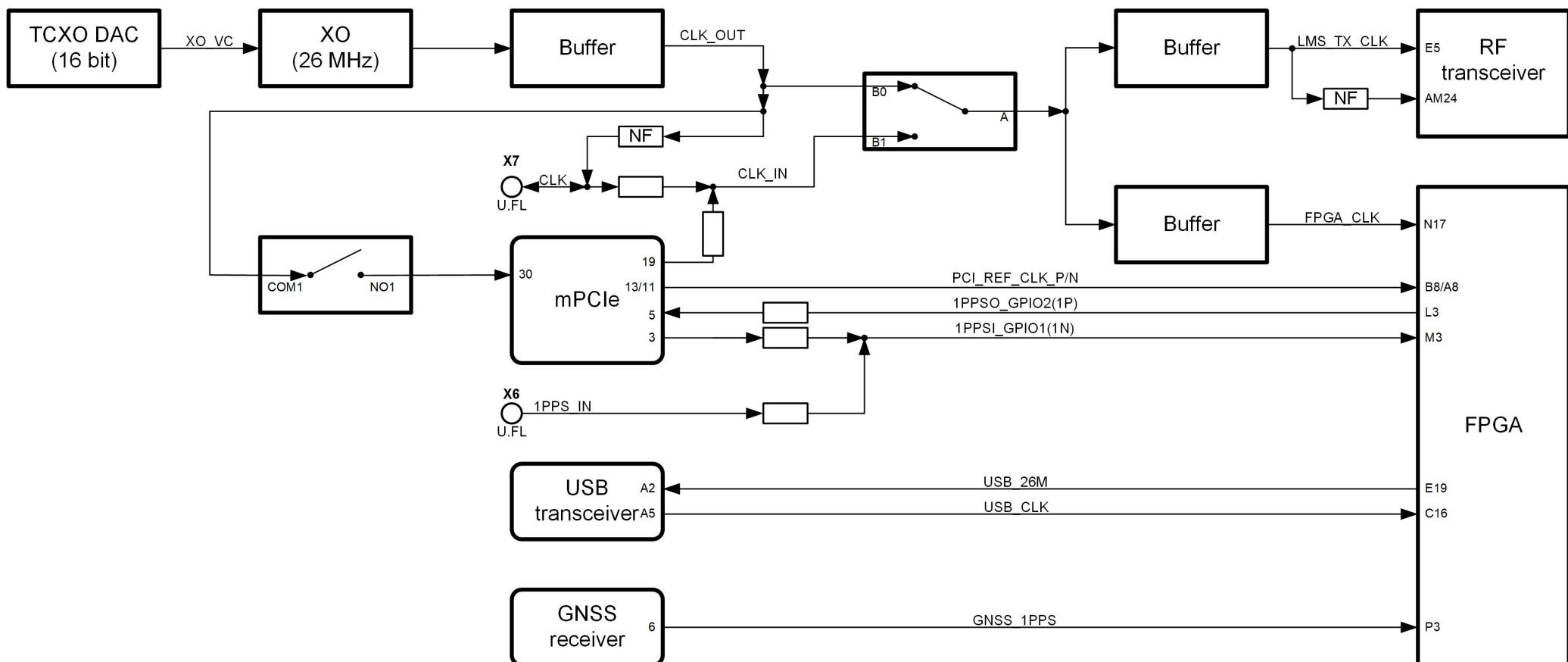
Date: **2023-01-03** Time: **12:09:15** Sheet **2** of **10**

File: **02_Power_Diagram.SchDoc**

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Clock diagram



Project name: **LimeSDR-XTRX_Inv0.PnjPcb**

Title: **Clock diagram**

Size: **A4** Revision: **v1.0**

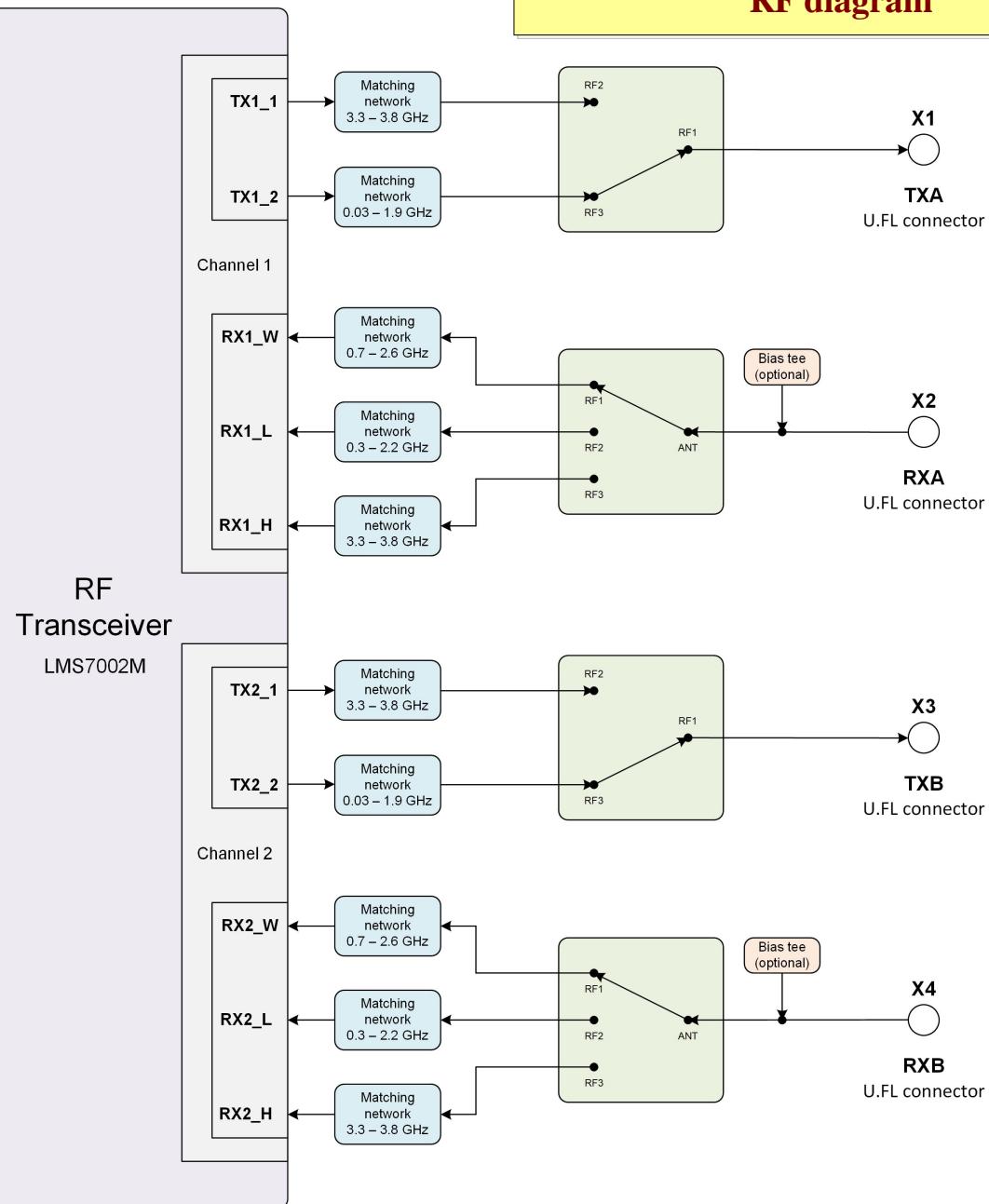
Date: **2023-01-03** Time: **12:09:18** Sheet **3** of **10**

File: **03_Clock_Diagram.SchDoc**

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RF diagram



Project name: **LimeSDR-XTRX_Iv0.PnjPcb**

Title: **RF diagram**

Size: **A4** Revision: **v1.0**

Date: **2023-01-03** Time: **12:09:21** Sheet **4** of **10**

File: **04_RF_Diagram.SchDoc**

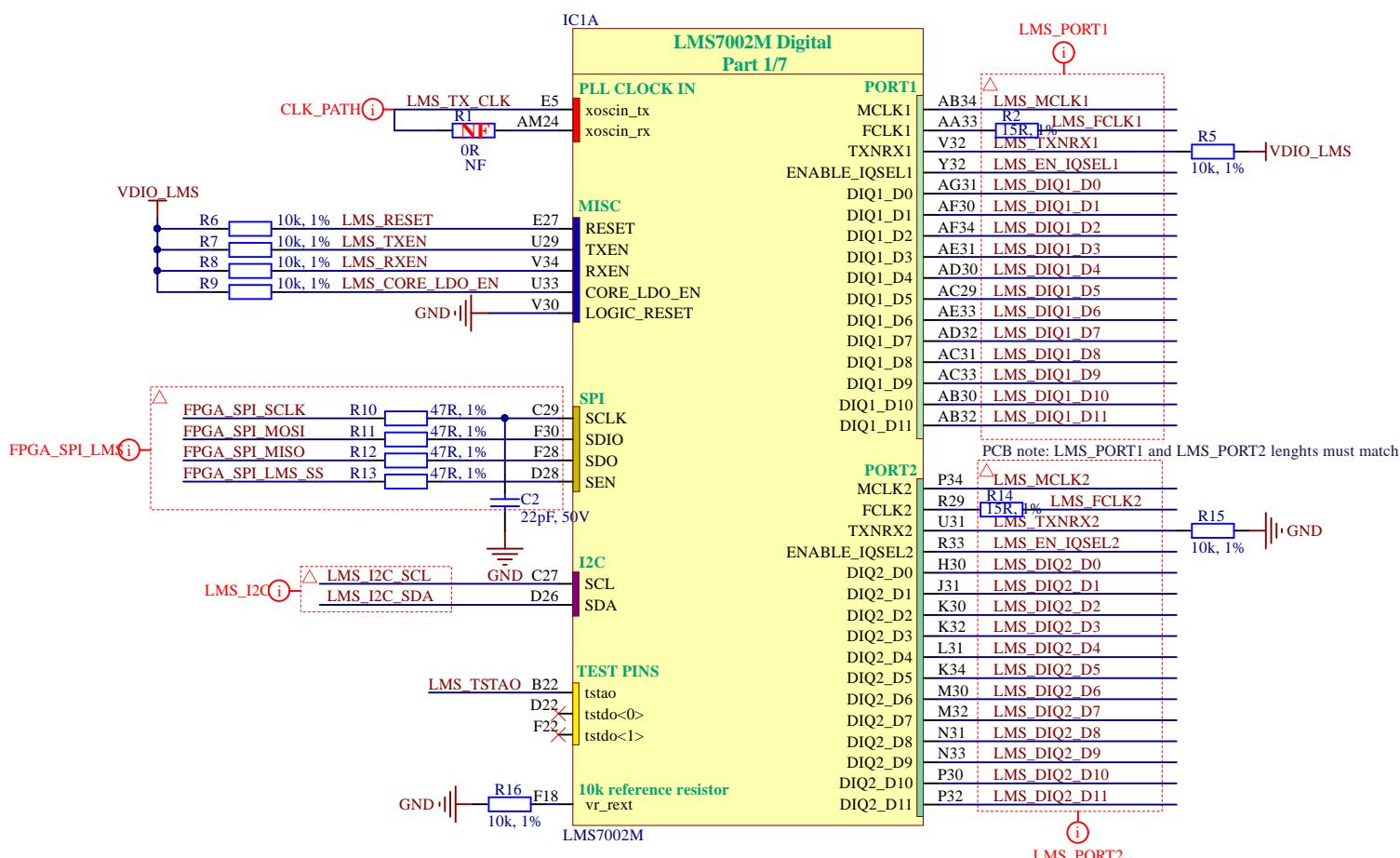
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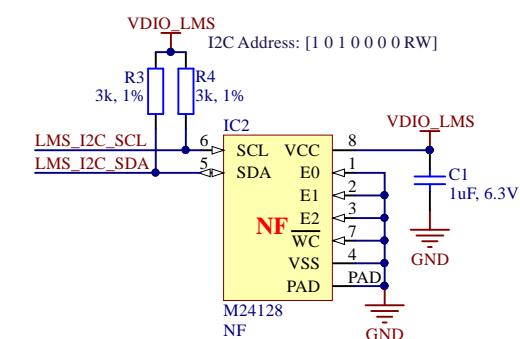
NF elements on sheet: R1, IC2
Number of NF elements on sheet: 2

LMS7002M misc

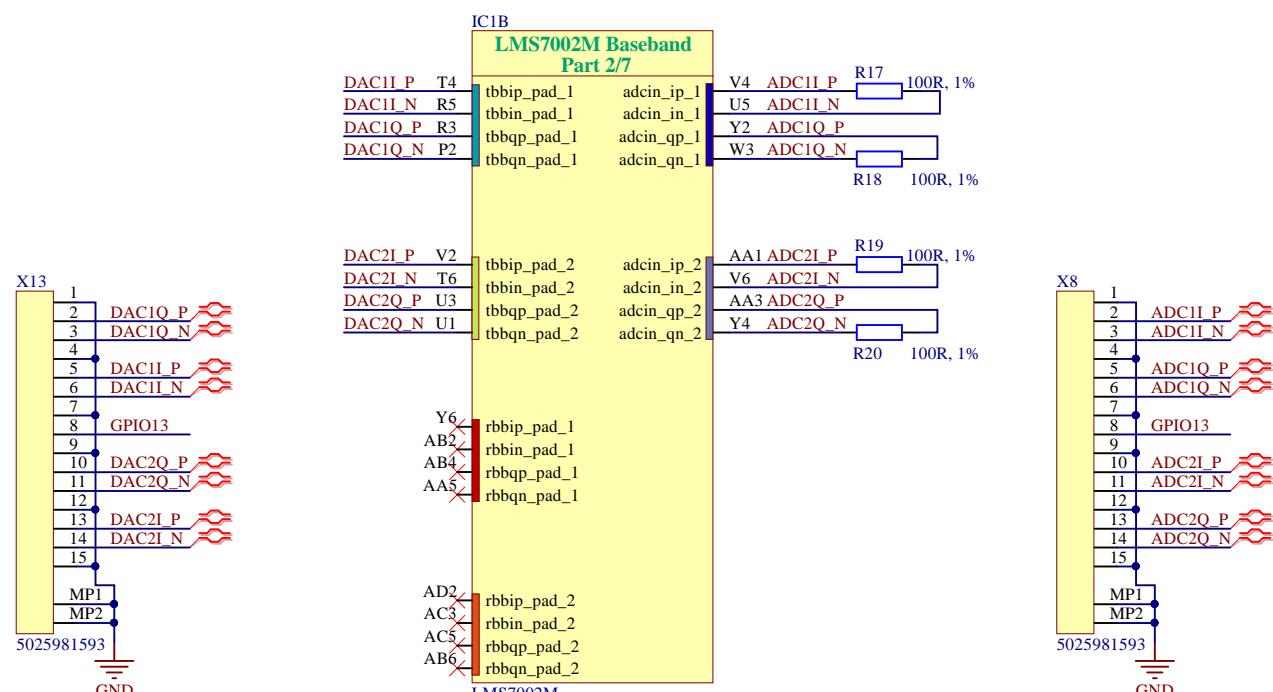
LMS7002M digital circuit



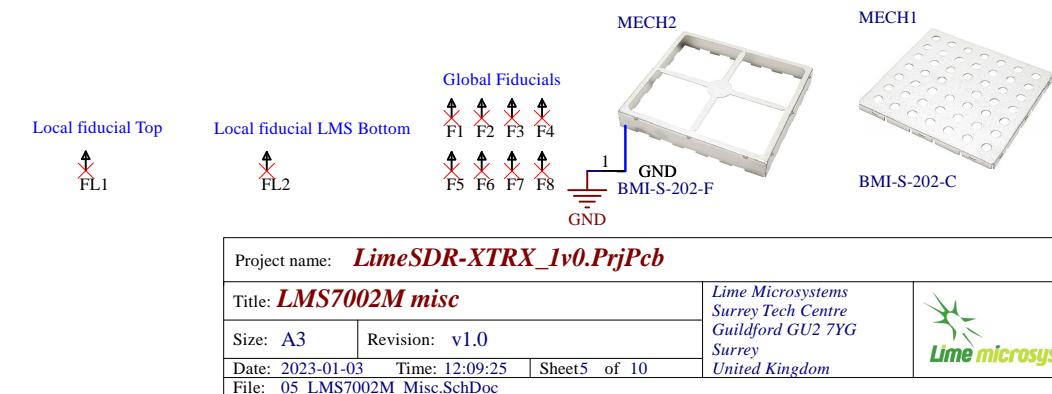
LMS EEPROM



Baseband external IO

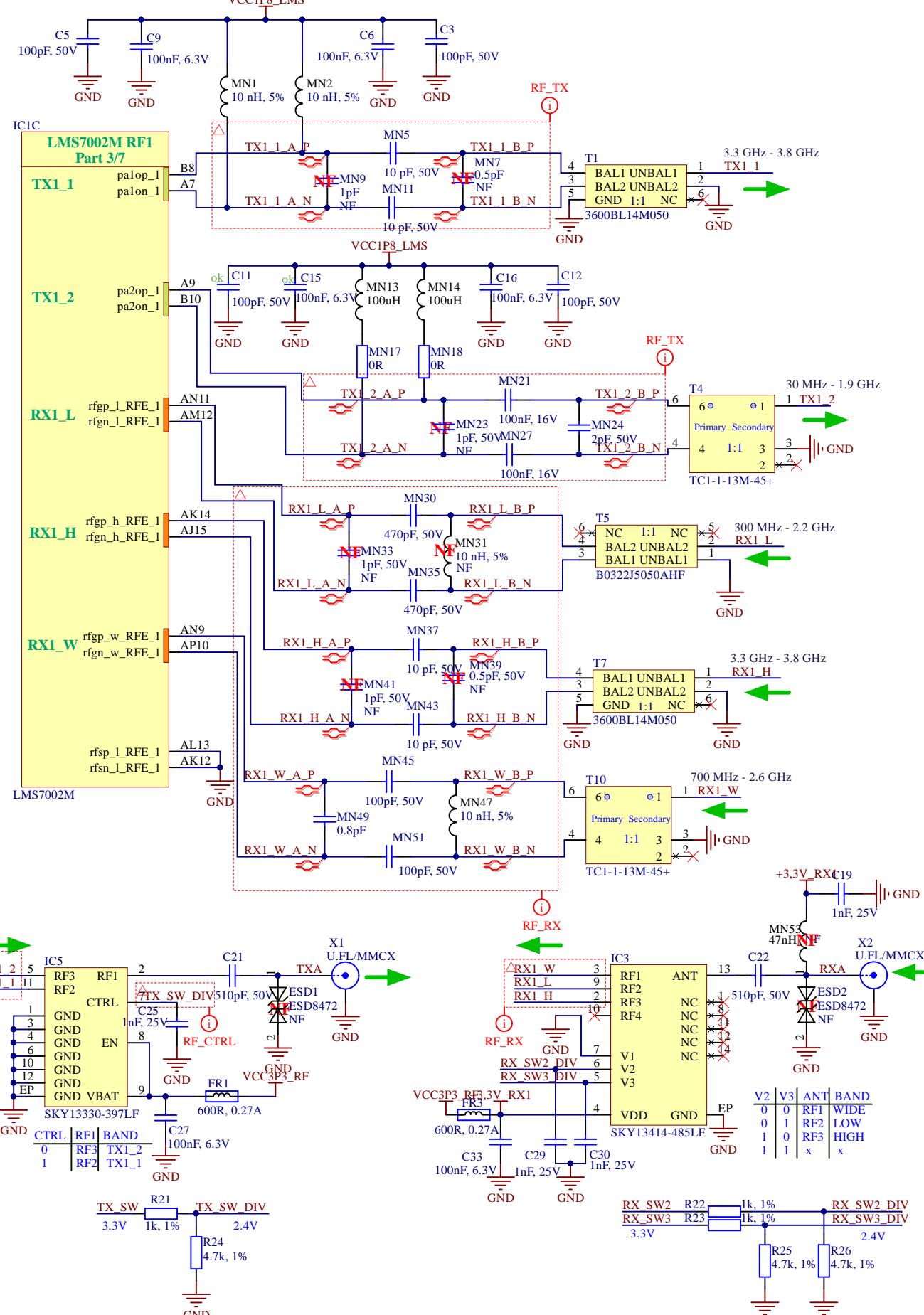


Mechanical components

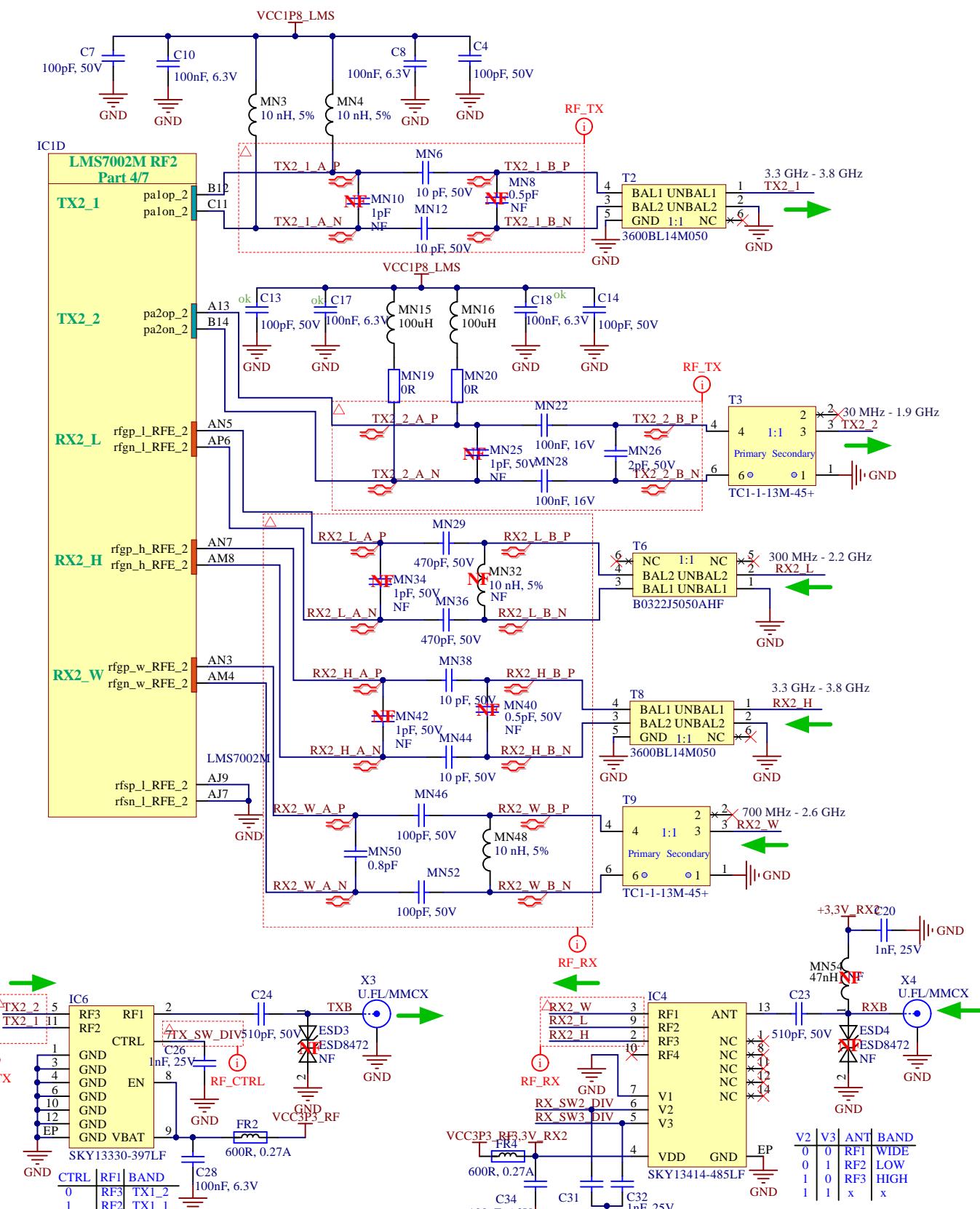


LMS7002M RF circuits

LMS RF Channel 1



LMS RF Channel 2



Project name: LimeSDR-XTRX_lv0_PriPch

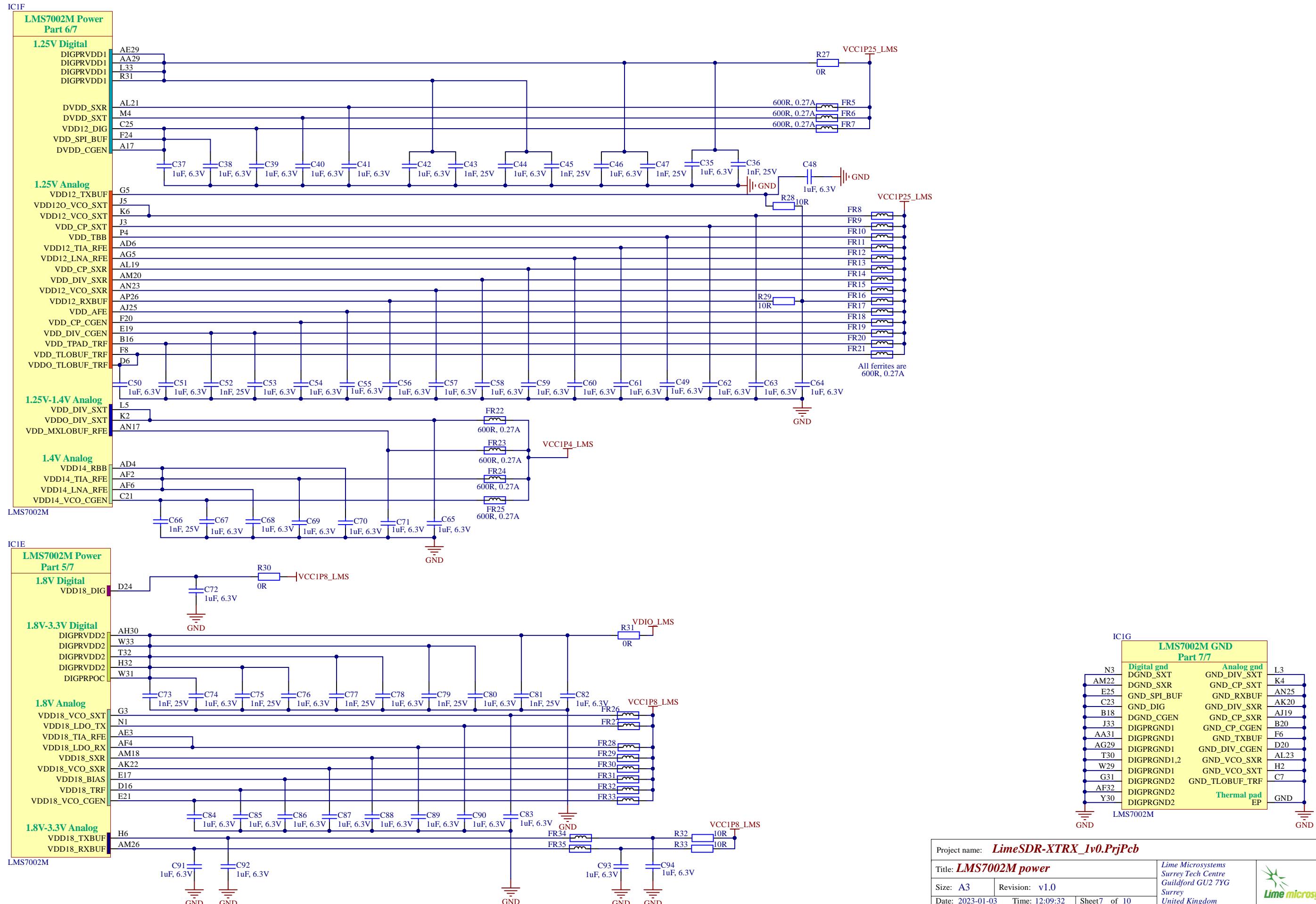
Project name: **LMS7002M BE**

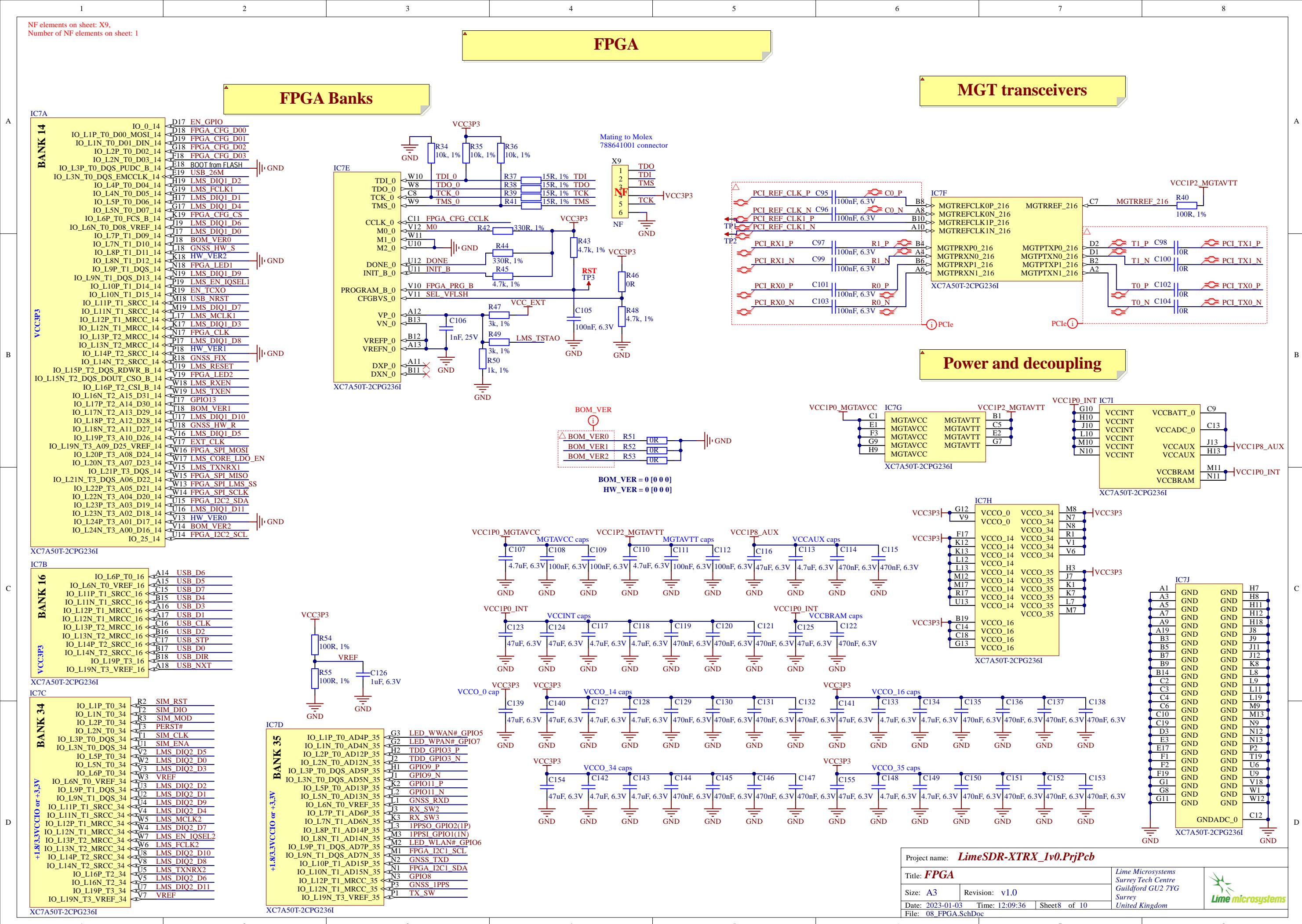
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NF elements on sheet: -
Number of NF elements on sheet: 0

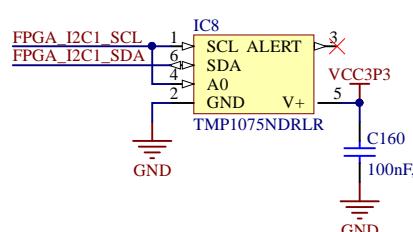
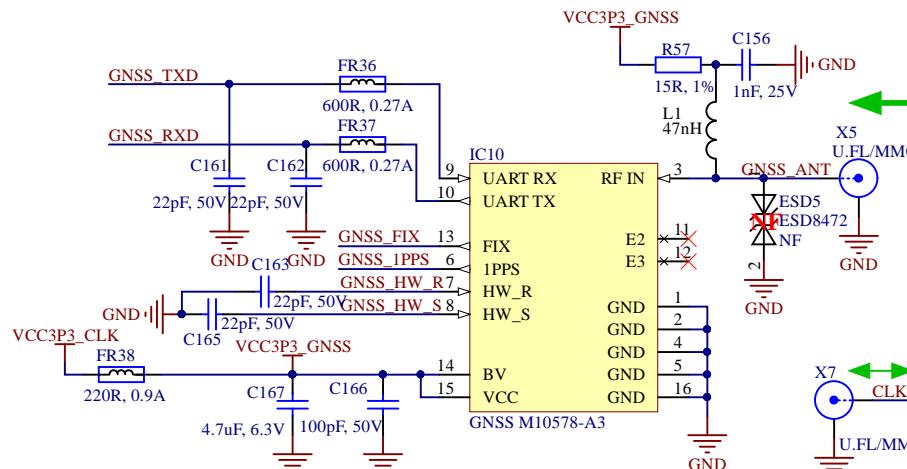
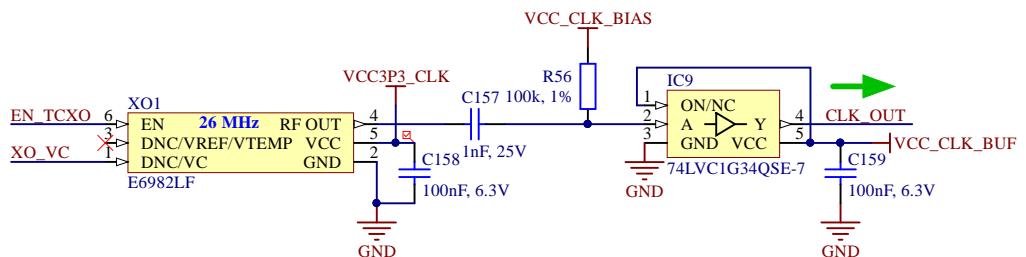
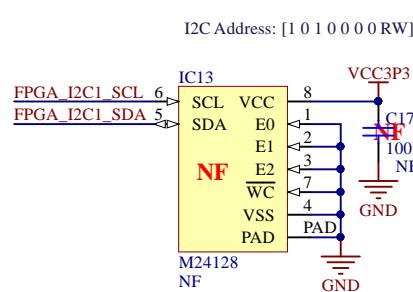
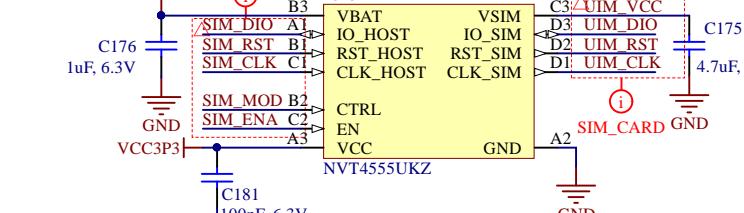
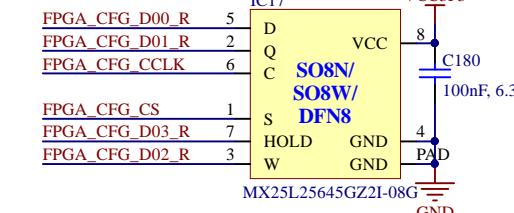
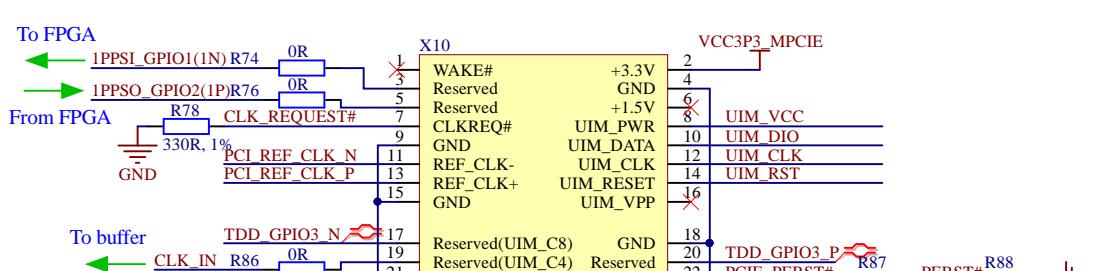
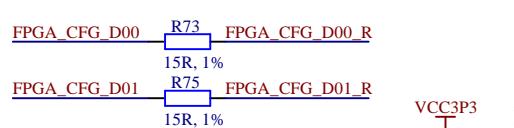
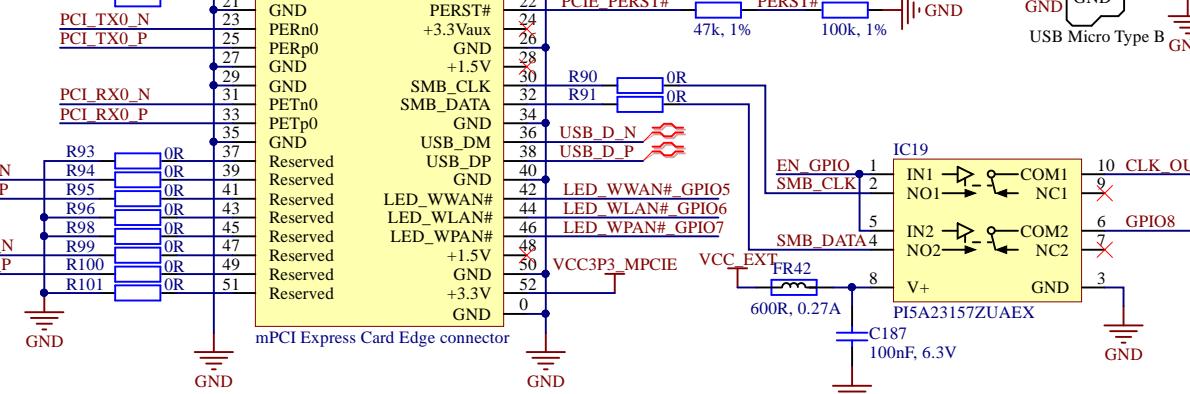
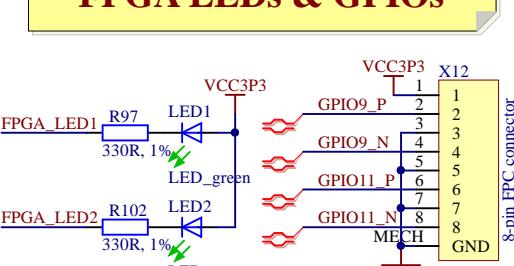
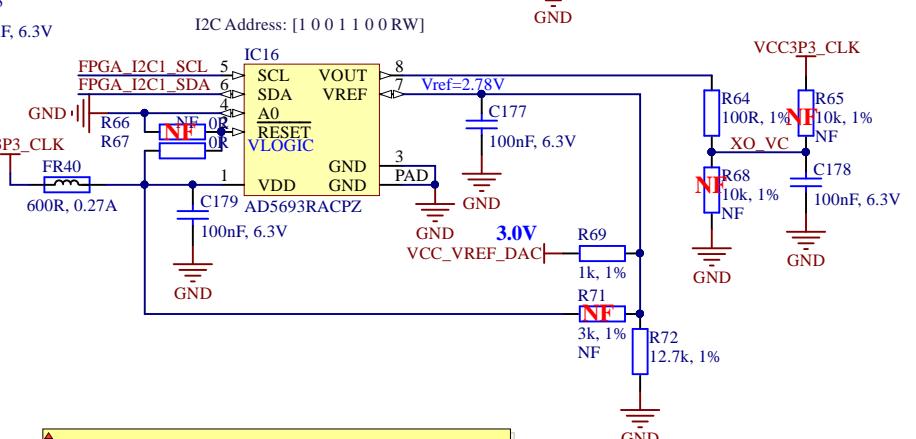
LMS7002M power supply circuit





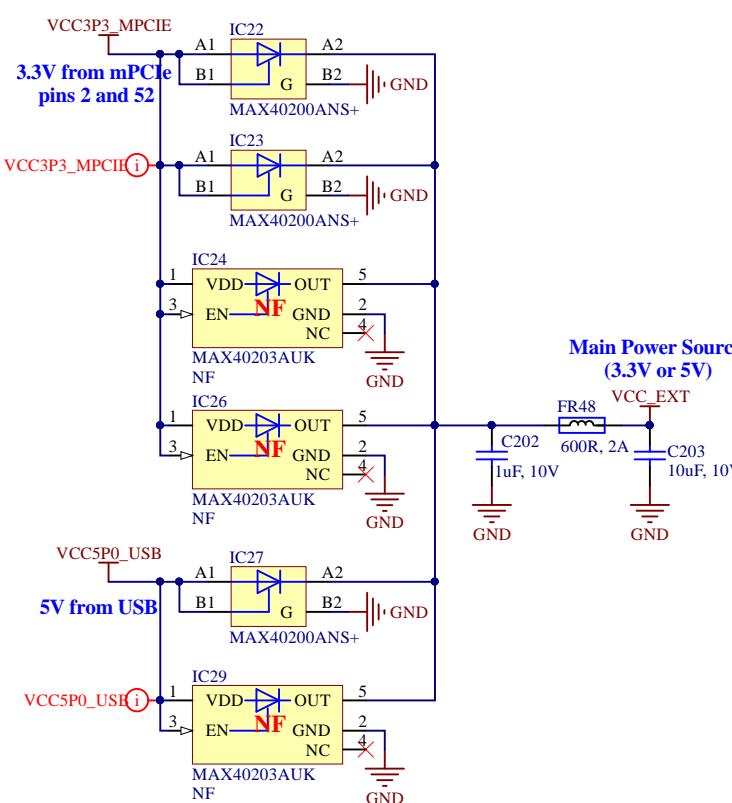
Misc**I2C Temperature sensor**

I2C Address: [1 0 0 1 0 1 1 RW]

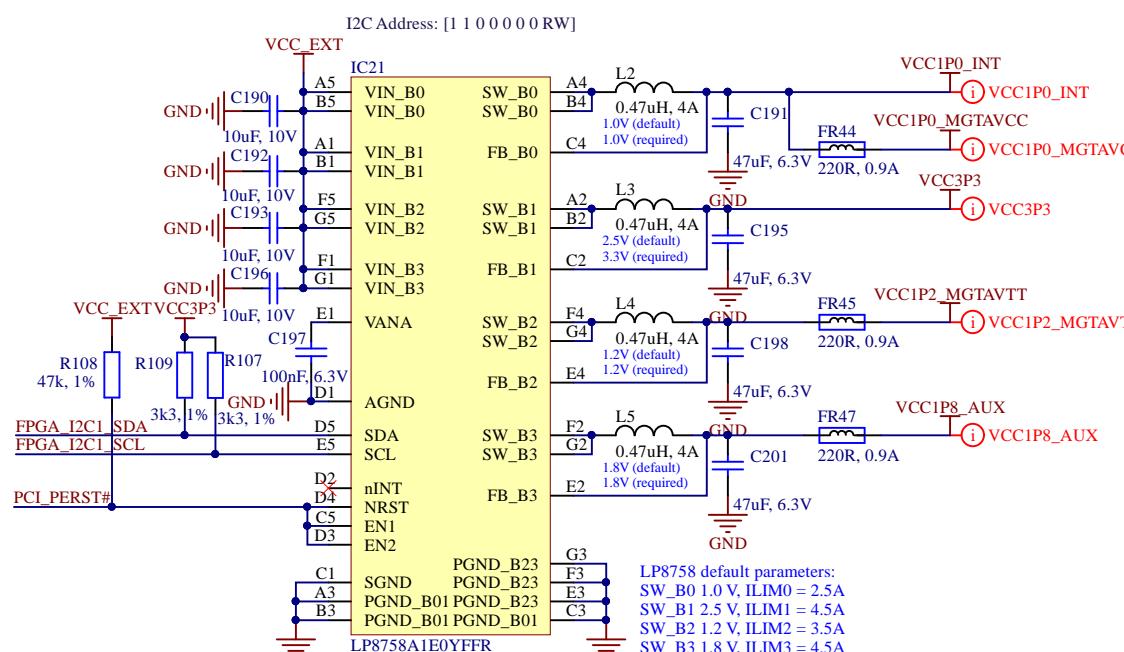
**GNSS Receiver****Clock****I2C EEPROM****SIM card interface****FPGA configuration Flash****mPCIe****FPGA LEDs & GPIOs****USB transceiver**Project name: **LimeSDR-XTRX_1v0.PrfPcb**Title: **Misc**Size: **A3** Revision: **v1.0**Date: **2023-01-03** Time: **12:09:40** Sheet**10** of **10**
File: **09_Misc.SchDoc**

Board power circuits

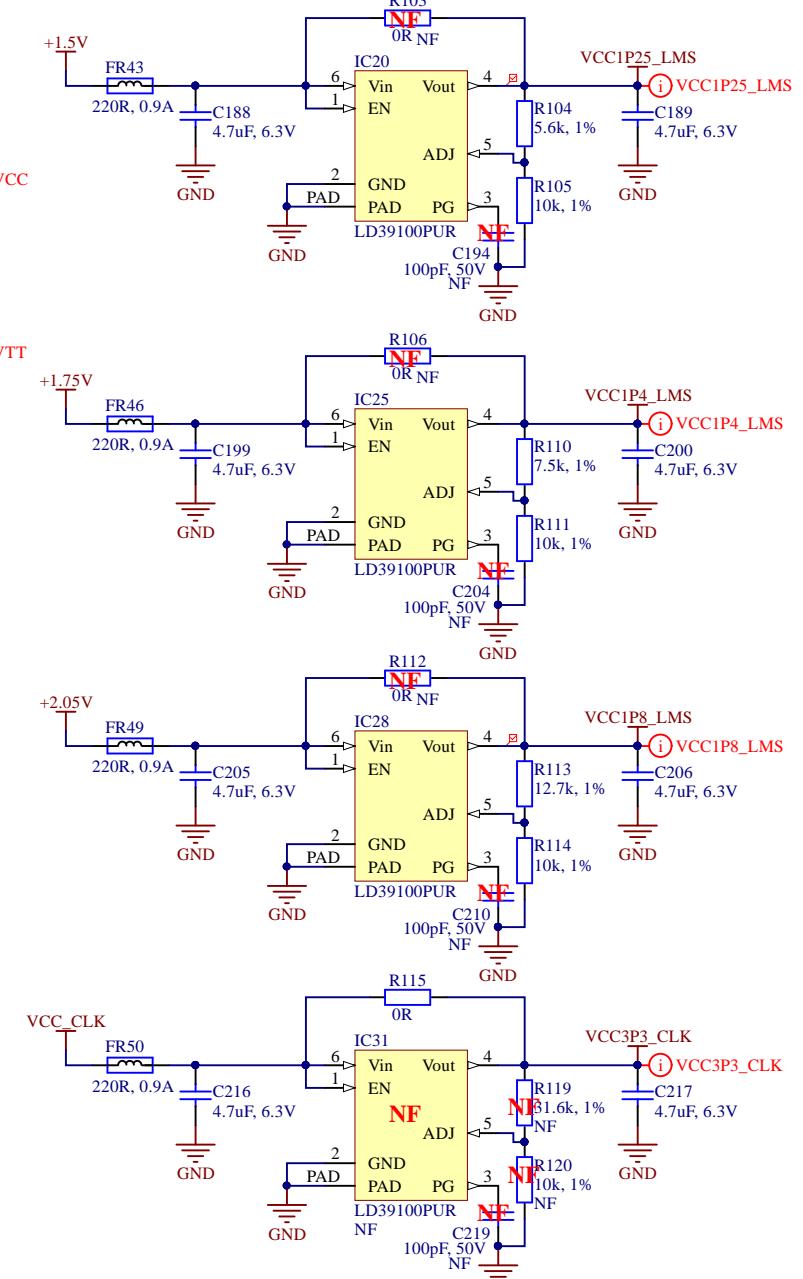
Power input



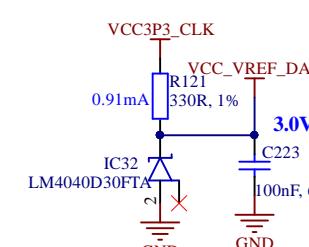
Switching regulators



Linear regulators



Voltage reference



Project name: LimeSDR-XTRX_1v0.PnjPcb

Title: Power

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Size: A3 Revision: v1.0

Date: 2023-01-03 Time: 12:09:44 Sheet 10 of 10

File: 10_Power.SchDoc

1 2 3 4 5 6 7 8