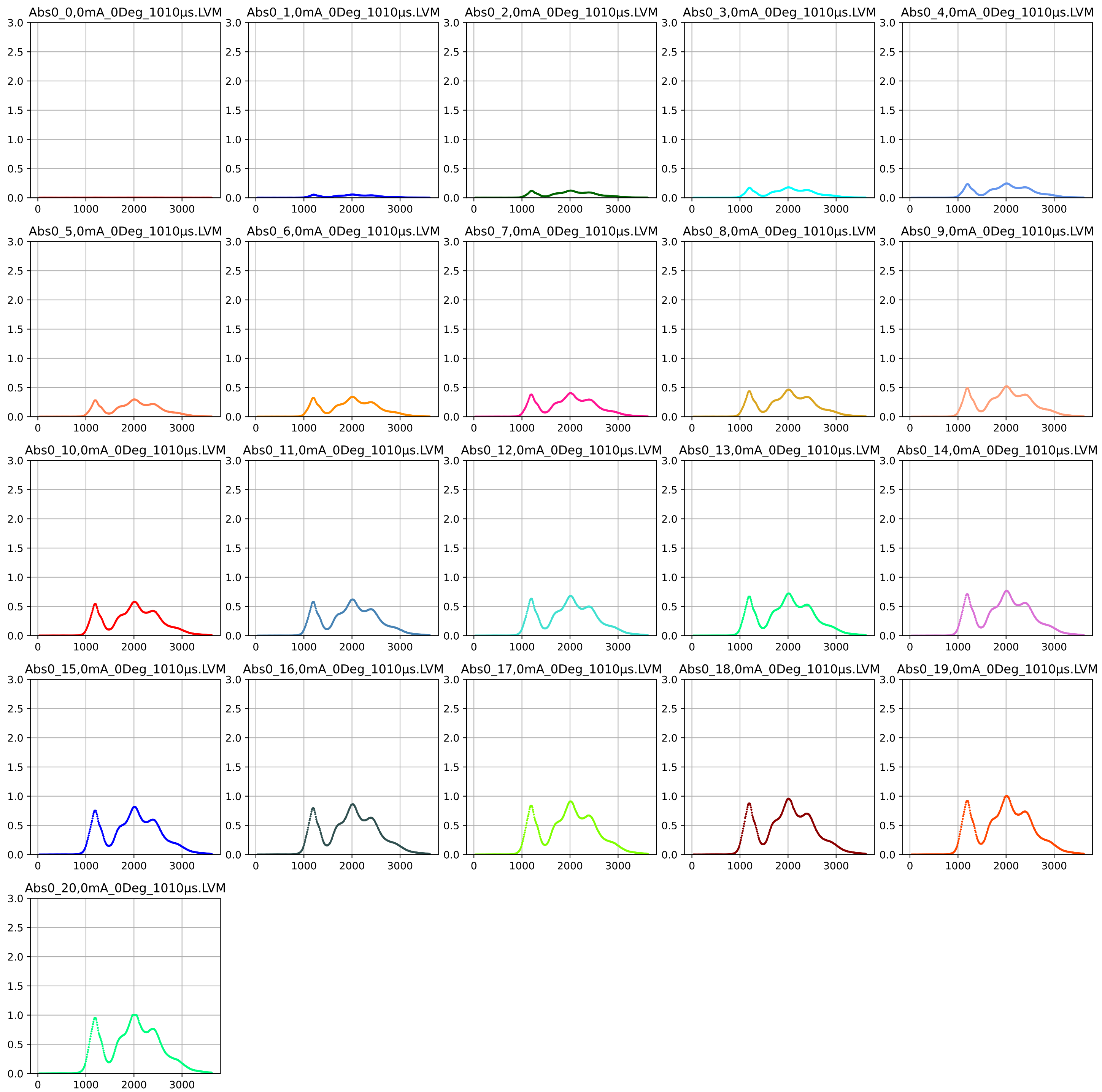
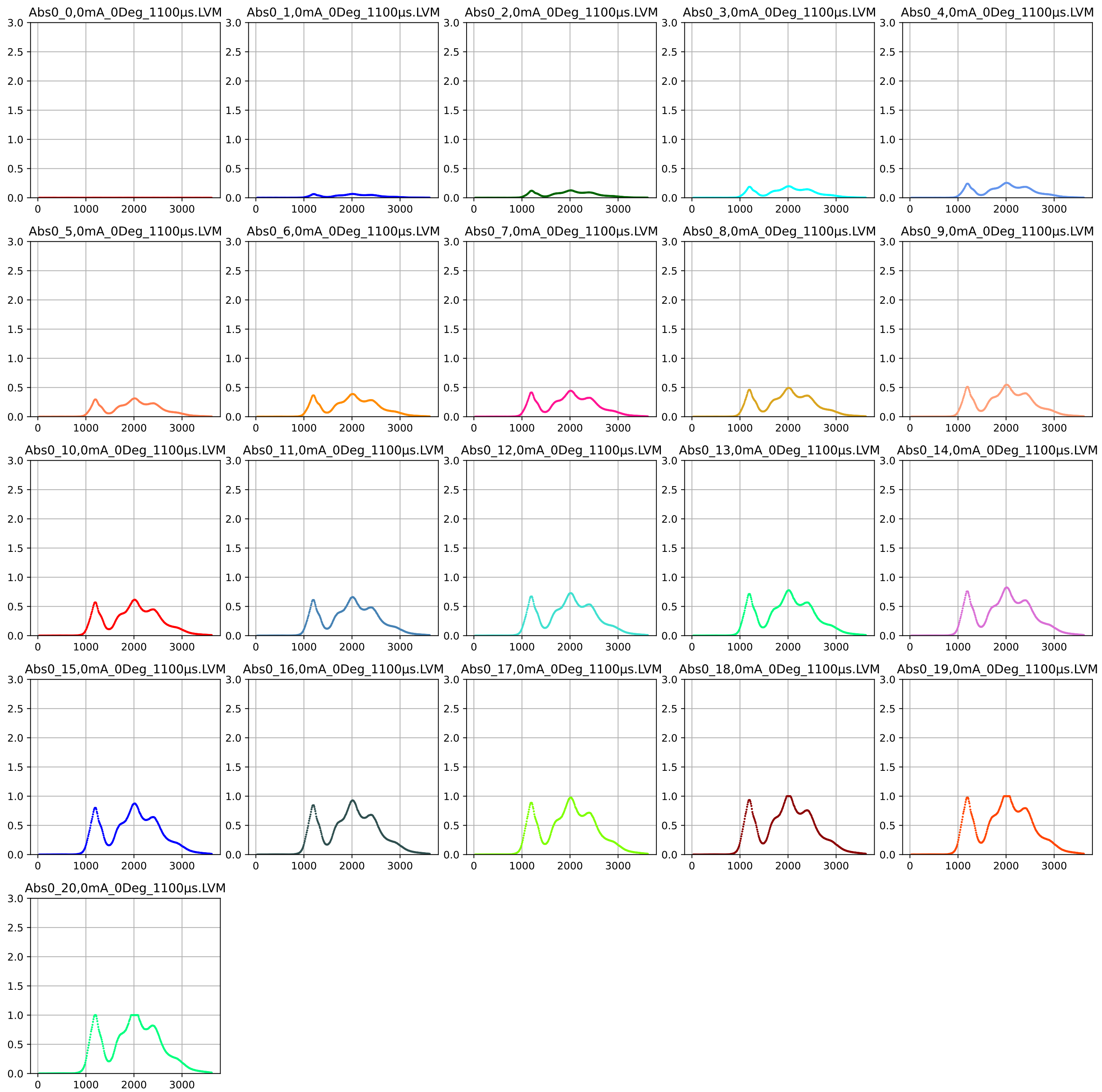


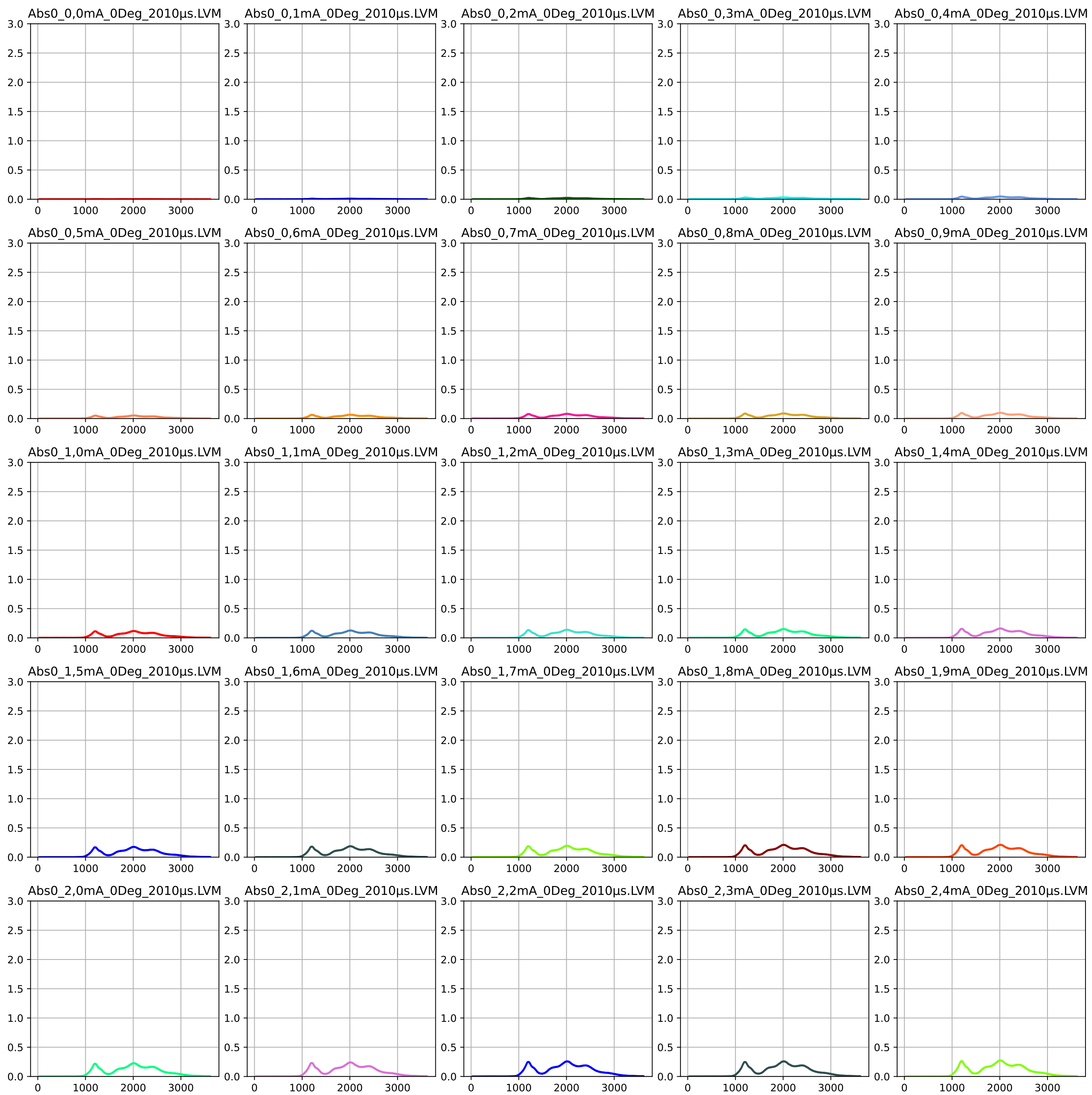
Abs0\_xmA\_0Deg\_1010μs



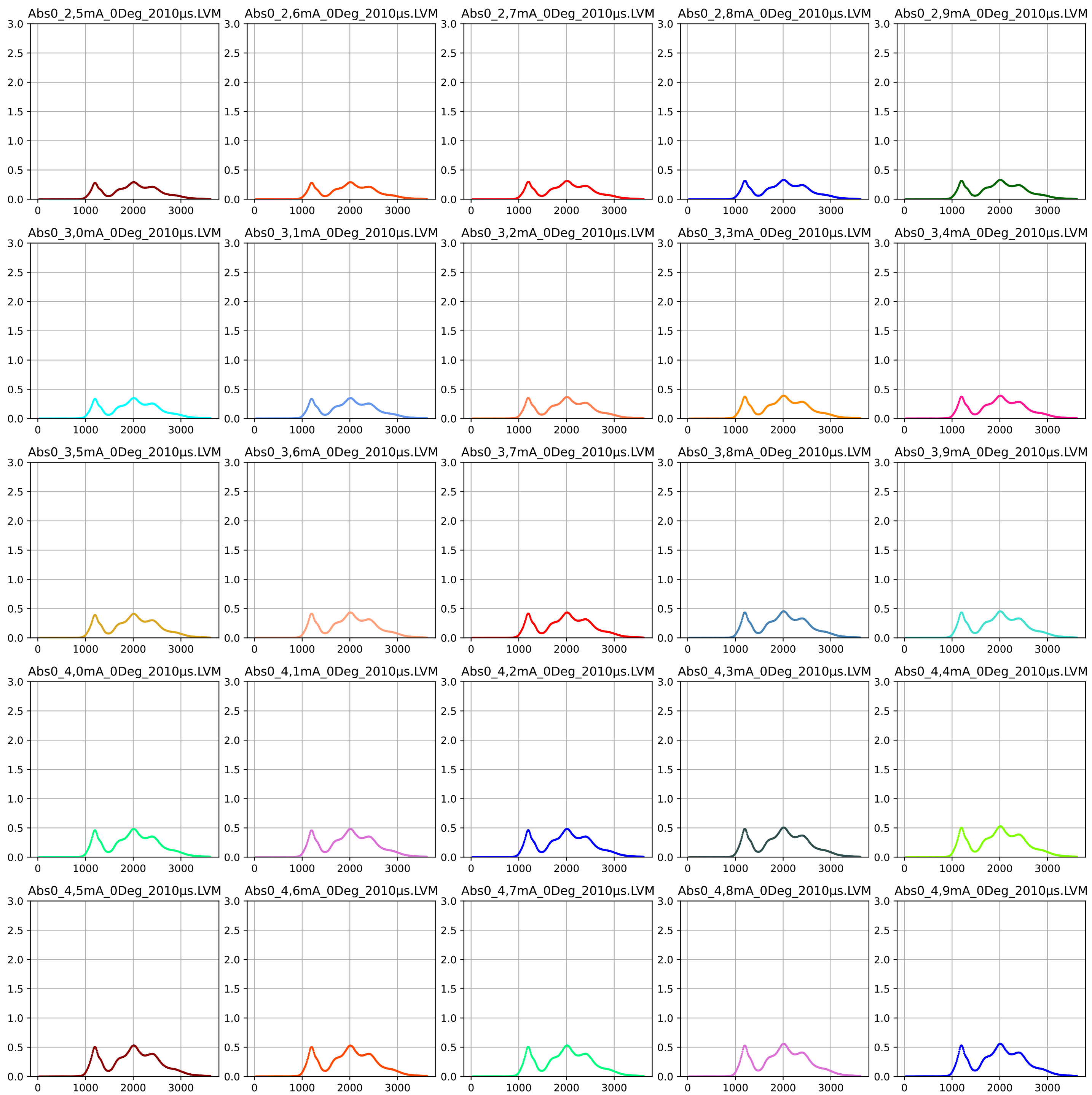
Abs0\_xmA\_0Deg\_1100μs



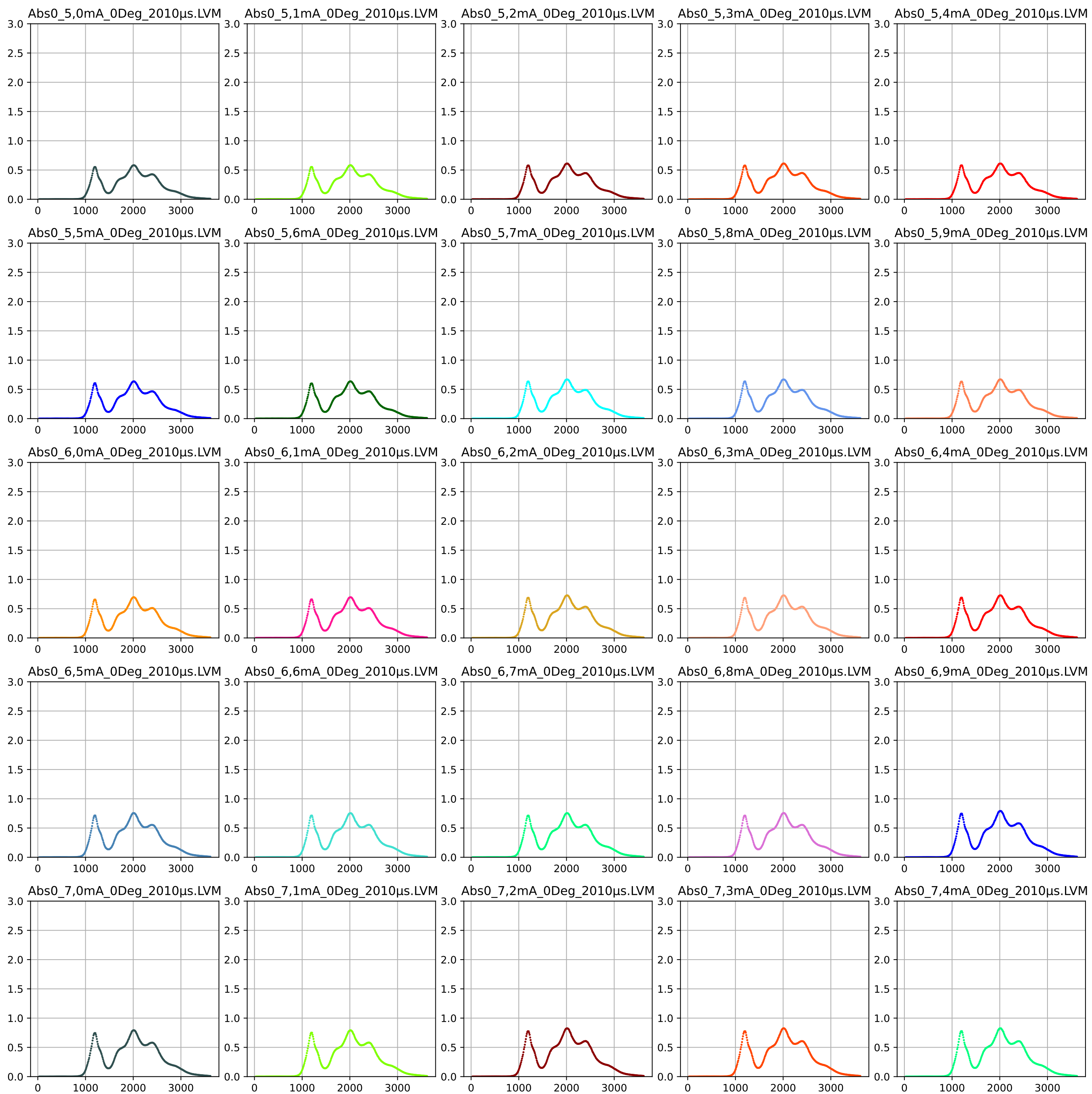
# Abs0\_xmA\_0Deg\_2010μs



# Abs0\_xmA\_0Deg\_2010μs

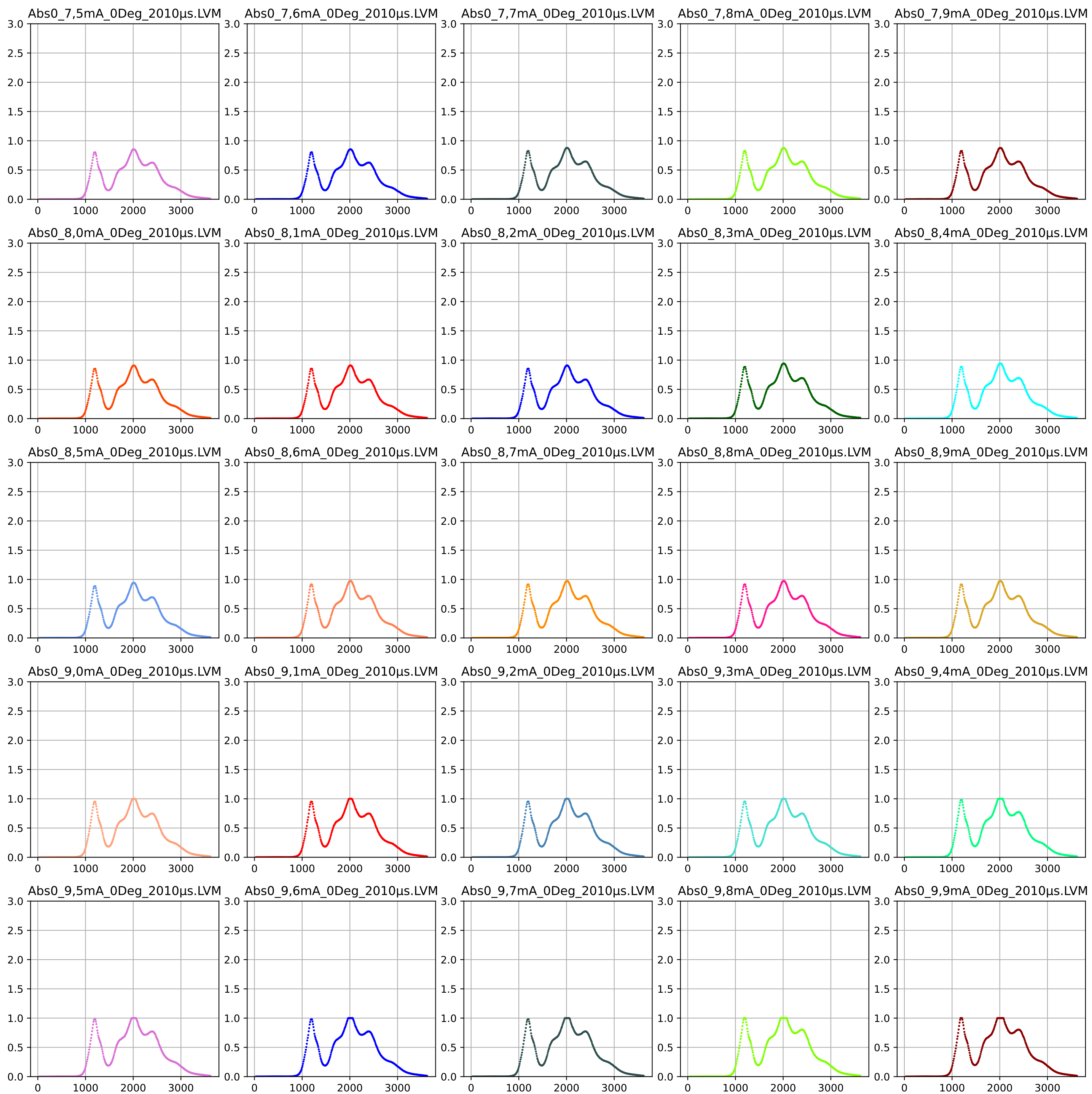


# Abs0\_xmA\_0Deg\_2010μs

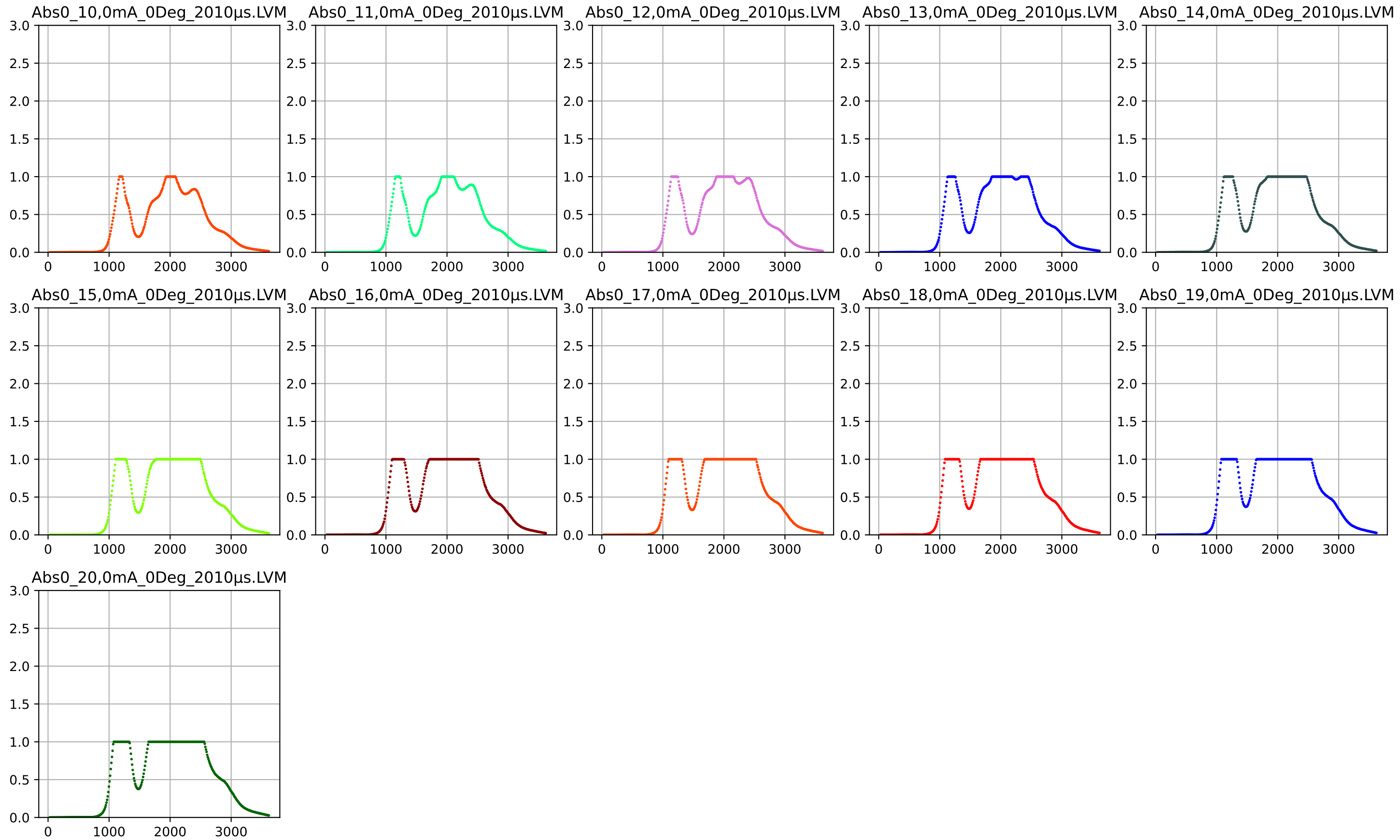




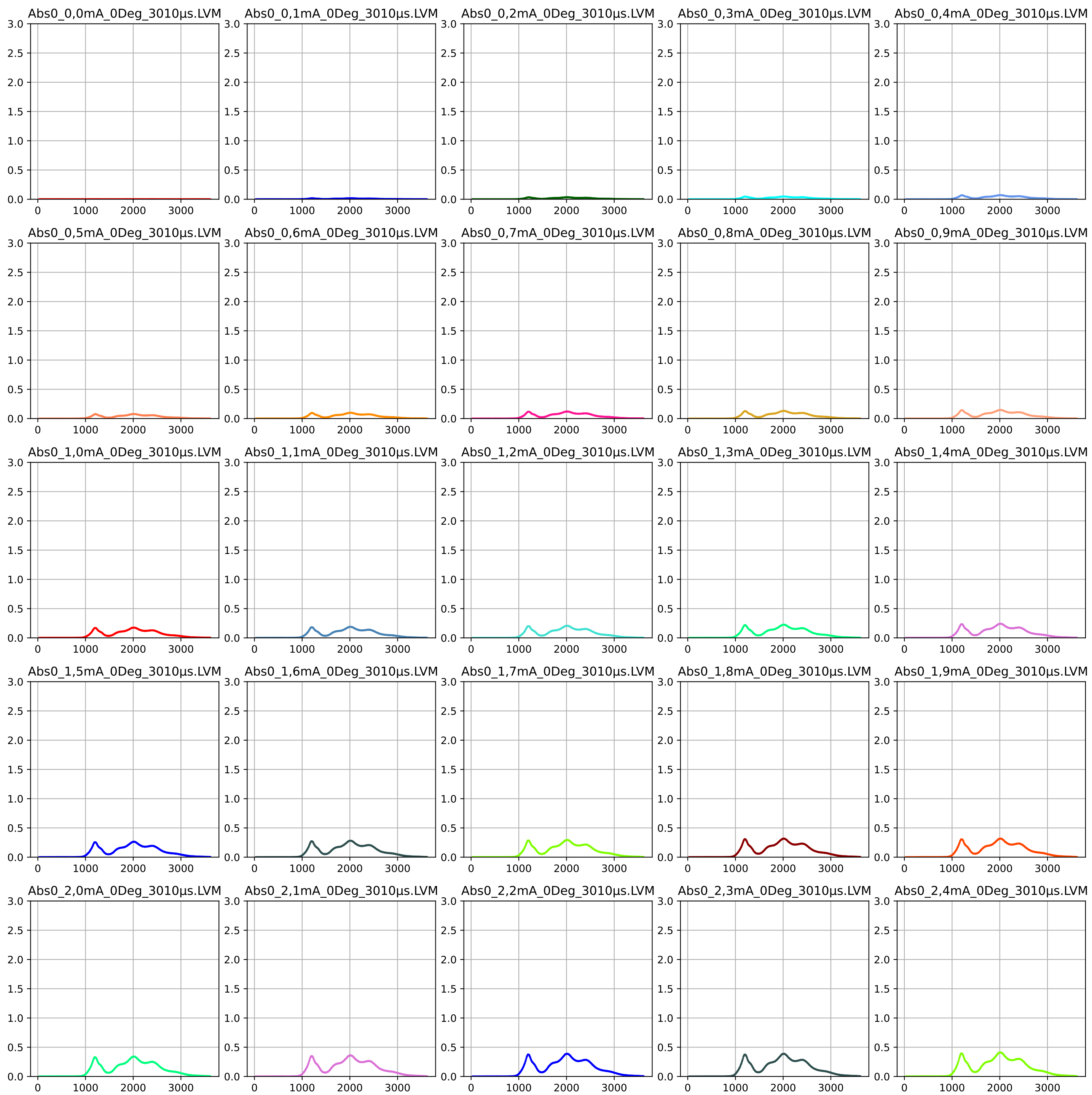
# Abs0\_xmA\_0Deg\_2010μs



# Abs0\_xmA\_0Deg\_2010μs

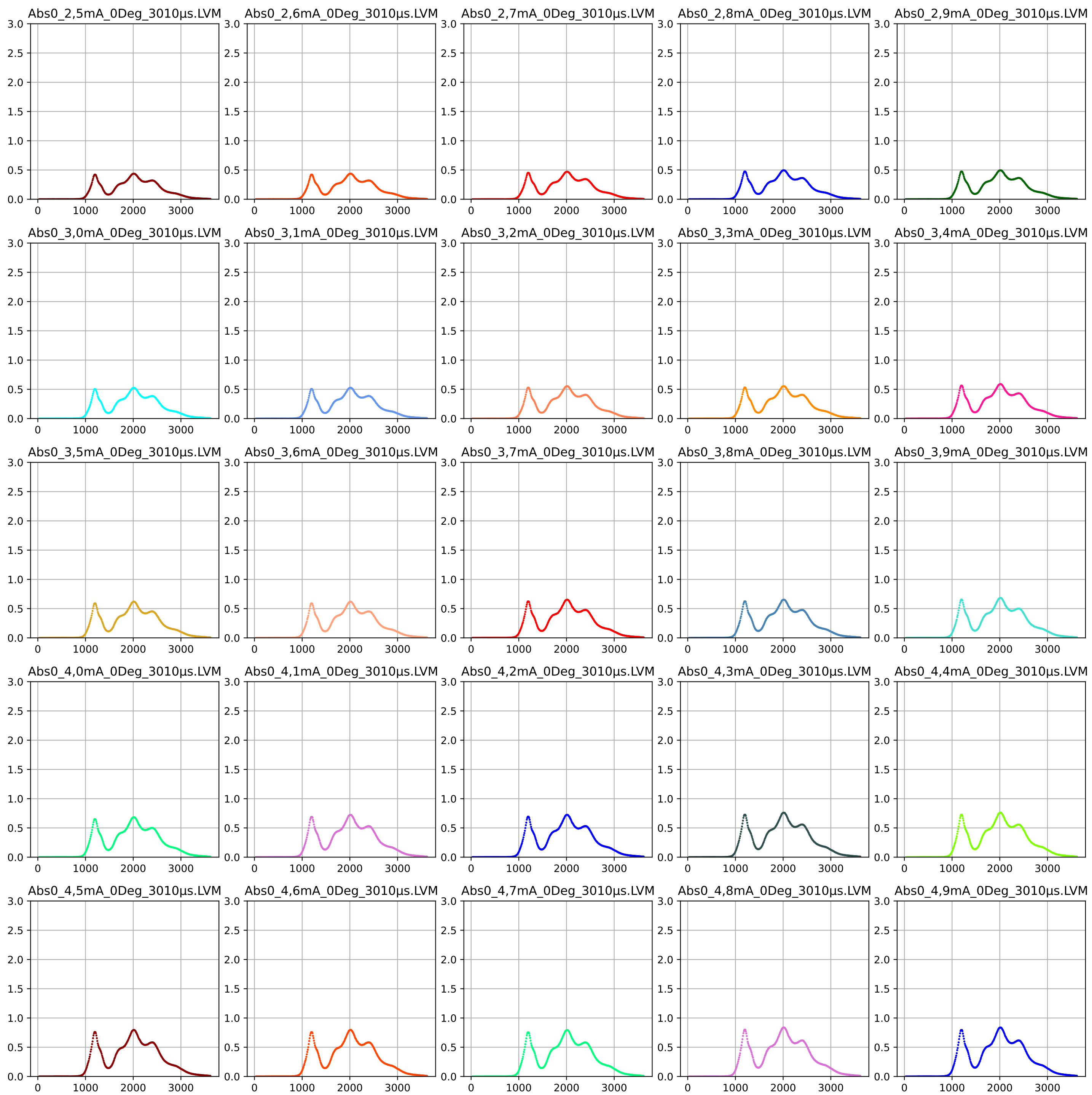


# Abs0\_xmA\_0Deg\_3010μs

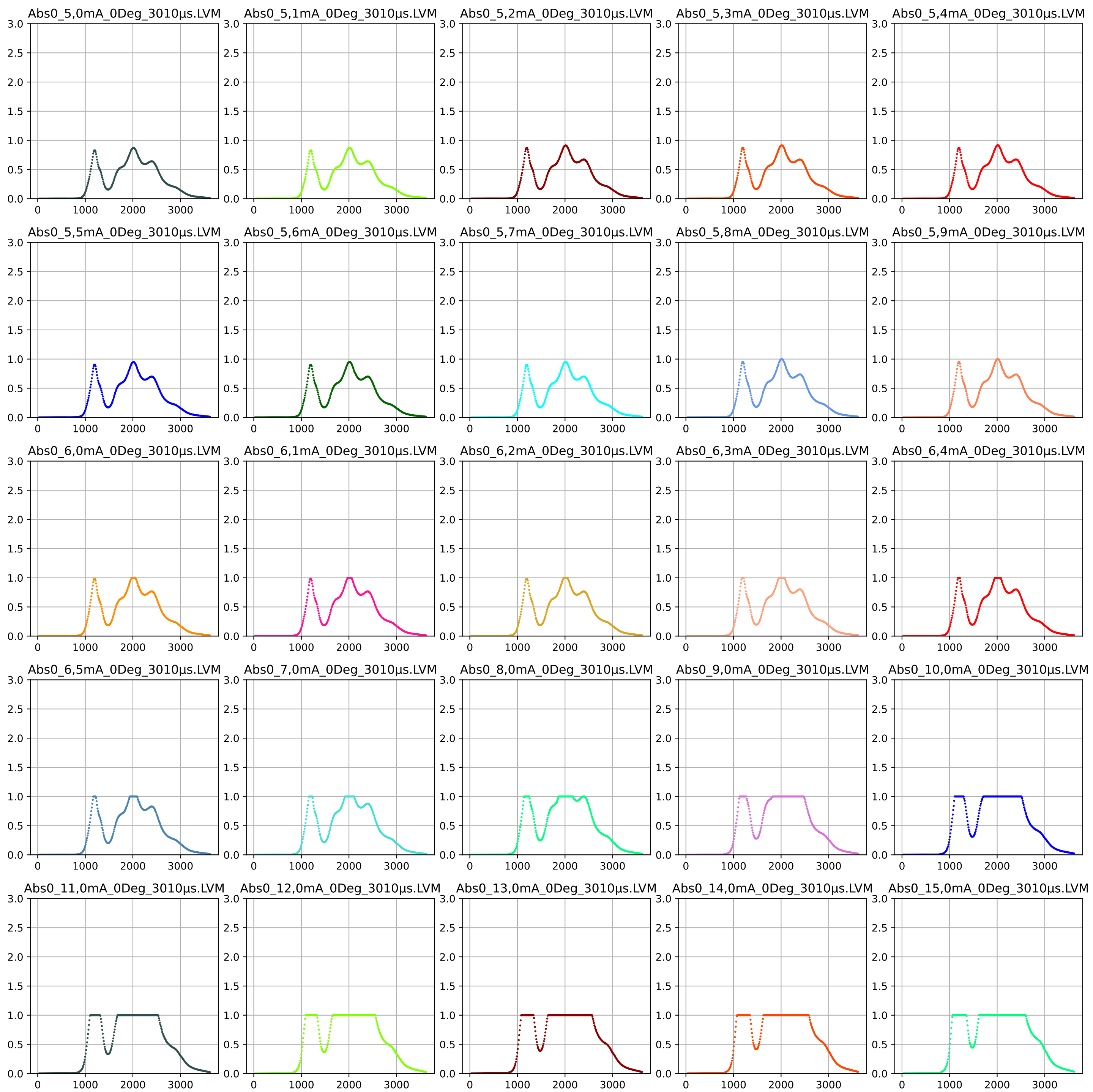




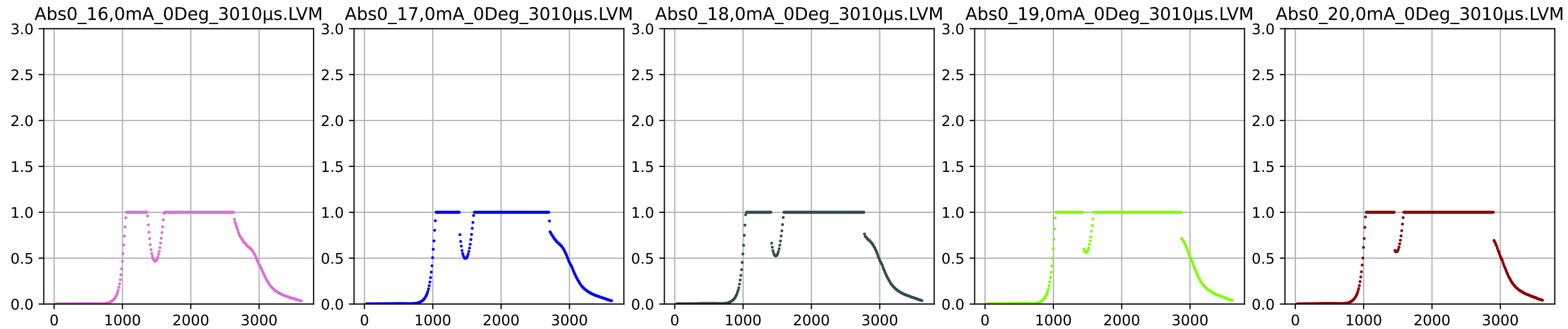
# Abs0\_xmA\_0Deg\_3010μs



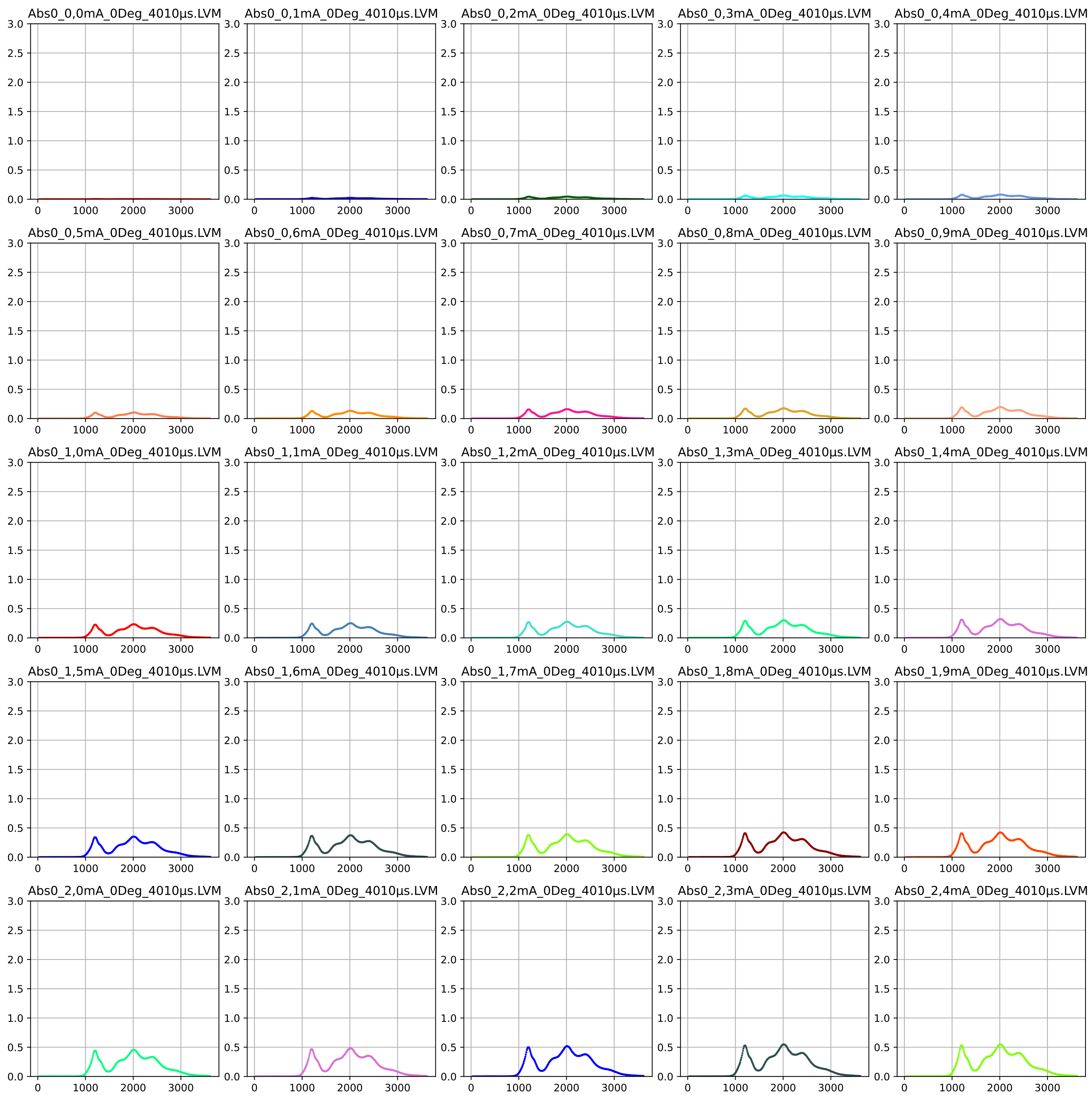
# Abs0\_xxmA\_0Deg\_3010μs



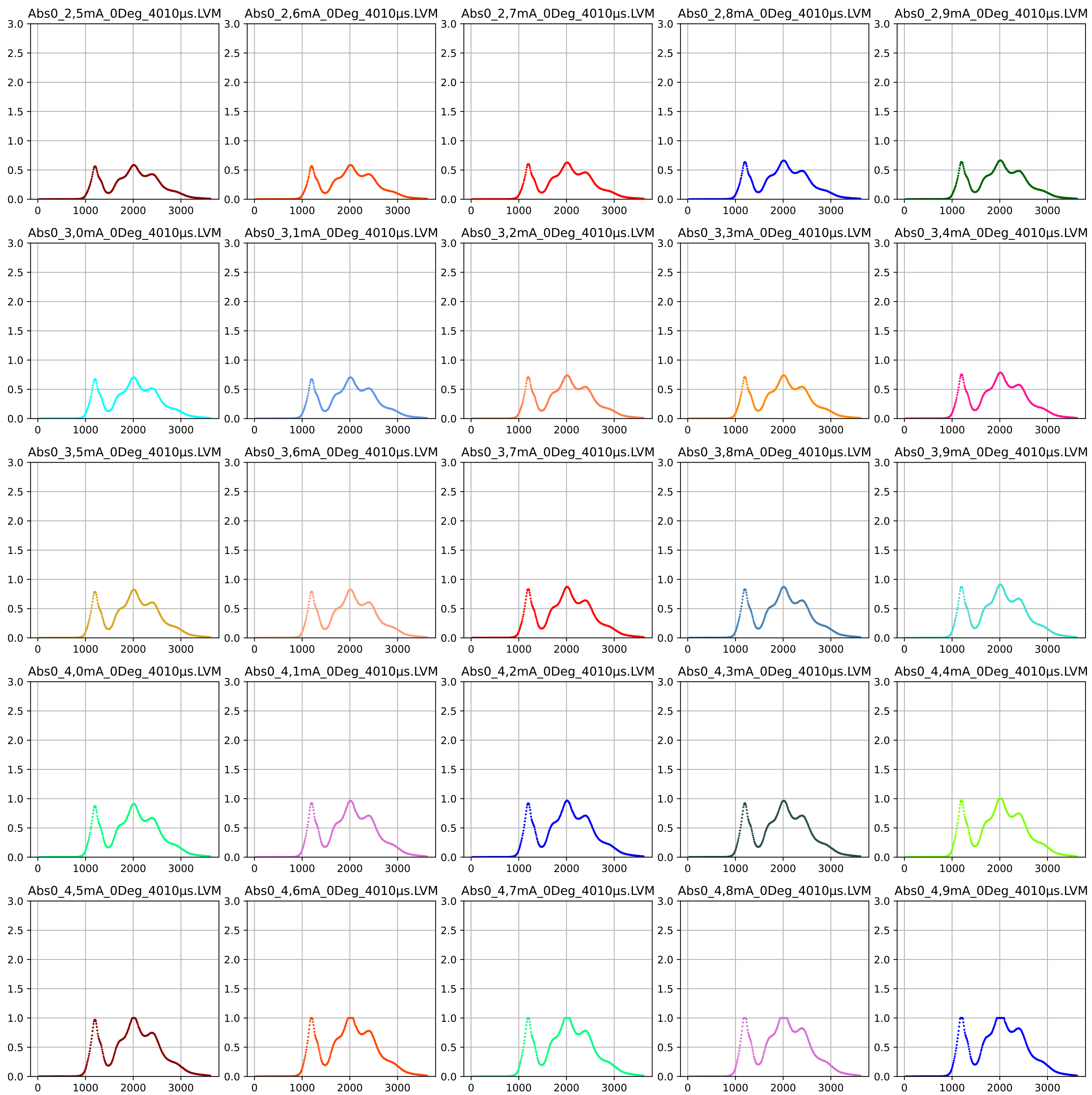
# Abs0\_xmA\_0Deg\_3010μs



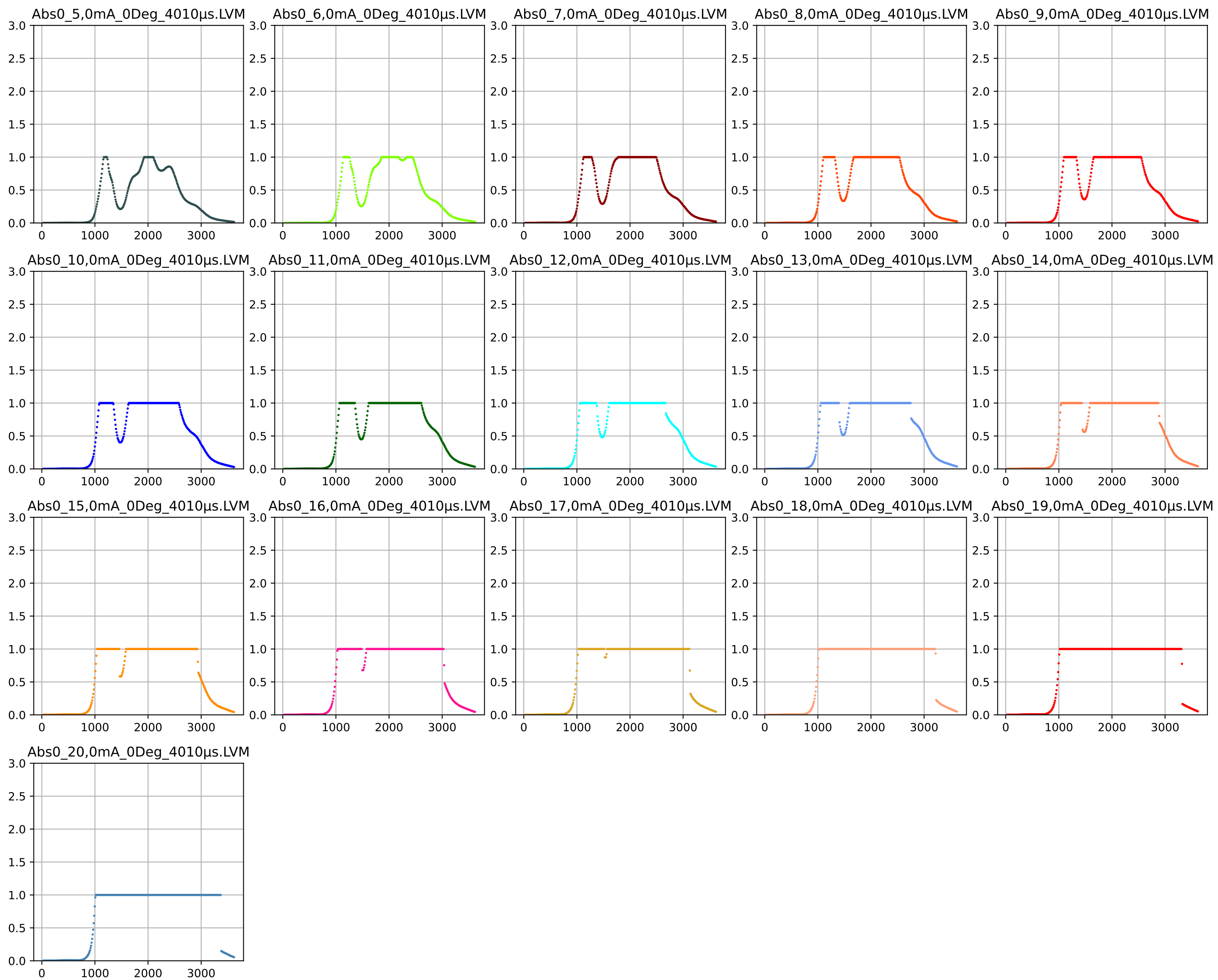
# Abs0\_xmA\_0Deg\_4010μs



# Abs0\_xmA\_0Deg\_4010μs

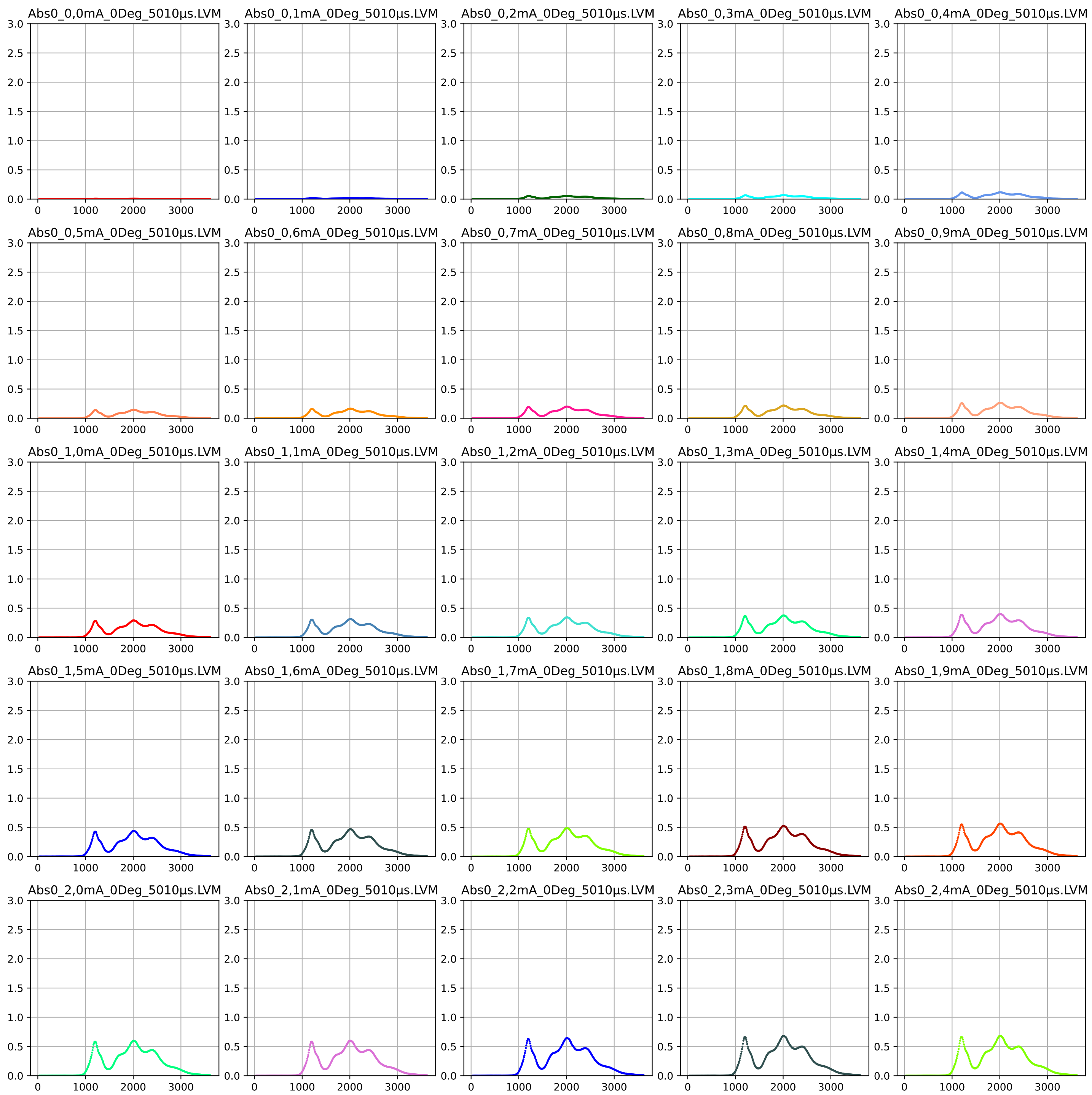


# Abs0\_xmA\_0Deg\_4010μs

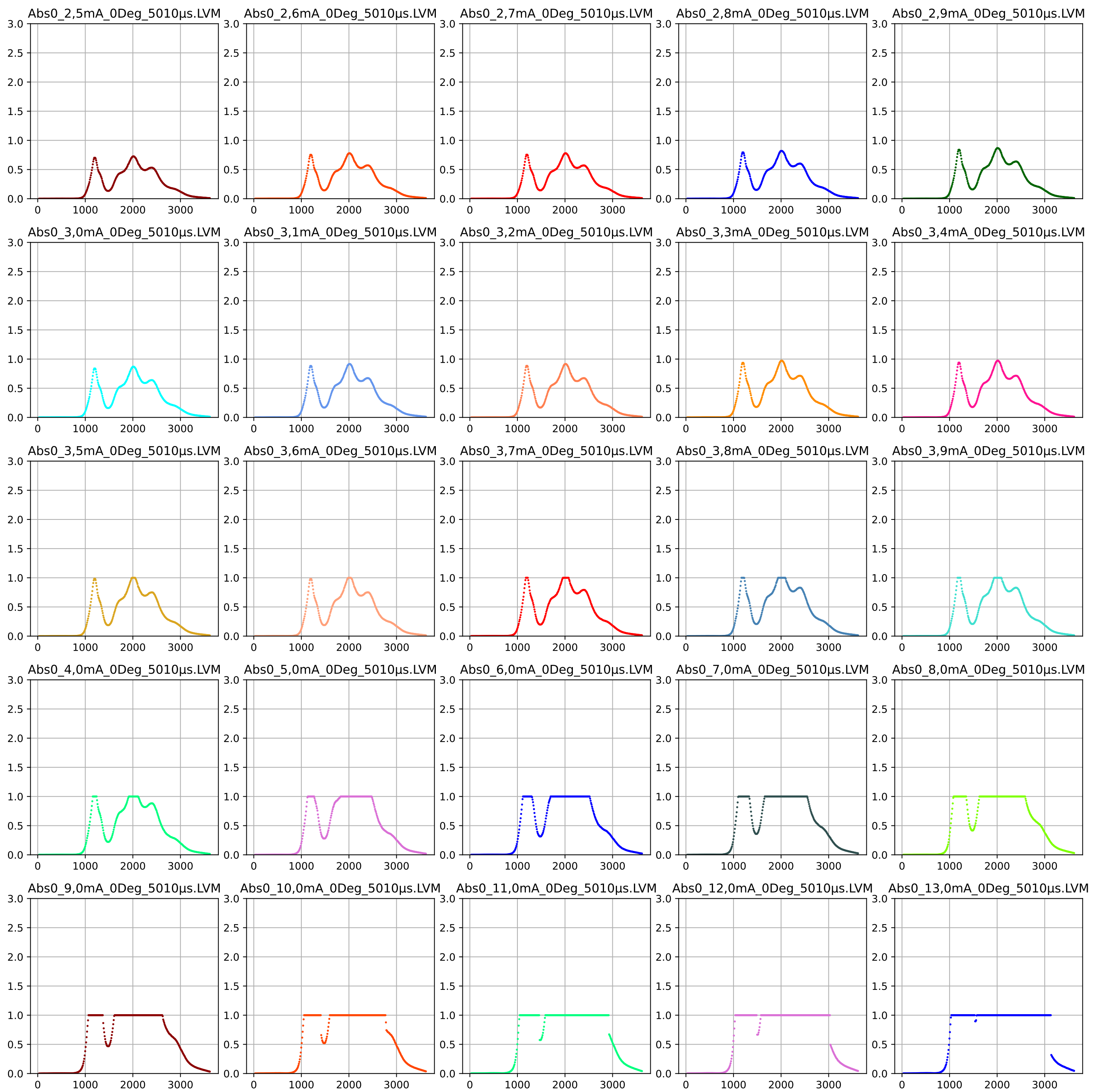




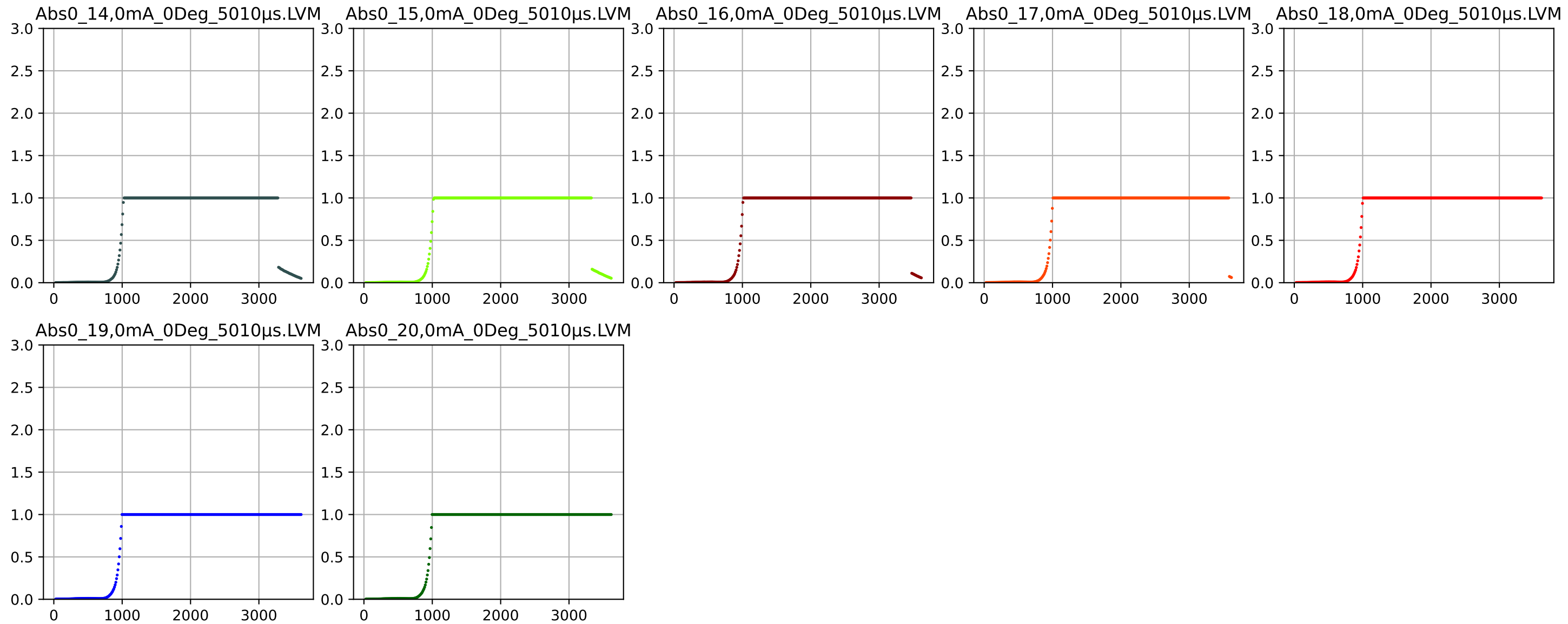
# Abs0\_xmA\_0Deg\_5010μs



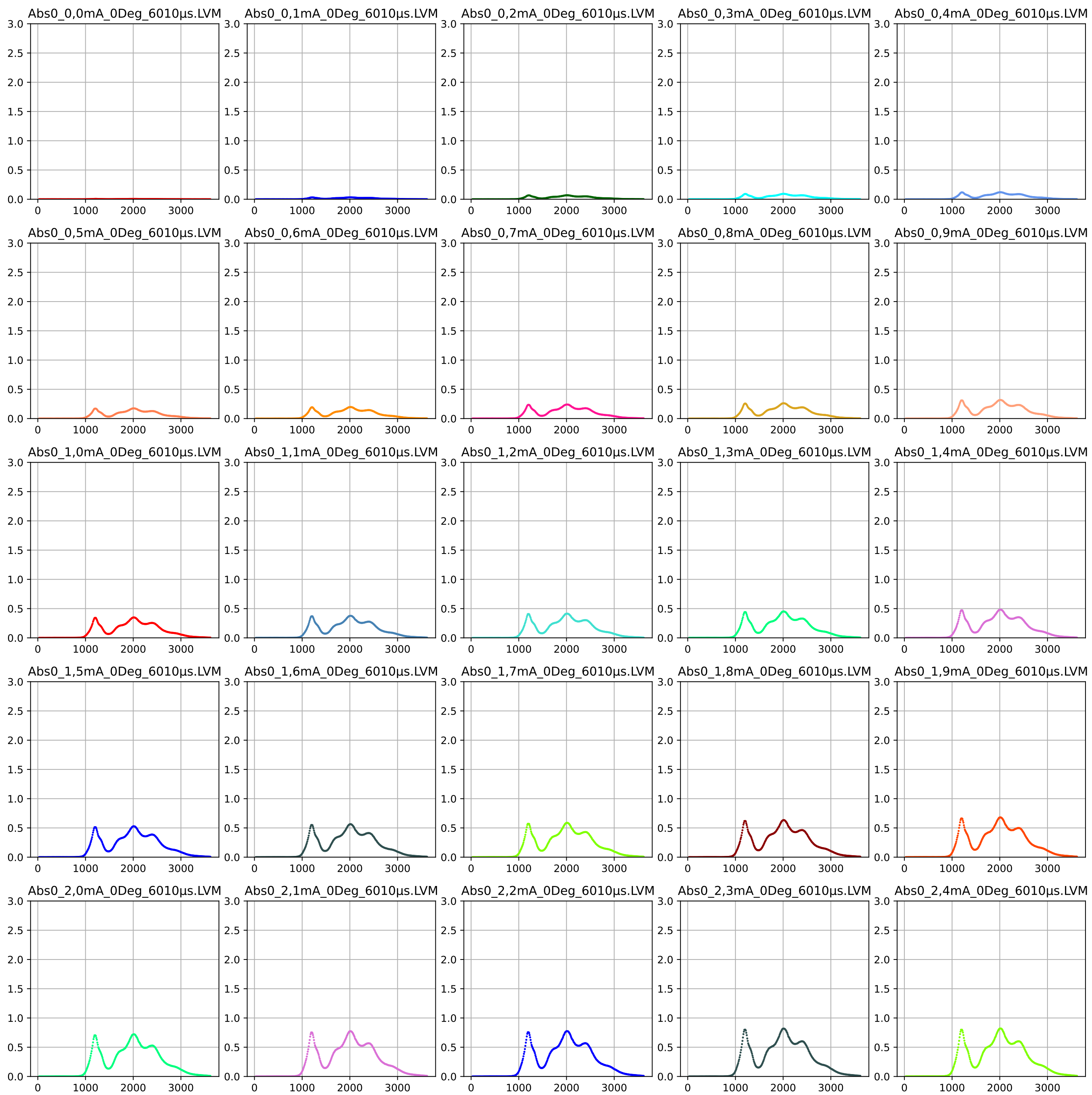
# Abs0\_xxmA\_0Deg\_5010μs



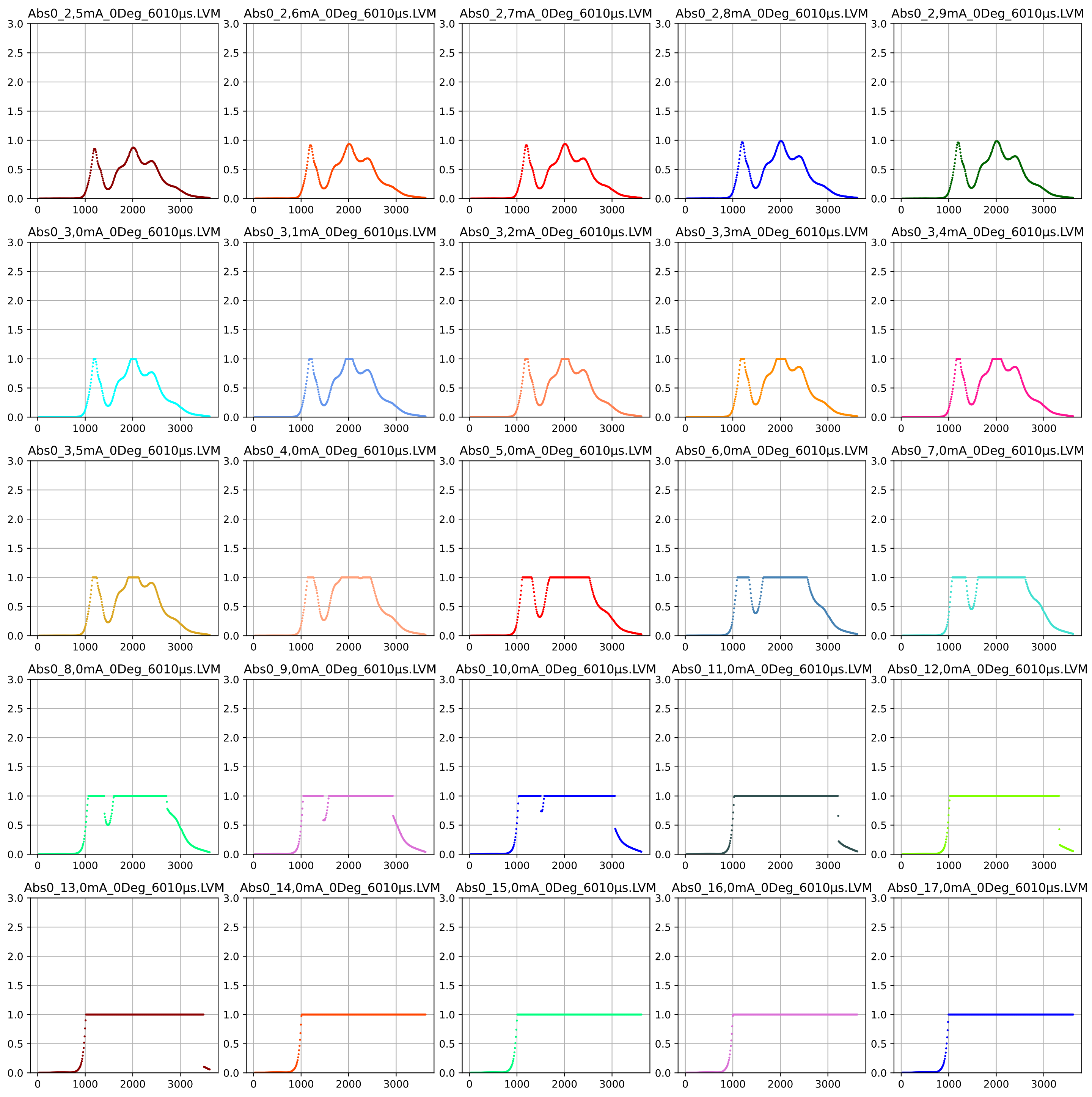
# Abs0\_xmA\_0Deg\_5010μs



# Abs0\_xmA\_0Deg\_6010μs

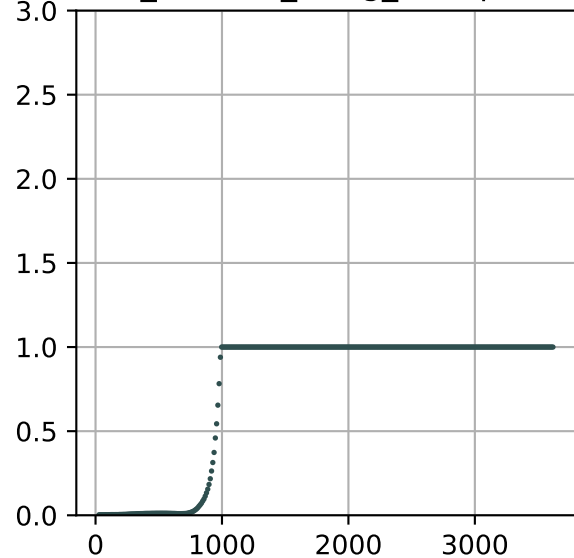


# Abs0\_xxmA\_0Deg\_6010μs

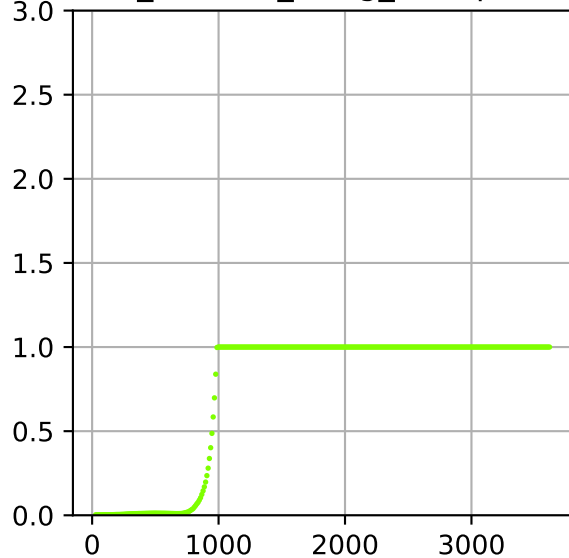


Abs0\_xmA\_0Deg\_6010 $\mu$ s

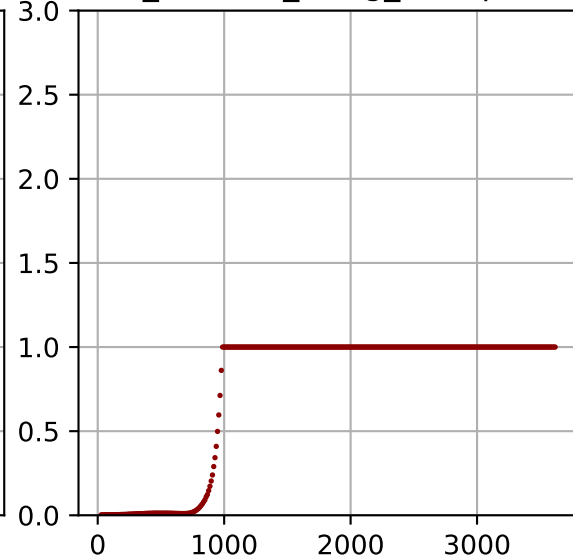
Abs0\_18,0mA\_0Deg\_6010 $\mu$ s.LVM



Abs0\_19,0mA\_0Deg\_6010 $\mu$ s.LVM

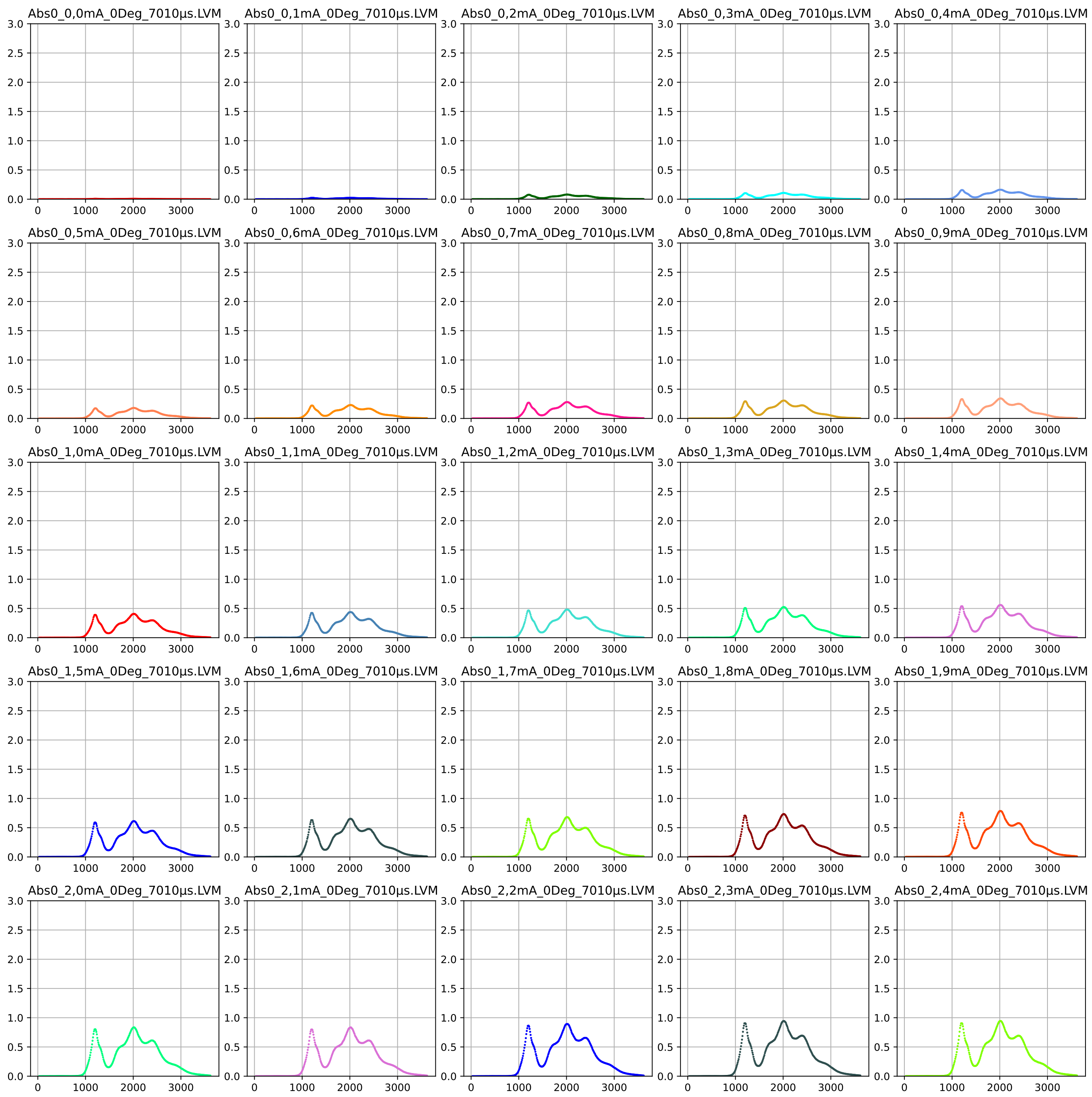


Abs0\_20,0mA\_0Deg\_6010 $\mu$ s.LVM

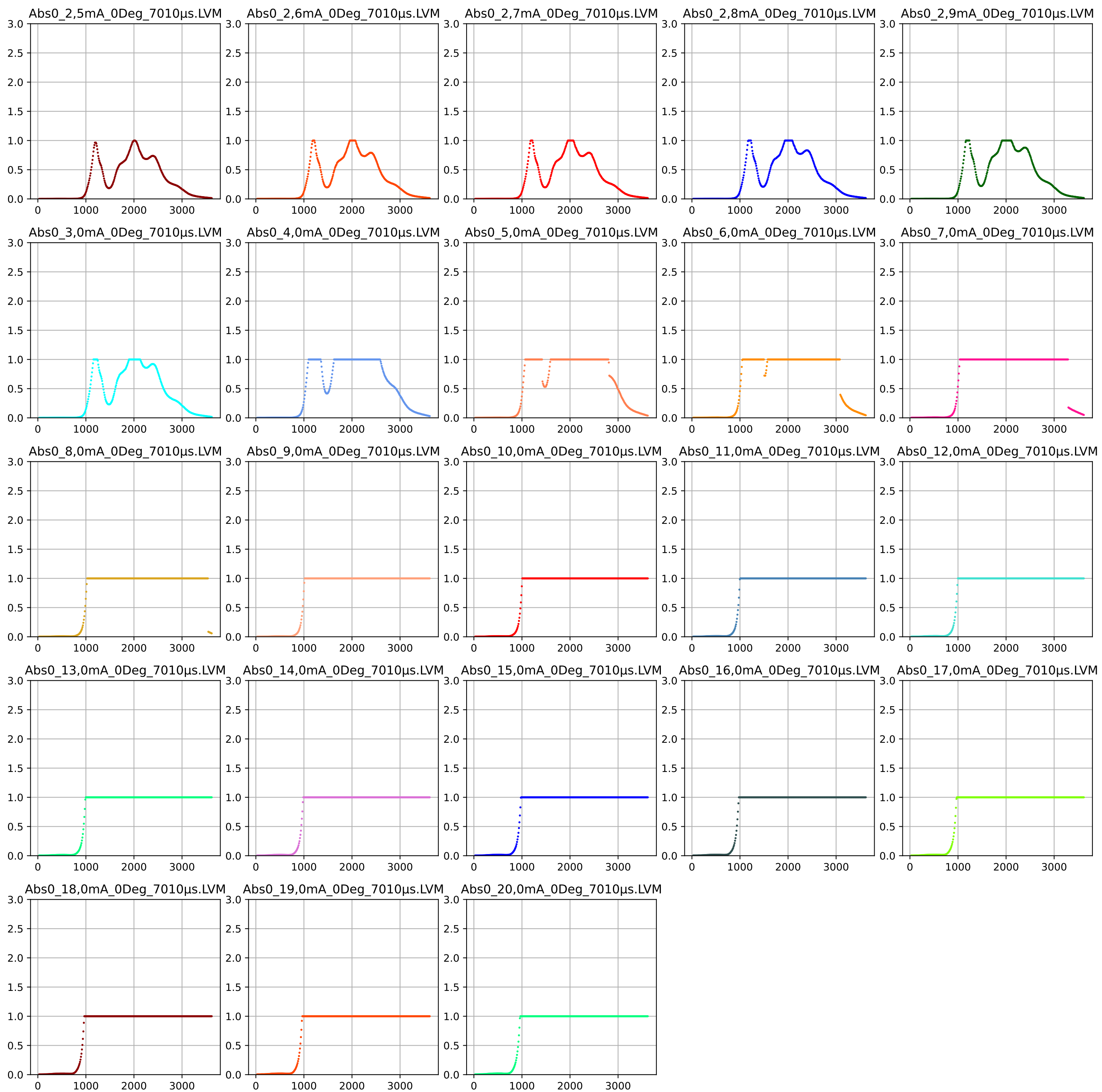




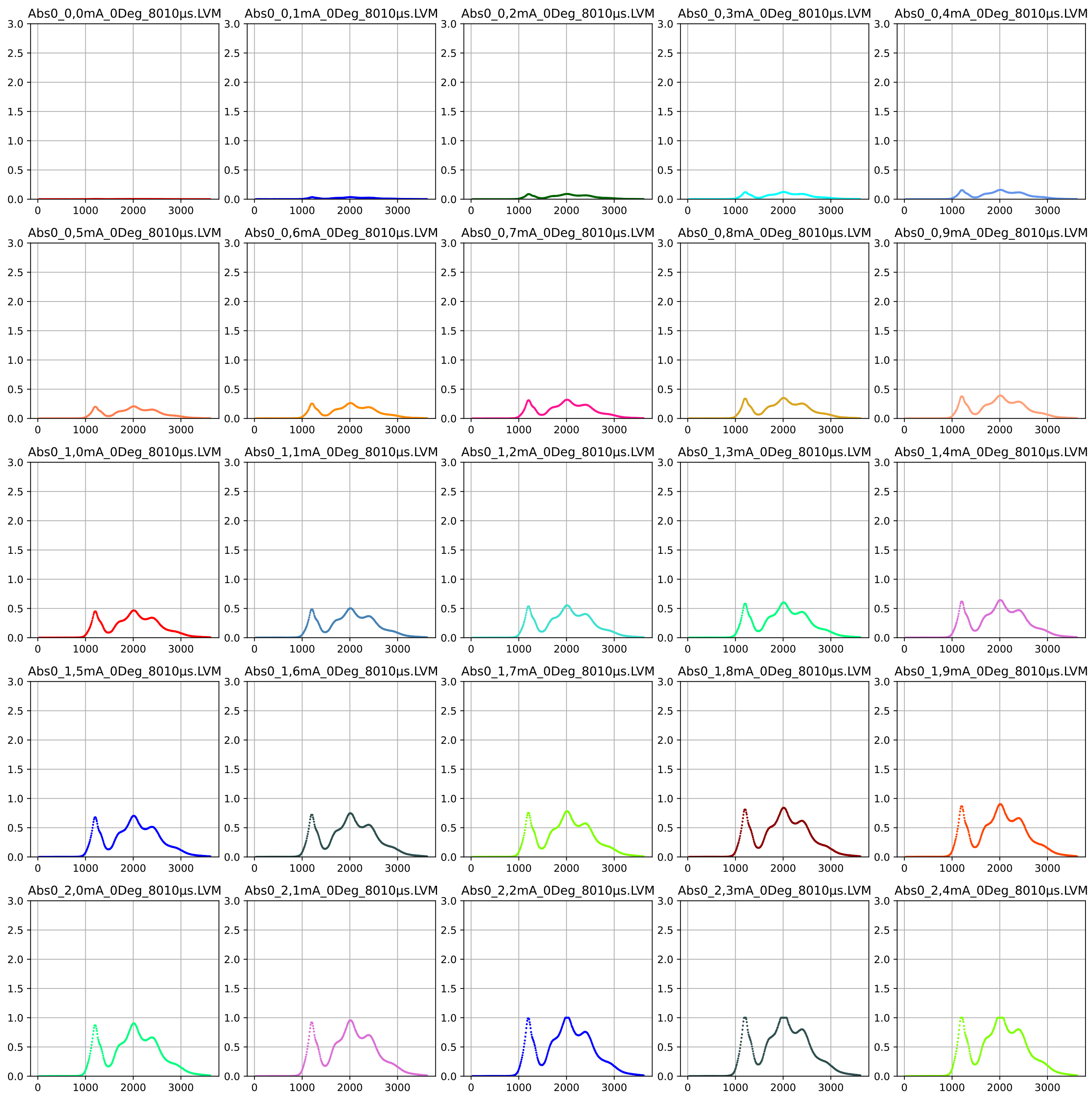
# Abs0\_xmA\_0Deg\_7010μs



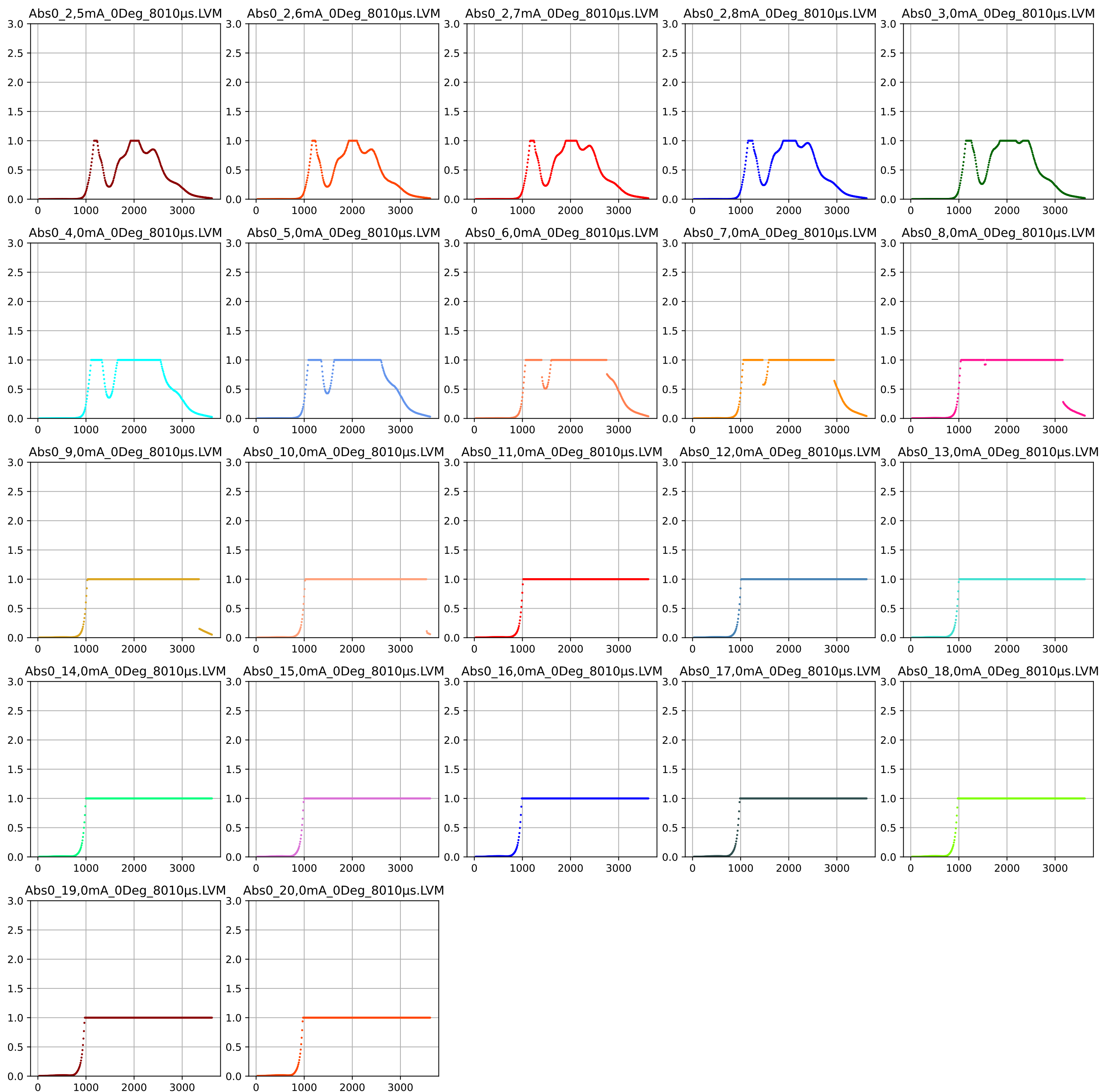
# Abs0\_xmA\_0Deg\_7010μs



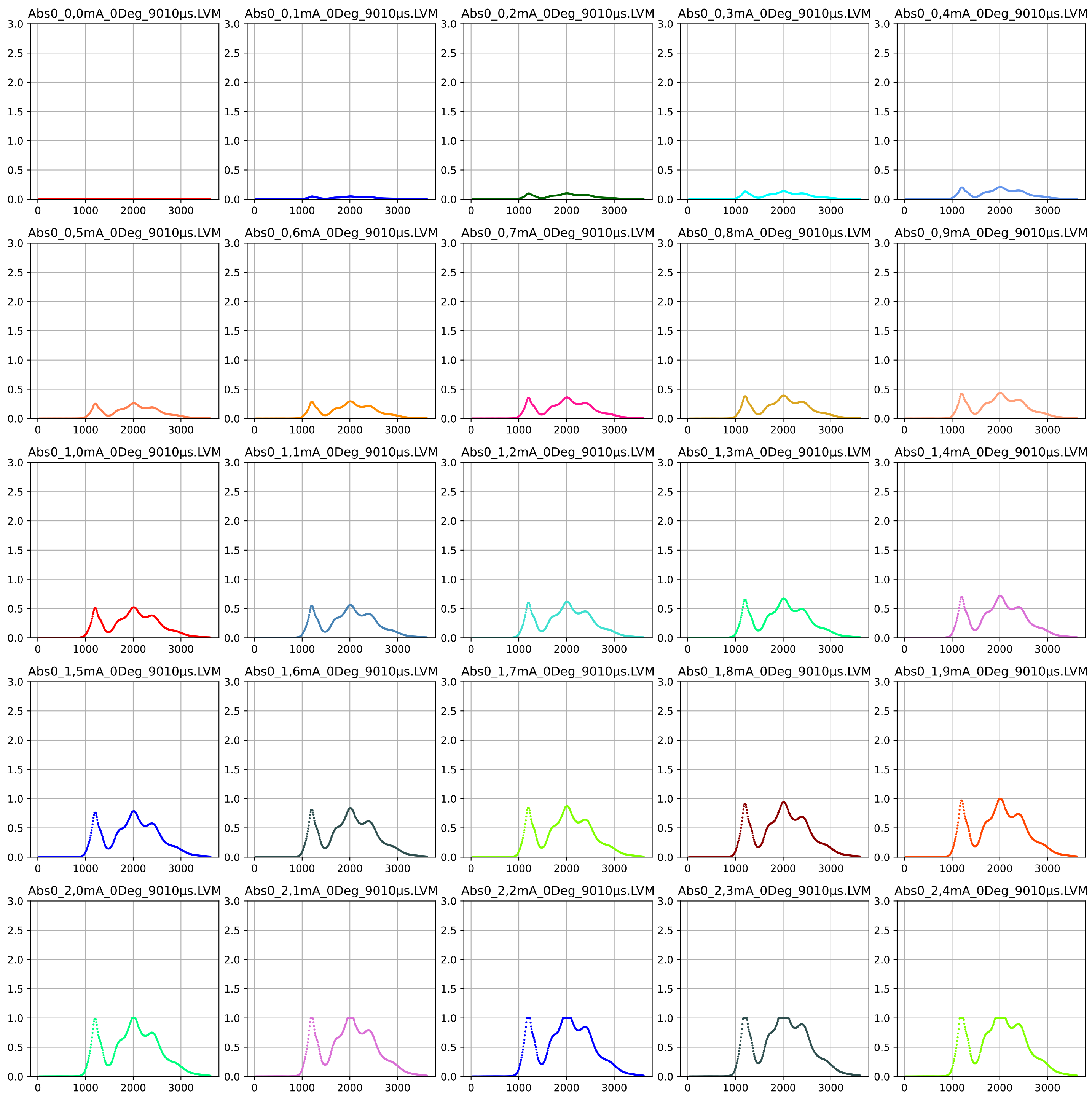
# Abs0\_xmA\_0Deg\_8010μs



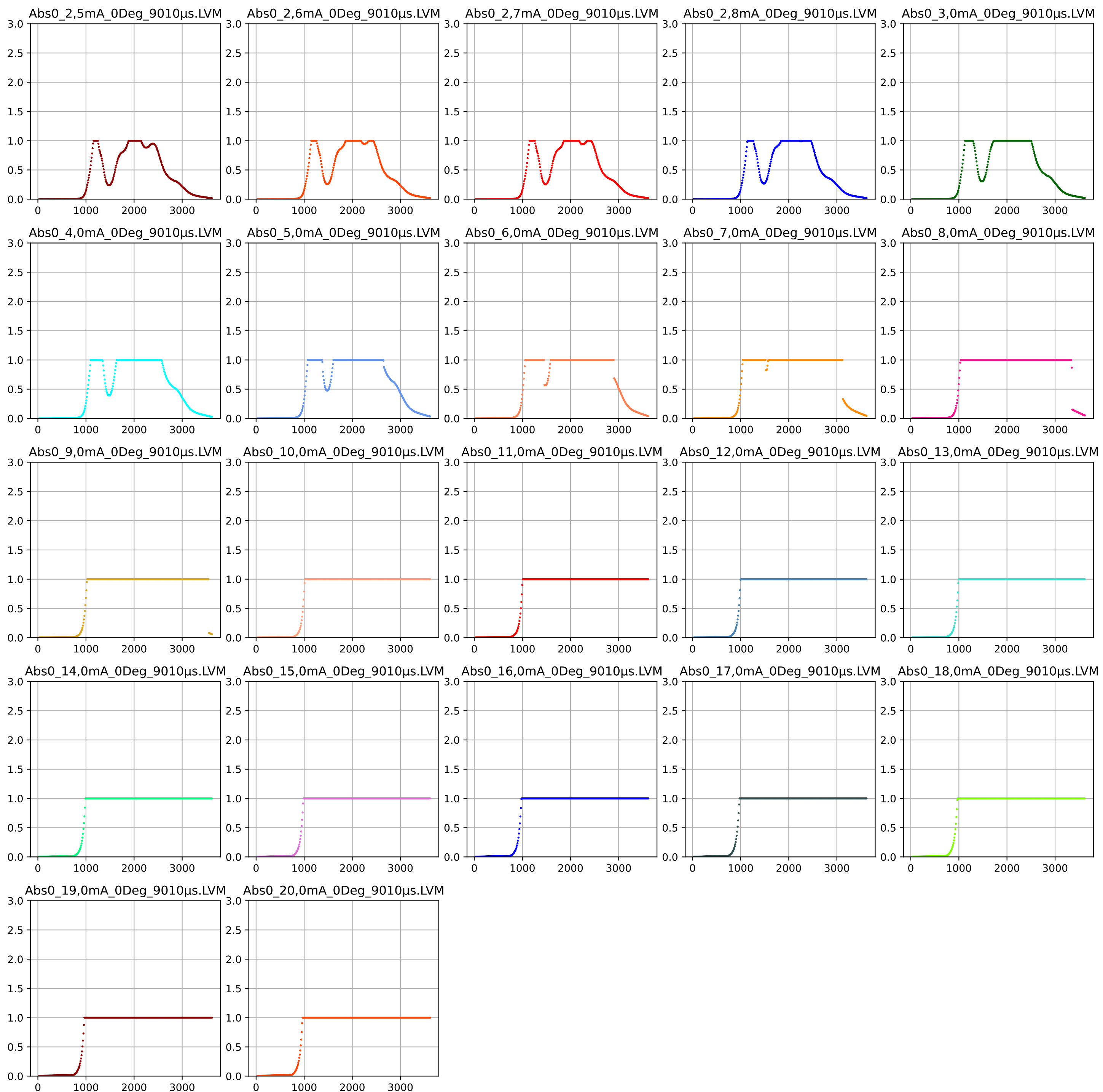
# Abs0\_xmA\_0Deg\_8010μs



# Abs0\_xmA\_0Deg\_9010μs

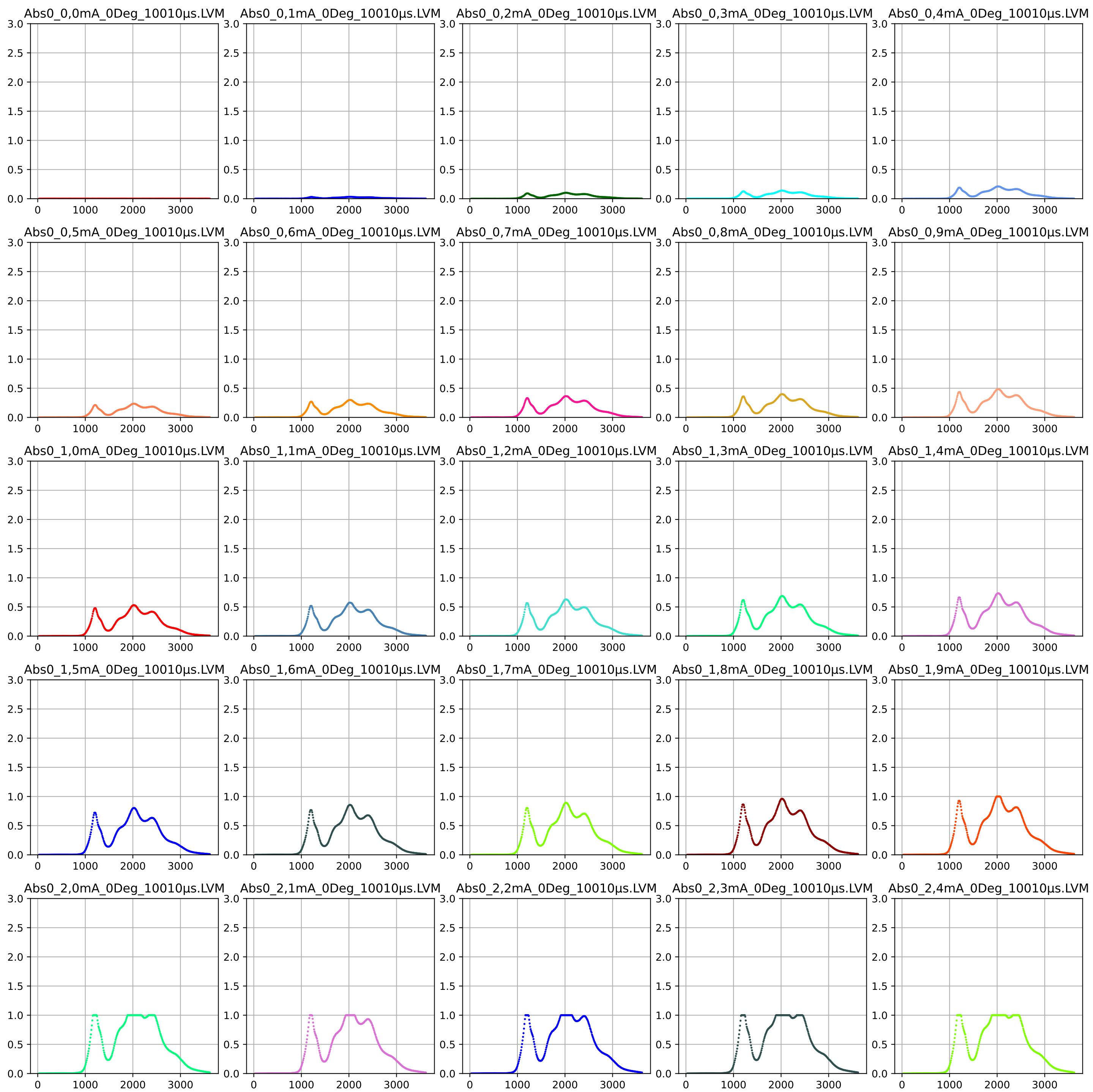


# Abs0\_xmA\_0Deg\_9010μs





# Abs0\_xmA\_0Deg\_10010μs



# Abs0\_xmA\_0Deg\_10010μs

