

Instructions: This is a closed book, closed note exam. Calculators are not permitted. If you have a question, raise your hand and I will come to you. Please work the exam in pencil and do not separate the pages of the exam. For maximum credit, show your work.

Good Luck!

Your Name (*please print*) _____

1	2	3	4	5	total
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30	35	25	30	30	150



Problem 1 (3 parts, 30 points)**Compilation, Concurrency & Interrupts**

Part A (20 points) Perform at least **five** standard compiler optimizations on the following C code fragment by writing the optimized version (in C) to the right.

<pre>int foo(int x, int y) { int g = 32; int h=0, a = 0, b = 1, i=100; while ((x*y)<i) { a += g*(h+i); b *= exp(a, (h+i)); i--; } return (b); }</pre>	\Rightarrow	<pre>int foo(int x, int y){ int a=0,b=1,i=100; int tmp = x*y; while(tmp<i){ a += i << 5; b *= exp(a, i); i--; } return (b); }</pre>
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Briefly describe which standard compiler optimizations you applied:

- 1.
- 2.
- 3.
- 4.
- 5.

Part B (6 points) In the table below, draw lines to match the type of concurrency with the architectural support required for it.

<i>Type of concurrency</i>	Draw lines here:	<i>Architectural Support</i>
Data-level parallelism (DLP)		Multicore system with shared memory
Instruction-level parallelism (ILP)		Multimedia ISA extensions (e.g., MMX, SSE)
Thread-level parallelism (TLP)		Pipelining

Part C (4 points) What is the difference between a nonmaskable interrupt (NMI) and an asynchronous interrupt?

Problem 2 (4 parts, 35 points)**Associative Sets**

Consider a hash table that is implemented using the following struct definitions.

```
typedef struct Entry {
    int      Key;
    int      Value;
    struct Entry *Next;
} Entry;

typedef struct {
    Entry    *Buckets[13];
    int      Size;
} HashTable;
```

Part A (5 points) If a hash table of type `HashTable` is created which contains **273** entries (each of type `Entry`), how many total words of storage are used by this hashtable? (Show work.)

Part B (5 points) Suppose the same `Values` that are contained in the 273 entries are stored in an indexed set (an array), each `Value` indexed by the entry's `Key`. Assume the `Key` can be any unsigned integer representable by 32 bits. How many total words of storage are used by the indexed set? (Show work.)

Part C (10 points) Suppose the bucket lists in the `Buckets` array in **Part A** contain a number of `Entry` objects, evenly distributed across the hash table buckets. Assume that *computing the hash function* takes an average of **five operations** and *comparing two keys* takes an average of **four operations**. Ignore effects of spatial and temporal reference locality. Suppose that **75%** of keys looked up are found in the hash table and **25%** are not found. How many of these operations would be required for the average lookup in the hash table described above if the bucket list is unsorted versus sorted? (Show work.)

number of operations when each
bucket list is *unsorted*:

number of operations when each
bucket list is *sorted*:

Part D (15 points) Write a C subroutine called `CreateEntry` that takes two integers (named `K` and `V`), allocates an object of type `Entry` on the heap with fields `Key=K`, `Value=V`, and `Next=NULL`, and returns a pointer to the `Entry` object allocated. The subroutine should be sure to check whether there was enough space on the heap for the object to be allocated and if not, it should print an error message and exit.

Problem 3 (1 parts, 25 points)**Heap and Hash Table**

Consider an open hash table composed of a four-bucket table, with each bucket containing a variable length list. Each list entry has three slots <key, value, next> corresponding to the three word groupings in the entries section. The hash function is $\text{key} \bmod \text{four}$. Inserted entries are *appended to the end* of a bucket list. Deallocated entries are maintained on a LIFO free list. When the free list is empty, new entry objects are allocated from heap memory. Accesses are listed as <op, key, [value]>. Simulate the access list below and draw the ending state. Assume the hash table is initially empty, the heap pointer is initially 5016 and the free pointer is initially 0.

Heap Pointer	5016	Free List	0000
-----------------	------	--------------	------

Buckets

5000		5004		5008		5012	
------	--	------	--	------	--	------	--

Entries

5016		5040		5064		5088	
5020		5044		5068		5092	
5024		5048		5072		5096	
5028		5052		5076		5100	
5032		5056		5080		5104	
5036		5060		5084		5108	

Hash Table Access Trace

#	op	key	value	#	op	key	value
1	insert	2003	111	5	insert	2007	555
2	insert	2001	222	6	remove	2003	n/a
3	insert	2007	333	7	remove	2005	n/a
4	insert	2005	444	8	insert	2002	666

Problem 4 (4 parts, 30 points)**Garbage Collection, Function Pointers, and MIPS**

Below is a snapshot of heap storage. Values that are pointers are denoted with a “\$”. The heap pointer is \$6168. The heap has been allocated contiguously beginning at \$6000, with no gaps between objects.

addr	value	addr	value	addr	value	addr	value	addr	value	addr	value
6000	16	6032	12	6064	0	6096	16	6128	12	6160	0
6004	33	6036	28	6068	4	6100	6172	6132	72	6164	0
6008	\$6132	6040	\$6120	6072	\$6100	6104	16	6136	9	6168	0
6012	16	6044	\$6080	6076	8	6108	5	6140	\$6004	6172	0
6016	80	6048	16	6080	\$6036	6112	\$6148	6144	20	6176	0
6020	8	6052	\$6092	6084	6012	6116	8	6148	6046	6180	0
6024	25	6056	\$6024	6088	4	6120	32	6152	8	6184	0
6028	\$6036	6060	0	6092	\$6080	6124	\$6024	6156	26	6188	0

Part A (6 points) Suppose the stack holds a local variable whose value is the memory address \$6052 and register \$3 holds the address \$6004. No other registers or static variables currently hold heap memory addresses. List the addresses of all objects in the heap that are *not* garbage.

Addresses of

Non-Garbage Objects: _____

Part B (6 points) If the local variable whose value is the address \$6052 is popped from the stack, which addresses will be reclaimed by each of the following strategies? If none, write “none.”

Reference Counting:	
Mark and Sweep:	
Old-New Space (copying):	

Part C (8 points) Complete the C code below by following these two steps:

1. Create a local variable, called `compare`, in `My_Search` that is a function pointer that points to `GT` if `ascending` is nonzero and to `LT` otherwise. Define the function pointer type with `typedef`.
2. Pass this function pointer to the subroutine `Climb` as its third parameter.

```
int GT(int x, int y)
    return(x>y);
int LT(int x, int y)
    return(abs(x)<abs(y));

typedef _____; /* part 1*/
int My_Search(int a, int b, int ascending) {

    _____; /* part 1*/

    _____; /* part 1*/

    Climb(a, b, _____); /* part 2 */
    ...rest of My_Search's body...
}
```

Part D (10 points) Write the MIPS code implementation of the following C program fragment.

```
int A[100] = {4, -1, 3, ..., 17};
int B[25];
int i;
for(i=0; i<25; i++)
    B[i] = A[4*i];
```

Modify only registers \$1, \$2, \$3, and \$4. *For maximum credit, include comments.*

Label	Instruction	Comment
	<code>.data</code>	
<code>Aaddr:</code>	<code>.word 4, -1, 3, ..., 17</code>	<code># int A[100]={4,-1,3,...,17};</code>
<code>Baddr:</code>	<code>.alloc 25</code>	<code># int B[25];</code>
	<code>.text</code>	
		<code># initialize loop counter</code>
		<code># is counter < 100 (i<25)</code>
		<code># if not, Exit Loop</code>
		<code># 4*i</code>
		<code># read A[4*i]</code>
		<code># write it to B[i]</code>
		<code># increment counter</code>
		<code># loop back</code>
<code>Exit:</code>	<code>...</code>	<code># instructions after the loop</code>

The function `Bar` (below left) calls function `Foo` after completing code block 1. Write MIPS assembly code that properly calls `Foo`. Include all instructions between code block 1 and code block 2. Symbolically label all required stack entries and give their values if they are known (below right).

Bar's	FP	9900	XXX	XXX
		9896	A[1]	25
		9892	A[0]	5
	SP	9888	N	0
		9884		
		9880		
		9876		
		9872		
		9868		
		9864		
		9860		
		9856		

label	instruction	comment
		# allocate activation frame
		# preserve bookkeeping info
		# push inputs
	jal Foo	# call Foo
		# restore bookkeeping info
		# read return value
		# store return value in A[0]
		# deallocate activation frame

MIPS Instruction Set (core)

<i>instruction</i>	<i>example</i>	<i>meaning</i>
arithmetic		
add	add \$1,\$2,\$3	$\$1 = \$2 + \$3$
subtract	sub \$1,\$2,\$3	$\$1 = \$2 - \$3$
add immediate	addi \$1,\$2,100	$\$1 = \$2 + 100$
add unsigned	addu \$1,\$2,\$3	$\$1 = \$2 + \$3$
subtract unsigned	subu \$1,\$2,\$3	$\$1 = \$2 - \$3$
add immediate unsigned	addiu \$1,\$2,100	$\$1 = \$2 + 100$
set if less than	slt \$1, \$2, \$3	if ($\$2 < \3), $\$1 = 1$ else $\$1 = 0$
set if less than immediate	slti \$1, \$2, 100	if ($\$2 < 100$), $\$1 = 1$ else $\$1 = 0$
set if less than unsigned	sltu \$1, \$2, \$3	if ($\$2 < \3), $\$1 = 1$ else $\$1 = 0$
set if < immediate unsigned	sltui \$1, \$2, 100	if ($\$2 < 100$), $\$1 = 1$ else $\$1 = 0$
multiply	mult \$2,\$3	Hi, Lo = $\$2 * \3 , 64-bit signed product
multiply unsigned	multu \$2,\$3	Hi, Lo = $\$2 * \3 , 64-bit unsigned product
divide	div \$2,\$3	Lo = $\$2 / \3 , Hi = $\$2 \bmod \3
divide unsigned	divu \$2,\$3	Lo = $\$2 / \3 , Hi = $\$2 \bmod \3 , unsigned
transfer		
move from Hi	mfhi \$1	$\$1 = \text{Hi}$
move from Lo	mflo \$1	$\$1 = \text{Lo}$
load upper immediate	lui \$1,100	$\$1 = 100 \times 2^{16}$
logic		
and	and \$1,\$2,\$3	$\$1 = \$2 \& \$3$
or	or \$1,\$2,\$3	$\$1 = \$2 \mid \$3$
and immediate	andi \$1,\$2,100	$\$1 = \$2 \& 100$
or immediate	ori \$1,\$2,100	$\$1 = \$2 \mid 100$
nor	nor \$1,\$2,\$3	$\$1 = \text{not}(\$2 \mid \$3)$
xor	xor \$1, \$2, \$3	$\$1 = \$2 \oplus \$3$
xor immediate	xori \$1, \$2, 255	$\$1 = \$2 \oplus 255$
shift		
shift left logical	sll \$1,\$2,5	$\$1 = \$2 \ll 5$ (logical)
shift left logical variable	sllv \$1,\$2,\$3	$\$1 = \$2 \ll \$3$ (logical), variable shift amt
shift right logical	srl \$1,\$2,5	$\$1 = \$2 \gg 5$ (logical)
shift right logical variable	srlv \$1,\$2,\$3	$\$1 = \$2 \gg \$3$ (logical), variable shift amt
shift right arithmetic	sra \$1,\$2,5	$\$1 = \$2 \gg 5$ (arithmetic)
shift right arithmetic variable	srav \$1,\$2,\$3	$\$1 = \$2 \gg \$3$ (arithmetic), variable shift amt
memory		
load word	lw \$1, 1000(\$2)	$\$1 = \text{memory} [\$2+1000]$
store word	sw \$1, 1000(\$2)	memory $[\$2+1000] = \1
load byte	lb \$1, 1002(\$2)	$\$1 = \text{memory} [\$2+1002]$ in least sig. byte
load byte unsigned	lbu \$1, 1002(\$2)	$\$1 = \text{memory} [\$2+1002]$ in least sig. byte
store byte	sb \$1, 1002(\$2)	memory $[\$2+1002] = \1 (byte modified only)
branch		
branch if equal	beq \$1,\$2,100	if ($\$1 = \2), $\text{PC} = \text{PC} + 4 + (100 \times 4)$
branch if not equal	bne \$1,\$2,100	if ($\$1 \neq \2), $\text{PC} = \text{PC} + 4 + (100 \times 4)$
jump		
jump	j 10000	$\text{PC} = 10000 \times 4$
jump register	jr \$31	$\text{PC} = \$31$
jump and link	jal 10000	$\$31 = \text{PC} + 4$; $\text{PC} = 10000 \times 4$