

Problem FC-1 (4 parts)**Branches, Jumps, Loops**

Part A Write a MIPS code fragment that jumps to instruction address 0x8A6B9C2C.

		#
		#
		#

Part B Write a two instruction MIPS code fragment that branches to label `Target` if register \$4 is less than or equal to \$5.

		#
		#

Part C Write a C code fragment equivalent to the following MIPS code. Register \$2 holds the variable “N” and \$4 holds the variable “S”.

```
.text
Foo:    addi $4, $0, 0
Loop:   andi $5, $2, 1
        add  $4, $4, $5
        srl  $2, $2, 1
        bne  $2, $0, Loop
```

Equivalent C code:

Part D Briefly describe what the code fragment in Part C computes.