

WDKA



RESET

S3 •  
S2 •  
S1 •  
IO  
GND -  
5 -  
4 -  
12 -  
13 -  
3V {

W  
D  
K  
A

SDA Gnd Digital I/O Digital I/O  
SCL ARef 13 12 11 10 9 8 7 6 5 4 3 2 1 0

R6 R7 R8 R9

ARD RX TX  
RN TX RX  
R5 R4 R3 C1 R2 R1



RFL

RFLANT

U2



C3

+



RFH

C16  
C2  
C19

ANT-XXX-SP

IOr 3v 5v Gnd Vin 0 1 2 3 4 5

RST

Analog In