

# ABHINAV SARJE

abhinav.sarje@gmail.com • (515) 203 9030 • www.abhinavsarje.net

<b>Education</b>	<b>Iowa State University</b> , Ames, IA, USA	August 2005 – August 2010
	PhD Candidate in Computer Engineering ( <i>advisor</i> : Dr. Srinivas Aluru) <i>Thesis</i> : Applications on emerging paradigms in parallel computing	<i>GPA</i> : 3.97/4.00
	<b>Indian Institute of Technology Guwahati</b> , Guwahati, India	August 2000 – May 2004
	Bachelor of Technology in Computer Science & Engineering	<i>GPA</i> : 8.35/10.00
<b>Research Interests</b>	High Performance Computing	Parallel Algorithms & Applications
	Algorithms on Emerging Architectures	Cloud Computing
	Computational Biology and Scientific Computing	String and Text Algorithms
<b>Work Experience</b>	<b>AOL Inc.</b> , Truviso	Software Engineer
	San Francisco, CA, U.S.A.	September 2010 – <i>present</i>
	Part of the web video search group, focussing on building a Truviso notification system to notify subscribers about the corresponding new videos for celebrities and TV shows found on the web, and celebrity search based on video and audio detection.	
	<b>Iowa State University</b> , Computer Engineering Department	Research Assistant
	Ames, IA, U.S.A.	August 2005 – <i>present</i>
	Worked on the development and implementation of parallel algorithms on emerging paradigms for various problems with applications in computational and systems biology, and materials science. This includes algorithms for genomic alignments and scheduling of all-pairs computations on emerging multi- and many-core architectures, cloud computing on tree structures, and provably optimal parallel algorithms for the all- $k$ -nearest neighbor problem, and multi-search on trees.	
	<b>Samsung India Software Operations</b>	Software Engineer
	Bangalore, India	December 2004 – July 2005
	Contributed to the design and development of traffic management algorithms for network traffic distributor (IP switch). Involved in the development and testing of IP Multimedia Subsystem (IMS) for large scale 3G wireless network deployment.	
	<b>Center for Development of Telematics</b>	Research Engineer
	Bangalore, India	August 2004 – December 2004
	Involved in the development and testing of network management software for telecommunications switches and servers. Worked on VoIP management project being initiated in the group.	
	<b>Linköping University</b> , Computational Biology Group, IFM	Project Student
	Linköping, Sweden	May 2003 – July 2003
	Worked on a survey of data clustering methods used for microarray data, and implementation of agglomerative hierarchical clustering and basic $k$ -means clustering algorithms as libraries in Java to be used by the researchers as modules for microarray data analysis.	
	<b>Indian Institute of Technology Bombay</b> , Computer Science Department	Summer Intern
	Mumbai, India	May 2002 – June 2002
	Worked on fault testing of Xilinx FPGA demonstration boards using VHDL to design testbeds.	
<b>Primary Graduate Research Work</b>	<ul style="list-style-type: none"><li>• Parallel genomic alignments on heterogeneous multi-cores [C &amp; Cell SDK].</li><li>• Accelerating pairwise computations on clusters of multi-cores [C++, Cell SDK, MPI-2 &amp; CUDA].</li><li>• An abstract framework for trees structures on clouds [C++ &amp; MPI-2].</li><li>• Provably optimal parallel algorithm for <math>k</math>-nearest neighbors and related problems [Ongoing].</li><li>• Parallel algorithm for multi-search on tree structures [Ongoing].</li></ul>	

<b>Relevant Graduate-level Course-work</b>	Parallel Algorithms and Programming Parallel Algorithms for Scientific Applications Fundamental Algorithms in Computational Biology Probabilistic Methods in Computer Engineering Introduction to Computational Geometry	Distributed Algorithms Theory of Computation Computer Systems Architecture Database Implementation Principles of Compilers
<b>Relevant Technical Skills</b>	<i>[Expert/good knowledge of]</i> <ul style="list-style-type: none"> <li>• <b>Languages:</b> C++, C, Unix shell scripting, Ruby.</li> <li>• <b>Parallel programming standards and paradigms:</b> MPI-2, nVidia CUDA, Intel TBB, IBM Cell SDK, OpenMP, Posix threads.</li> <li>• <b>Parallel architectures:</b> Clusters, MPPs, many/multi-cores and heterogeneous systems including Cell B.E. and GPGPUs.</li> </ul>	
<b>Select Publications</b>	<ul style="list-style-type: none"> <li>• <b>A. Sarje, J. Zola and S. Aluru, “Accelerating Pairwise Computations on the Cell Processors”</b>, IEEE Transactions on Parallel and Distributed Systems – Special Issue on High-Performance Computing with Accelerators, to appear August 2010.</li> <li>• <b>J. Zola, M. Aluru, A. Sarje and S. Aluru, “Parallel Information Theory Based Construction of Genome-wide Gene Regulatory Networks”</b>, IEEE Transactions on Parallel and Distributed Systems, to appear 2010.</li> <li>• <b>A. Sarje and S. Aluru, “Parallel Genomic Alignments on the Cell Broadband Engine”</b>, IEEE Transactions on Parallel and Distributed Systems, vol. 20, no. 11, pp. 1600–1610, 2009.</li> <li>• <b>A. Sarje and S. Aluru, “A MapReduce Style Framework for Computations on Trees”</b>, in 39th International Conference on Parallel Processing (ICPP), to appear 2010.</li> <li>• <b>A. Sarje, J. Zola and S. Aluru, “Constructing Gene Regulatory Networks on Clusters of Cell Processors”</b>, in proc. of the 38th International Conference on Parallel Processing (ICPP), pp. 108–115, 2009.</li> <li>• <b>A. Sarje and S. Aluru, “Parallel Biological Sequence Alignments on the Cell Broadband Engine”</b>, in proc. of the 22nd IEEE International Parallel and Distributed Processing Symposium (IPDPS), pp. 1–11, 2008.</li> <li>• <b>A. Sarje, A. Chawre and S. Nair, “Reinforcement Learning of Player Agents in RoboCup Soccer Simulation”</b>, in proc. of the 4th IEEE International Conference on Hybrid Intelligent Systems (HIS), pp. 480–481, 2004.</li> <li>• <b>A. Sarje and S. Aluru, “Parallel Algorithms for Alignments on the Cell B.E.”</b>, in Bioinformatics: High Performance Parallel Computer Architectures, Ed. B. Schmidt, Taylor &amp; Francis Group/CRC Embedded Multi-Core Systems Series, July 2010.</li> <li>• <b>A. Sarje, J. Zola and S. Aluru, “Pairwise Computations on the Cell Processor with Applications in Computational Biology”</b>, in Scientific Computing with Multicore and Accelerators, Eds. J. Dongarra, D. A. Bader and J. Kurzak, Chapman &amp; Hall/CRC Computational Science Series, to appear 2010.</li> <li>• <b>A. Sarje, “Parallel Techniques for Efficient Pairwise Computations on Emerging Architectures”</b>, in PhD Forum at the 24th International Parallel and Distributed Processing Symposium (IPDPS), 2010 (<b>TCPP best paper award</b>).</li> <li>• <b>A. Sarje and S. Aluru, “A Mapreduce Style Framework for Trees”</b>, Technical Report, Department of Electrical and Computer Engineering, Iowa State University, 2009.</li> </ul>	
<b>Professional Affiliations, Activities &amp; Achievements</b>	<ul style="list-style-type: none"> <li>• Best paper award at TCPP PhD forum, held at IPDPS 2010.</li> <li>• Research Excellence Award, Iowa State University, 2010.</li> <li>• Graduate student member of the IEEE, and IEEE Computer Society.</li> <li>• External referee for various conferences and workshops, including IPDPS 2010, ICPP 2010, CF 2010, ICCS 2010, COCOON 2009, PPAM 2009, IC3 2009, HiPC 2008, CPM 2008.</li> </ul>	
<b>References</b>	<i>Available on request.</i>	