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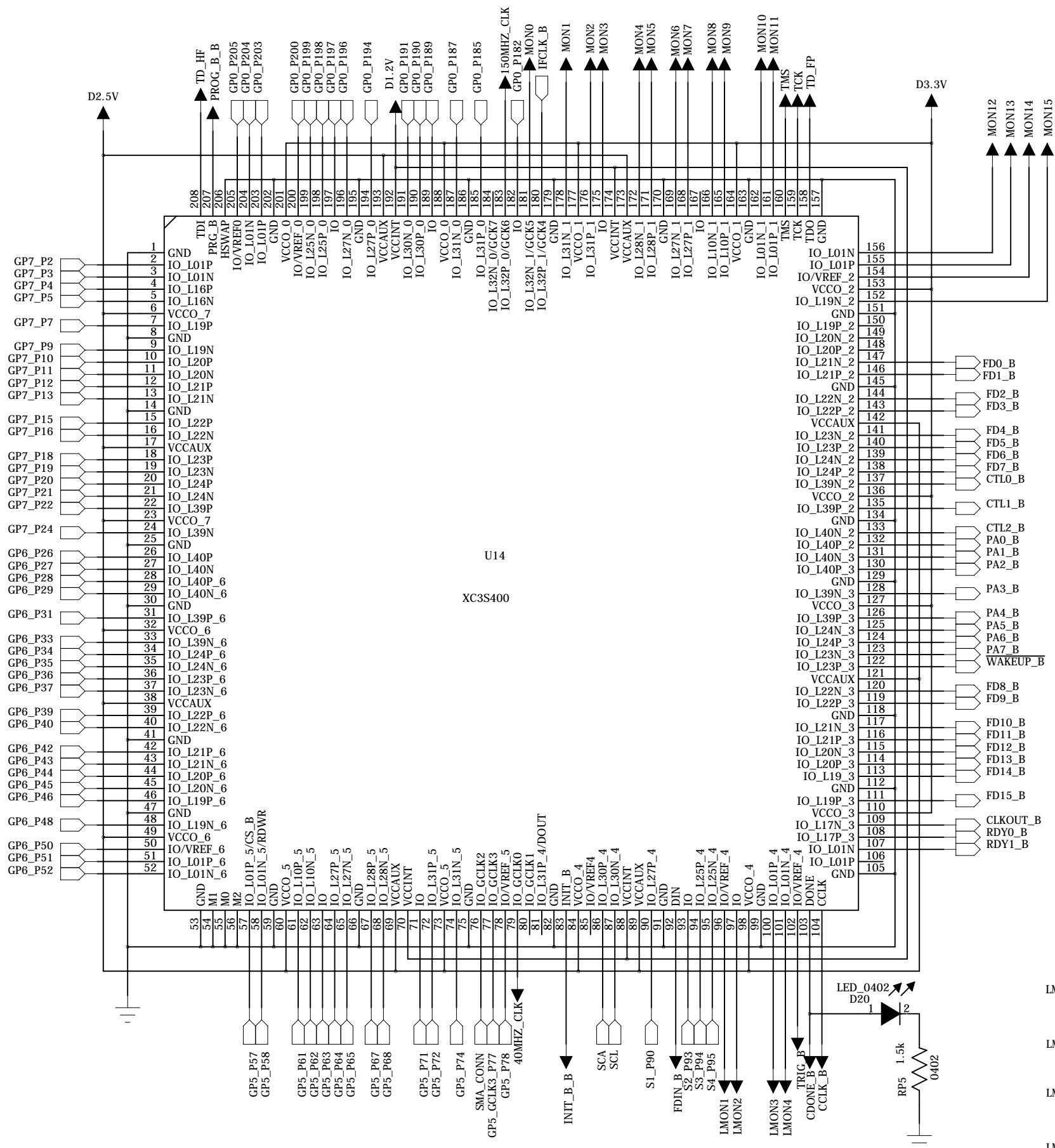
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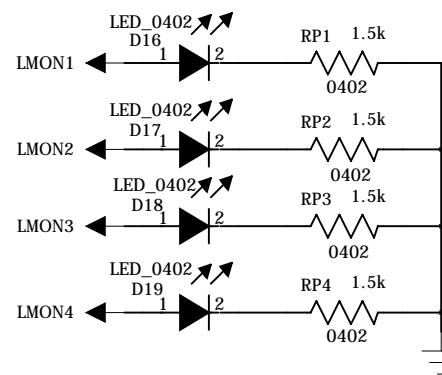
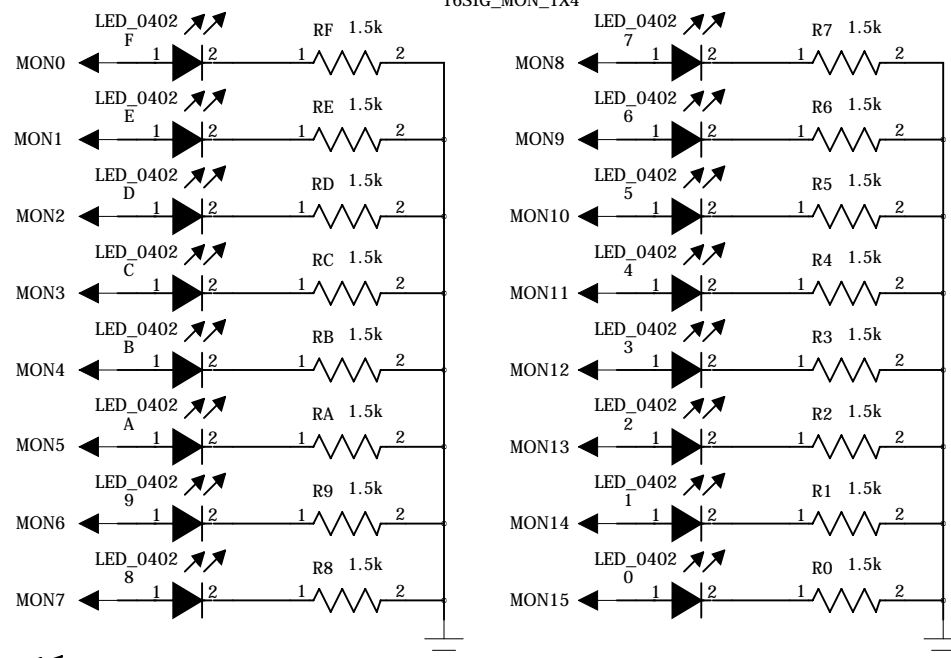
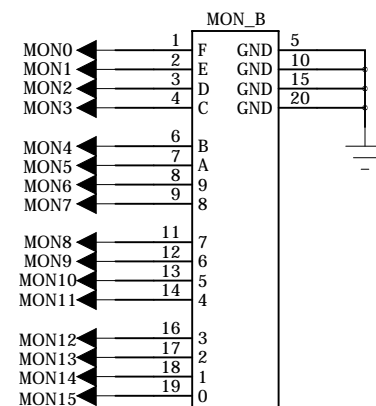
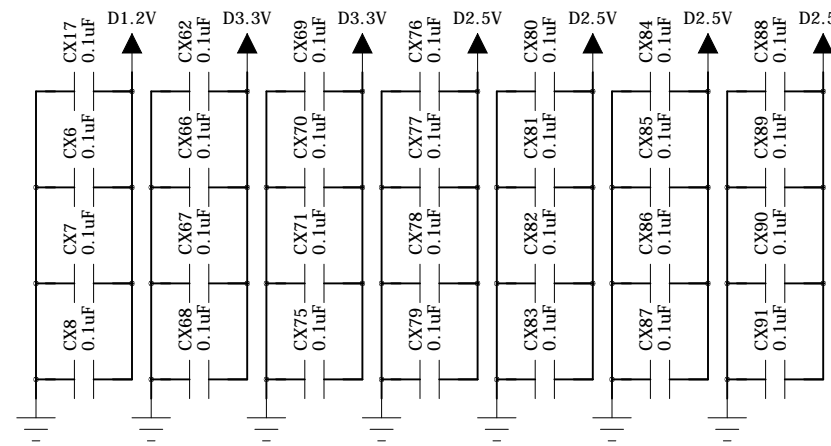
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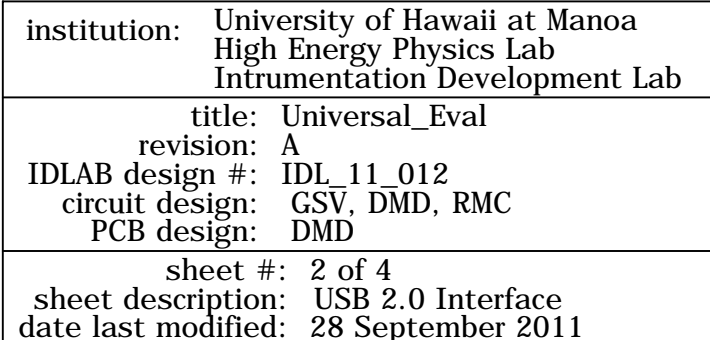
A



institution: University of Hawaii at Manoa
High Energy Physics Lab
Instrumentation Development Lab

title: Universal_Eval
revision: A
IDLAB design #: IDL_11_012
circuit design: GSV, DMD, RMC
PCB design: DMD

sheet #: 1 of 4
sheet description: Overall ASIC Control
date last modified: 28 September 2011



E

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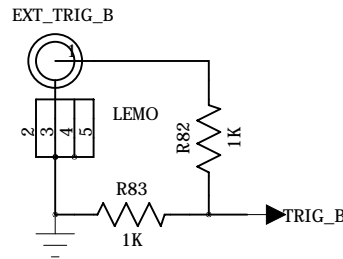
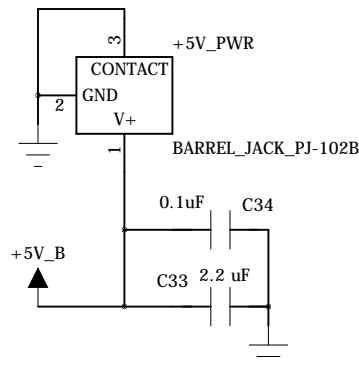
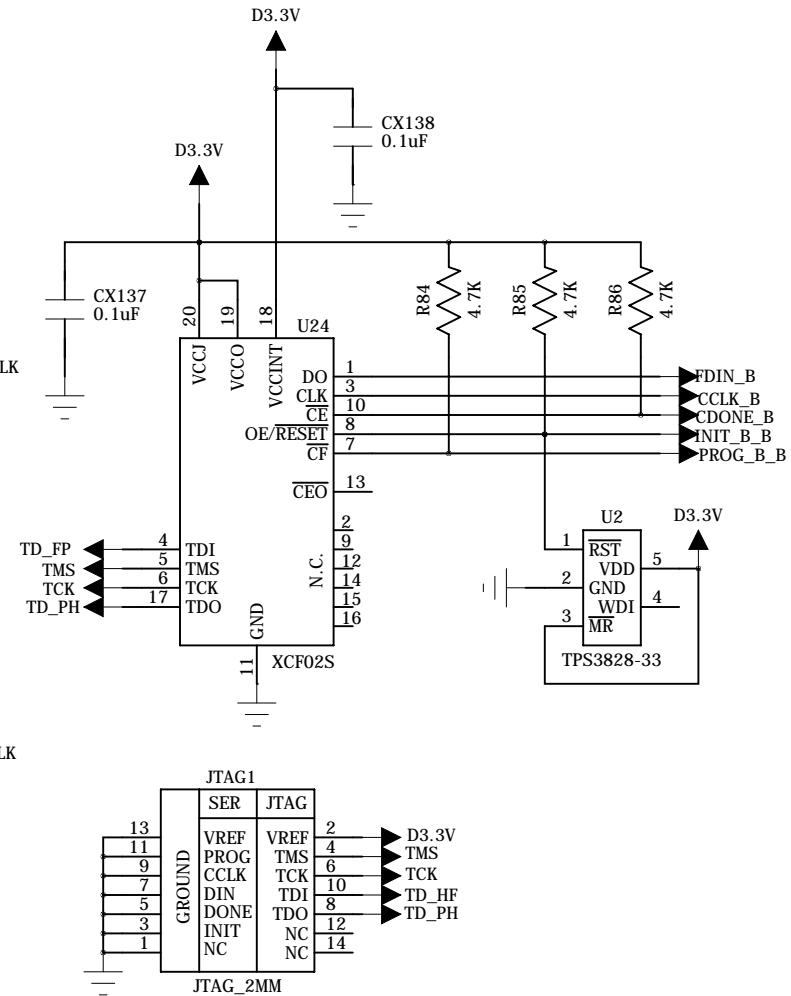
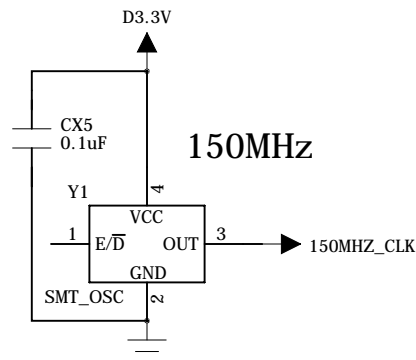
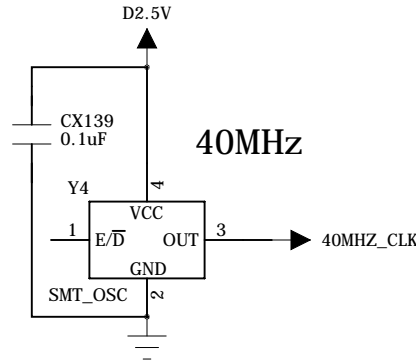
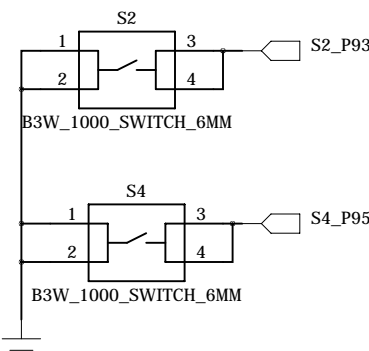
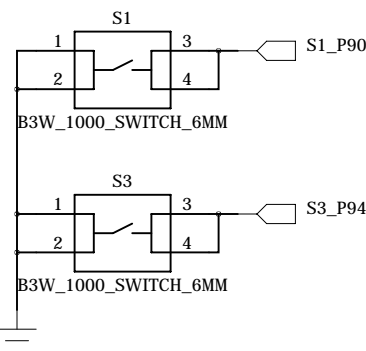
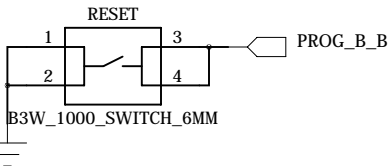
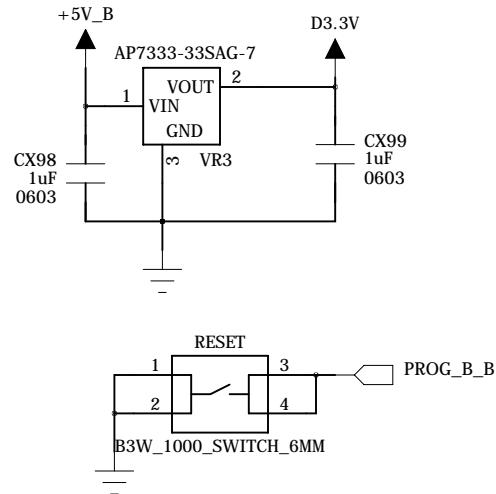
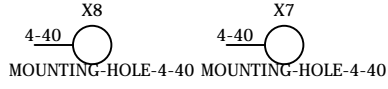
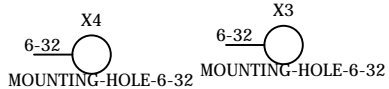
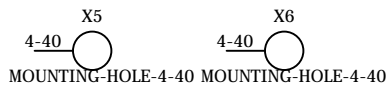
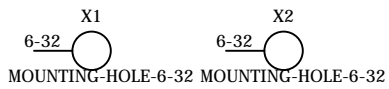
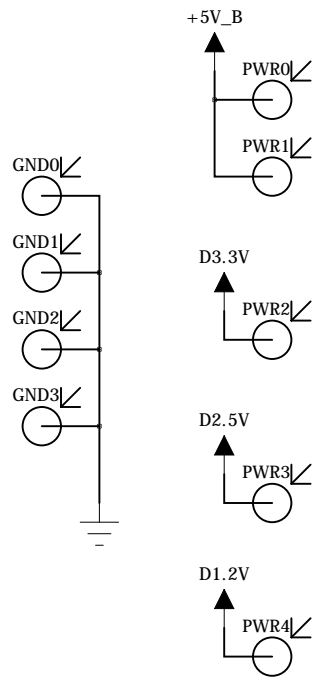
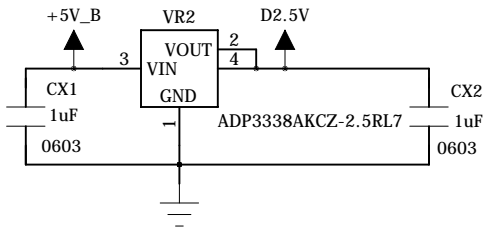
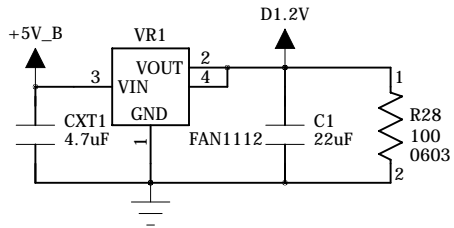
E

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institution: University of Hawaii at Manoa
High Energy Physics Lab
Instrumentation Development Lab

title: Universal_Eval
revision: A
IDLAB design #: IDL_11_012
circuit design: GSV, DMD, RMC
PCB design: DMD

sheet #: 3 of 4
sheet description: Power Block
date last modified: 28 September 2011

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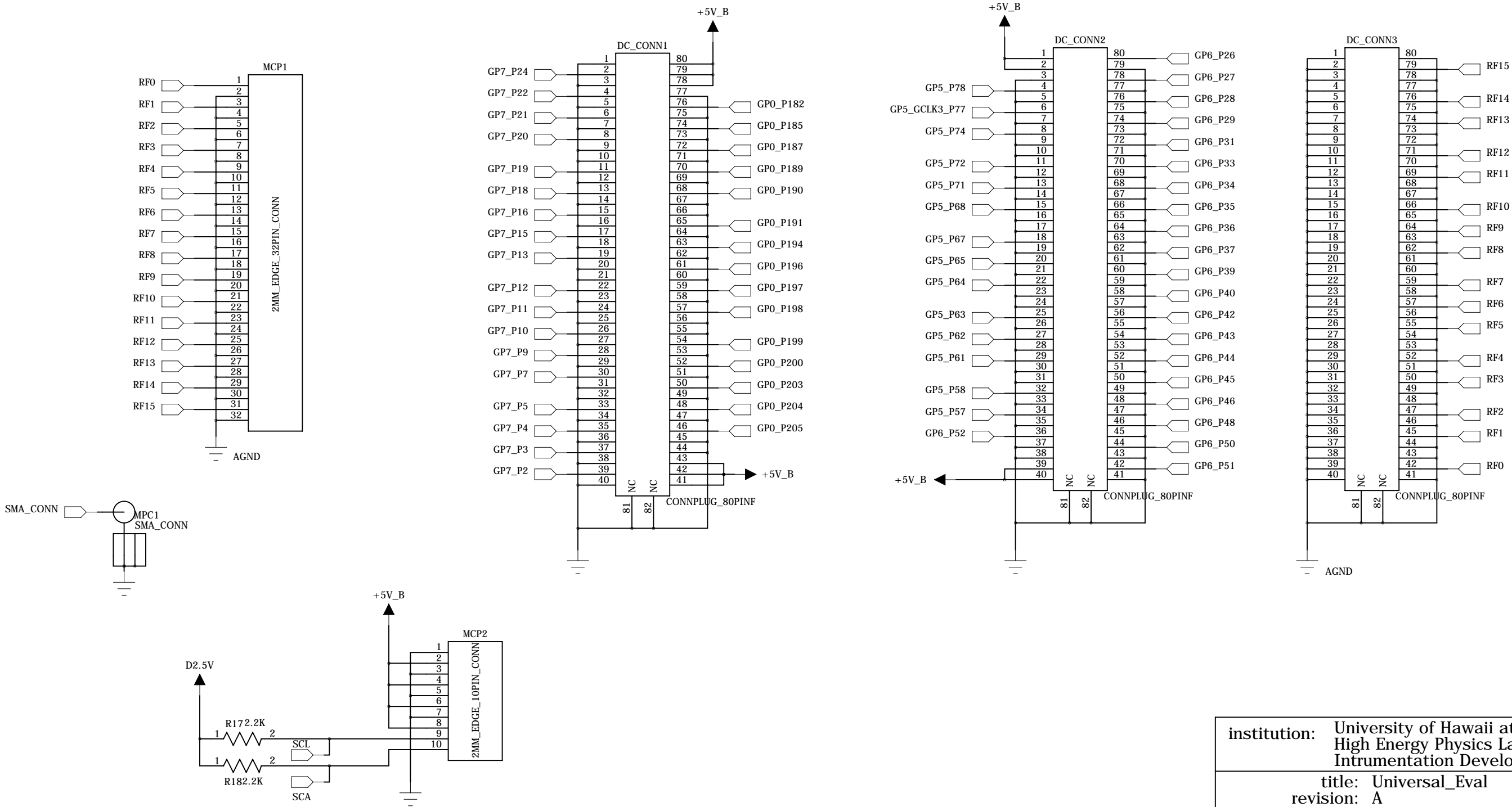
E

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institution:	University of Hawaii at Manoa High Energy Physics Lab Instrumentation Development Lab
title:	Universal_Eval
revision:	A
IDLAB design #:	IDL_11_012
circuit design:	GSV, DMD, RMC
PCB design:	DMD
sheet #:	4 of 4
sheet description:	ASIC DC Input/Output
date last modified:	28 September 2011