

# Massimo Zannoni

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00.00.1900 | Italian



## Working Experience

### Senior Analog Design Engineer

**Sony AVS AG** (formerly Insightness AG)  
*Wiesenstrasse 5, 8952 Schlieren, CH*  
Image Sensors / Computer Vision

*2017 – current*

- ◇ Analog IC design in CMOS technology
  - Event-Based Image Sensor
  - 3D-IC
- ◇ Project leading for IC design
- ◇ Optical and electrical characterization
- ◇ IT and network infrastructure maintenance

### Design Engineer

**GigOptix-Helix AG** (subsidiary of GigPeak, Inc.; now Renesas Electronics Corporation)  
*Seefeldstrasse 45, 8008 Zürich, CH*  
Opto-electronics

*2013 – 2017*

- ◇ High-speed analog IC design in BiCMOS technology
  - 28 Gbit/s TIA receivers and VCSEL drivers
- ◇ RF and optical measurement
- ◇ Customer support, production support and RMA analysis

### Thesis Worker

**ABB AB Corporate Research Center**  
*Forskargränd 7, 722 26 Västerås, Sweden*  
Power Technology Dept., Electric Power System Group

*July 2012 – December 2012*

- ◇ Semiconductor device modeling
- ◇ Power Electronics

## Education

### MSc in Electronic Engineering

**Università degli Studi di Parma, Italy**  
*Faculty of Engineering*

*2009 – 2013*

Analog/Digital IC Design, Semiconductor Physics, Sensor Technology, Power Electronics

- ◇ Thesis: *PSpice Modeling of 4.5 kV StakPak™ Modules*
- ◇ Grade: 107/110

## BSc in Electronic Engineering

Università degli Studi di Parma, Italy

2005 – 2009

Faculty of Engineering

Electronics, Electrotechnics, Information Technology, Controls, PCB Design

- ◊ Thesis: *Experimental Characterization of a Magneto-hydrodynamic Micro-Stirrer*
- ◊ Grade: 108/110

## Skills and Competences

### Languages

**Italian:** Native  
**English:** Fluent  
**German:** Beginner/Intermediate

### Technical and Organizational

- ◊ Analog IC Design
- ◊ 3D-IC
- ◊ Project leading and work coordination
- ◊ Full-custom analog IC layout
- ◊ Electrical and optical measurements and characterization
- ◊ Attitude to solve practical problems where limited standardization exists
- ◊ Data analysis and elaboration
- ◊ Frequently switch between different kinds of task
- ◊ Explain design issues across technical domains

### Computer Tools and Software Development

Operating Systems: Linux: very good knowledge  
Microsoft Windows: good knowledge  
Electronic CAD: Cadence DFII (Virtuoso, Spectre), Mentor TannerTools, Mentor Calibre, Mentor AFS, SPICE, Cadence OrCAD (PSPice)  
Programming: Python, C/C++, Matlab/Octave, shell scripting, VHDL, Verilog/VerilogA, L<sup>A</sup>T<sub>E</sub>X  
Version Control: Git, Subversion, ClioSoft SOS

### Others

- ◊ Good photo-editing and computer drawing skills
- ◊ Hand-working: good familiarity with tools and instruments for building electronic systems; basic skills in the use of main mechanic hand-tools and power-tools

### Personal Interests

- ◊ Photography
- ◊ Love for traveling
- ◊ Archery
- ◊ Love for the mountains (particularly, hiking and MTB)

### Annexes

- ◊ Appendix including patents, publications and references
- ◊ Reference letters (*Arbeitszeugnisse*) by former managers available upon request
- ◊ Personal website: <https://www.fotobill.com>