Massimo Zannoni

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00.00.1900 | Italian | Male



Working Experience

Design Engineer

GigOptix-Helix AG (subsidiary of GigPeak, Inc.) *Seefeldstrasse 45, 8008 Zürich, CH*

Optoelectronics

- ♦ High-speed Analog IC design in BiCMOS technology
 - · 28 Gbit/s TIA receivers and VCSEL drivers
- ♦ RF and optical measurement
- Customer support and RMA analysis
- ♦ Production support
- ♦ IT and network infrastructure maintenance

Thesis Worker

ABB AB Corporate Research Center

Forskargränd 7, 722 26 Västerås, Sweden

Power Technology Dept., Electric Power System Group

- ♦ Semiconductor device modeling
- ♦ Power Electronics

Education

MSc in Electronic Engineering

Università degli Studi di Parma, Italy

Faculty of Engineering

Analog/Digital IC Design, Semiconductor Physics, Sensor Technology, Power Electronics

- ♦ Thesis: *PSpice Modeling of 4.5 kV StakPak*TM *Modules*
- ♦ Grade: 107/110

BSc in Electronic Engineering

Università degli Studi di Parma, Italy

Faculty of Engineering

Electronics, Electrotechnics, Information Technology, Controls, PCB Design

- ♦ Thesis: Experimental Characterization of a Magneto-hydrodynamic Micro-Stirrer
- ♦ Grade: 108/110

2013 - current

July 2012 - December 2012

2009 - 2013

2005 - 2009

Personal Skills and Competences

Languages

Italian: Native English: Fluent

German: Beginner/Intermediate

See Common European Framework (CEF) self-assessment table in the appendix.

Technical Skills and Competences

♦ Analog IC Design

- ♦ Full-custom analog IC layout
- ♦ RF and optical measurements
- ♦ Relate models to physical reality
- Attitude to solve practical problems where limited standardization exists
- ♦ Data analysis and elaboration
- \diamond Frequently switch between different kinds of task
- Clearly describe design issues before organization employees
- Hand-working: good familiarity with tools and instruments for building electronic systems; basic skills in the use of main mechanic hand and power tools

Computer Skills and Competences

Operating Systems: Linux: very good knowledge

Microsoft Windows: good knowledge

Electronic CAD: Cadence DFII (Virtuoso, Spectre), SPICE, Cadence OrCAD (PSpice), Cadence SoC

Encounter, Agilent ADS

Programming: Python, C/C++, Matlab/Octave, shell scripting, VHDL, Verilog/VerilogA, LATEX

Version Control: Subversion, ClioSoft SOS

Organizational and Social Competences

- ♦ 2009–2012: responsible for design and realization of the electrical system of the racing car in the *Formula SAE* (Formula Student) team of the University of Parma
- ♦ 4 years experience as executive in a volleyball sport association
- Occasional collaboration with a cooperative, named Lune Nuove, in Parma, organizing cultural events, as assistant in logistics
- Volleyball player for 12 years, then Ultimate Frisbee player until present day
- ♦ Teaching and organizing experience in a course of basic computer ability

Other Skills and Competences

- Good photo-editing and computer drawing skills
- ♦ Basic sound engineering skills
- ♦ Swiss Driving License: Category A and B

Personal Interests

- ♦ Love for traveling
- Photography
- Guitar player (formerly in an amateur rock band)

Annexes

- Appendix including publications, references and main projects developed during studies
- Reference letter by former manager available upon request