

Massimo Zannoni

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Working Experience

Analog Design Engineer

Insightness AG

2017 – current

Max-Högger-Strasse 6, 8048 Zürich, CH

Image Sensors / Computer Vision

- ◇ Analog IC design in CMOS technology
 - Event-Based Image Sensor
- ◇ Optical and electrical characterization
- ◇ IT and network infrastructure maintenance

Design Engineer

GigOptix-Helix AG (subsidiary of GigPeak, Inc.)

2013 – current

Seefeldstrasse 45, 8008 Zürich, CH

Optoelectronics

- ◇ High-speed Analog IC design in BiCMOS technology
 - 28 Gbit/s TIA receivers and VCSEL drivers
- ◇ RF and optical measurement
- ◇ Customer support, production support and RMA analysis

Thesis Worker

ABB AB Corporate Research Center

July 2012 – December 2012

Forskargränd 7, 722 26 Västerås, Sweden

Power Technology Dept., Electric Power System Group

- ◇ Semiconductor device modeling
- ◇ Power Electronics

Education

MSc in Electronic Engineering

Università degli Studi di Parma, Italy

2009 – 2013

Faculty of Engineering

Analog/Digital IC Design, Semiconductor Physics, Sensor Technology, Power Electronics

- ◇ Thesis: *PSPICE Modeling of 4.5 kV StakPak™ Modules*
- ◇ Grade: 107/110

BSc in Electronic Engineering

Università degli Studi di Parma, Italy

2005 – 2009

Faculty of Engineering

Electronics, Electrotechnics, Information Technology, Controls, PCB Design

- ◇ Thesis: *Experimental Characterization of a Magneto-hydrodynamic Micro-Stirrer*
- ◇ Grade: 108/110

Personal Skills and Competences

Languages

Italian: Native
English: Fluent
German: Beginner/Intermediate

See Common European Framework (CEF) self-assessment table in the appendix.

Technical Skills and Competences

- ◊ Analog IC Design
- ◊ Full-custom analog IC layout
- ◊ RF and optical measurements
- ◊ Relate models to physical reality
- ◊ Attitude to solve practical problems where limited standardization exists
- ◊ Data analysis and elaboration
- ◊ Frequently switch between different kinds of task
- ◊ Clearly describe design issues before organization employees
- ◊ Hand-working: good familiarity with tools and instruments for building electronic systems; basic skills in the use of main mechanic hand and power tools

Computer Skills and Competences

Operating Systems: Linux: very good knowledge
Microsoft Windows: good knowledge

Electronic CAD: Cadence DFII (Virtuoso, Spectre), SPICE, Cadence OrCAD (PSpice), Cadence SoC Encounter, Agilent ADS

Programming: Python, C/C++, Matlab/Octave, shell scripting, VHDL, Verilog/VerilogA, L^AT_EX

Version Control: Subversion, ClioSoft SOS

Organizational and Social Competences

- ◊ 2009–2012: responsible for design and realization of the electrical system of the racing car in the *Formula SAE* (Formula Student) team of the University of Parma
- ◊ 4 years experience as executive in a volleyball sport association
- ◊ Teaching and organizing experience in a course of basic computer ability

Other Skills and Competences

- ◊ Good photo-editing and computer drawing skills
- ◊ Basic sound engineering skills
- ◊ Swiss Driving License: Category A and B

Personal Interests

- ◊ Love for traveling
- ◊ Photography
- ◊ Ultimate Frisbee
- ◊ Guitar player (formerly in an amateur rock band)

Annexes

- ◊ Appendix including publications, references and main projects developed during studies
- ◊ Reference letter by former manager available upon request
- ◊ Personal website: <https://www.fotobill.com>