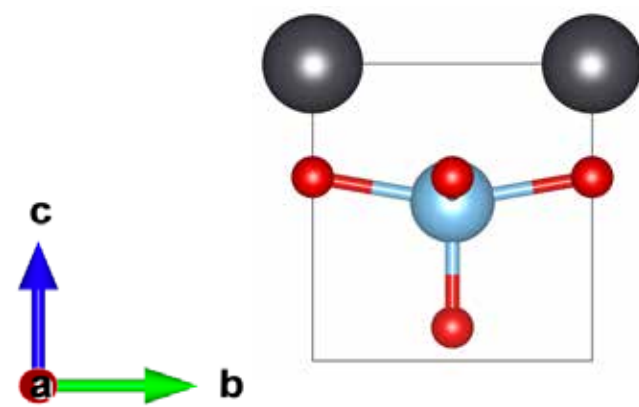
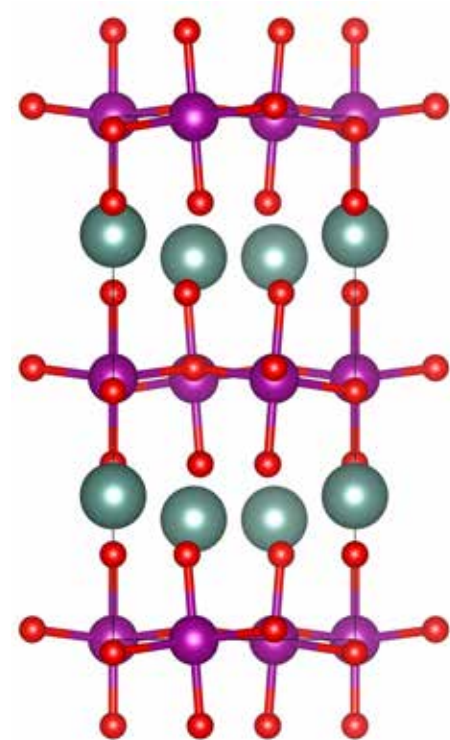


Input Structures

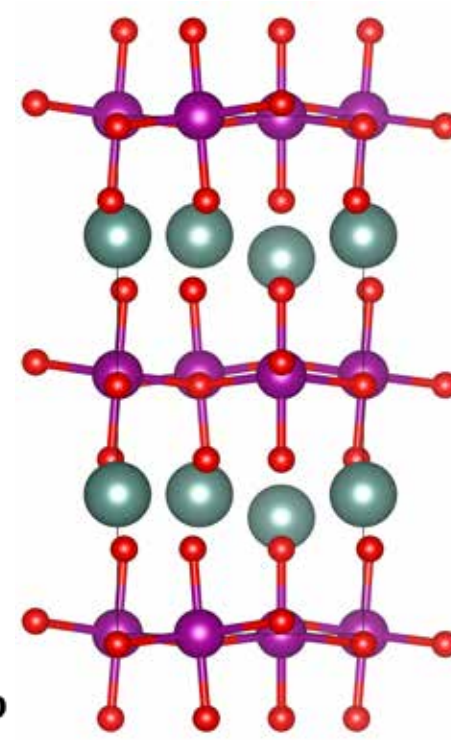
(a) P4mm



(d) am

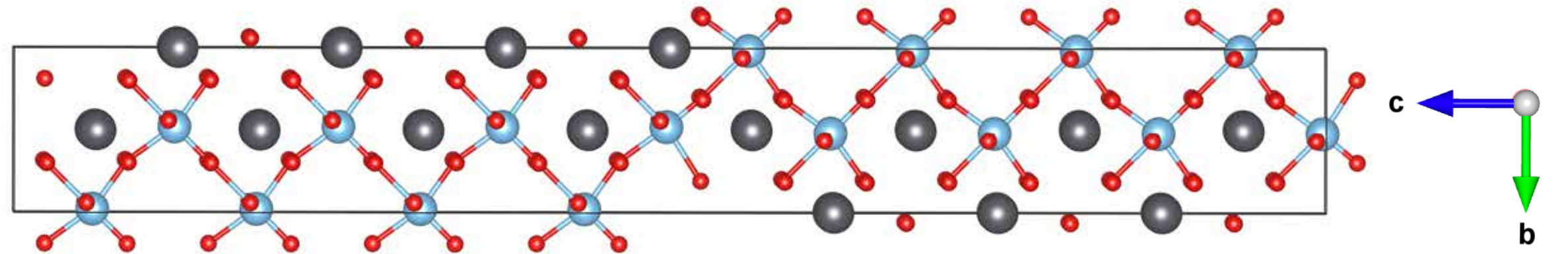


(e) bp

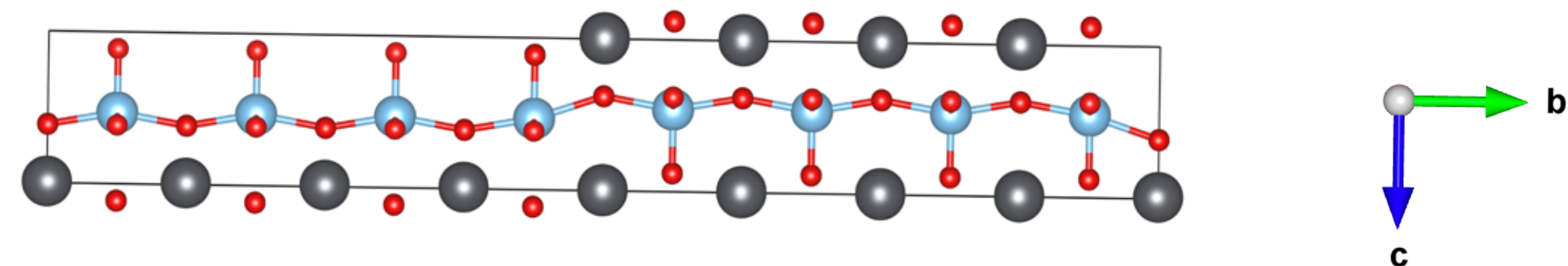


Output structures

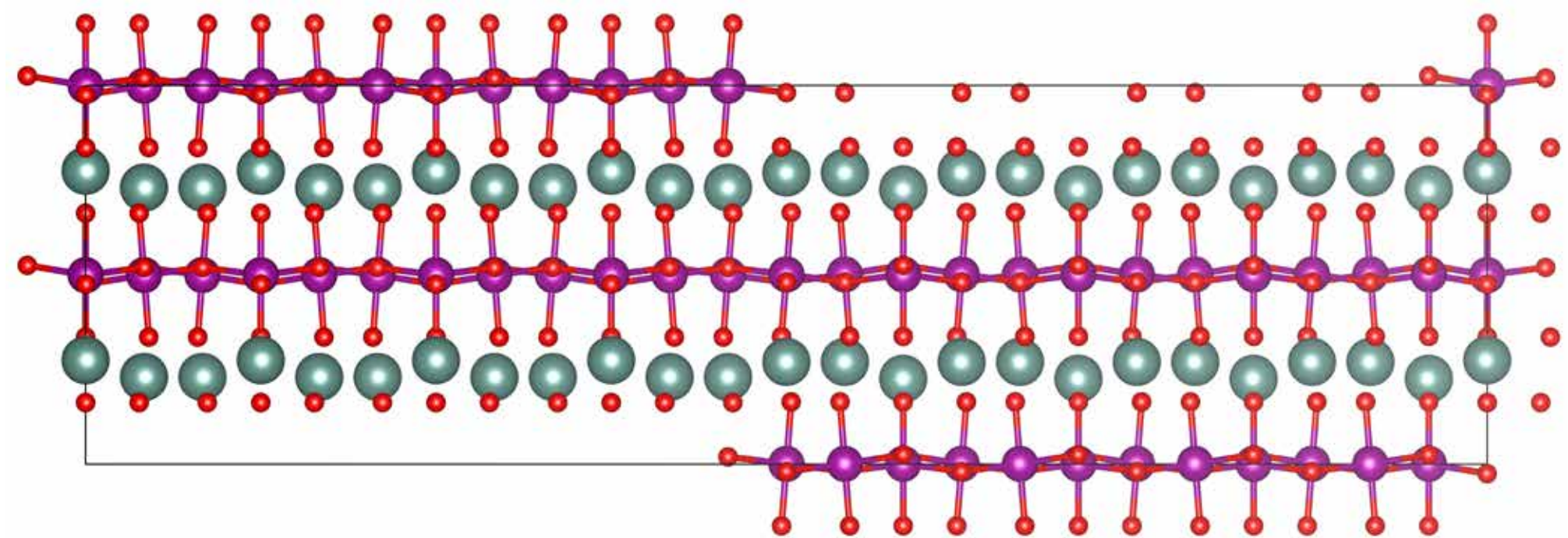
(b) T90



(c) T180



(f) Neutral domain wall



(g) Charged domain wall

