MŽOURACK — QUAAD is a 22HP module which consists of four independent 4-step sequencers. Each of them features a CV output (0-5V), a clock divider and a pattern setting. Patterns can be changed per-sequence via a dedicated knob or via CV, aswell as globally using the MSTR PTRN knob.

The module is designed with live-performance setting in mind, allowing for rapid changes to the behaviours of the outputs.

On the right is the recommended starting point for using this module for the first time.





- **A)** 4 knobs per channel for setting the output-voltage of the sequence per-step. Output voltage is unquantized, 0-5V.
- **B)** Clock dividers per-channel set how many clock signal pulses are needed to progress the sequence. Available divisions are 1-2-3-4-5-7-8-12-16-32.
- **C)** Here the pattern of each channel can be selected manually.
- **D)** CV inputs for individually changing the pattern of each channel
- **E)** Global pattern change adds an identical CV offset to each input in **D)**
- **C)+D)+E)** There are 6 patterns available these change the order of steps through which the sequence progresses. The patterns are listed below in the order in which they accessed with **C)+D)+E)**:

All are unique permutations, so that in a repeating sequence, the same pattern cannot be extracted from any pair of the available patterns.

The ranges of **E**) and **C**) are designed so that one can use either of these controls to scroll through all the available patterns. All patterns are also available with 5V wide CV range used in input **D**). Negative CV inputs are also useable, and scanning for patterns outside of the first 6 listed above will result in looping to the other end of the selection.

Self-patching to modulate the pattern of one sequence with another is advised!

- **F)** 0-5V CV output per channel.
- **G)** Clock divider output per channel. Gate length is equal to that of the incoming clock signal.
- **H)** Clock input, output, and internal clock generator tempo setting. This knob does nothing when external clock is used. Experimenting with non-steady clock signals is advised!
- I) Reset input + and manual reset. Sets all channels to the first pattern step on the next clock tick.