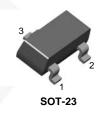


November 2014

MMBD4148 / MMBD4148SE / MMBD4148CC / MMBD4148CA Small Signal Diode



4148 4148SE 3 4148CC 4148CC 3 4148CA 4148CA

Ordering Information

Part Number	Top Mark	Package	Packing Method
MMBD4148	5H	SOT-23 3L	Tape and Reel
MMBD4148_D87Z	5H	SOT-23 3L	Tape and Reel
MMBD4148SE	D4	SOT-23 3L	Tape and Reel
MMBD4148CC	D5	SOT-23 3L	Tape and Reel
MMBD4148CA	D6	SOT-23 3L	Tape and Reel

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at $T_A = 25^{\circ}\text{C}$ unless otherwise noted.

Symbol	Parameter		Value	Unit
V _{RRM}	Maximum Repetitive Reverse Voltage		100	V
I _{F(AV)}	Average Rectified Forward Current		200	mA
	Non-Repetitive Peak Forward Surge Current	Pulse Width = 1.0 second	1.0	А
		Pulse Width = 1.0 microsecond	2.0	
T _{STG}	Storage Temperature Range		-55 to +150	°C
TJ	Operating Junction Temperature		150	°C

Thermal Characteristics

Values are at $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Value	Unit
P_{D}	Power Dissipation	350	mW
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	357	°C/W

Electrical Characteristics

Values are at $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Max.	Unit
V _R	Breakdown Voltage	$I_R = 5.0 \mu\text{A}$	75		V
		I _R = 100 μA	100		
V _F	Forward Voltage	I _F = 10 mA		1.0	V
I _R	Reverse Leakage Current	V _R = 20 V		25	nA
		V _R = 20 V, T _A = 150°C		50	μΑ
		V _R = 75 V		5.0	μΑ
C_{T}	Total Capacitance	$V_R = 0 \text{ V, f} = 1.0 \text{ MHz}$		4.0	pF
t _{rr}	Reverse Recovery Time	$I_F = 10 \text{ mA}, V_R = 6.0 \text{ V},$ $I_{RR} = 1.0 \text{ mA}, R_L = 100 \Omega$		4.0	ns

Typical Performance Characteristics

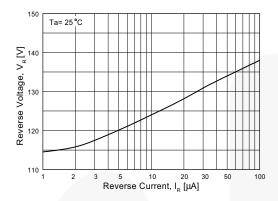


Figure 1. Reverse Voltage vs. Reverse Current BV - 1.0 to 100 μA

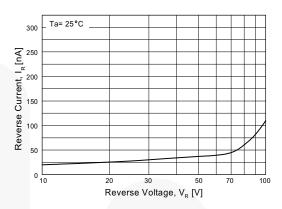


Figure 2. Reverse Current vs. Reverse Voltage IR - 10 to 100 V

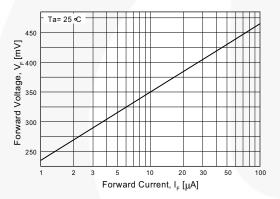


Figure 3. Forward Voltage vs. Forward Current VF - 1.0 to 100 μA

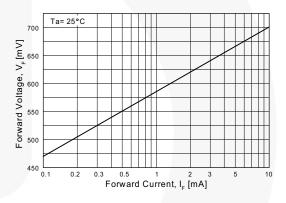


Figure 4. Forward Voltage vs. Forward Current VF - 0.1 to 10 mA

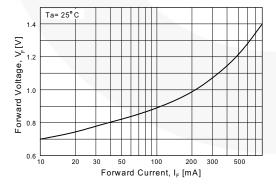


Figure 5. Forward Voltage vs. Forward Current VF - 10 to 800 mA

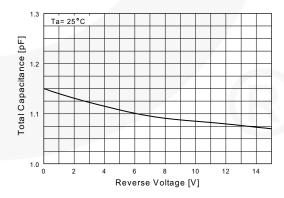


Figure 6. Total Capacitance vs. Reverse Voltage

Typical Performance Characteristics (Continued)

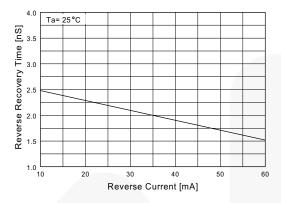


Figure 7. Reverse Recovery Time vs. Reverse Current TRR - IR 10 mA to 60 mA

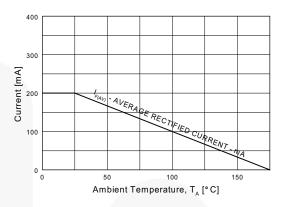


Figure 8. Average Rectified Current $(I_{F(AV)})$ vs. Ambient Temperature (T_A)

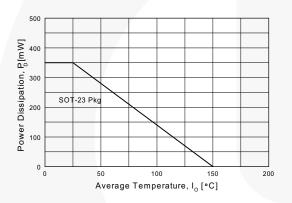
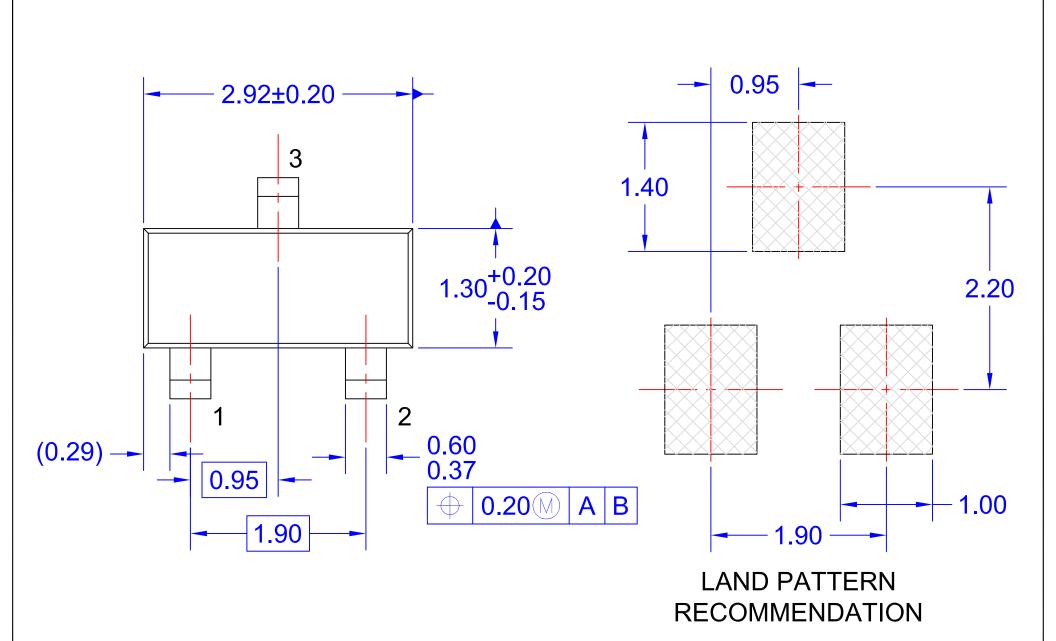
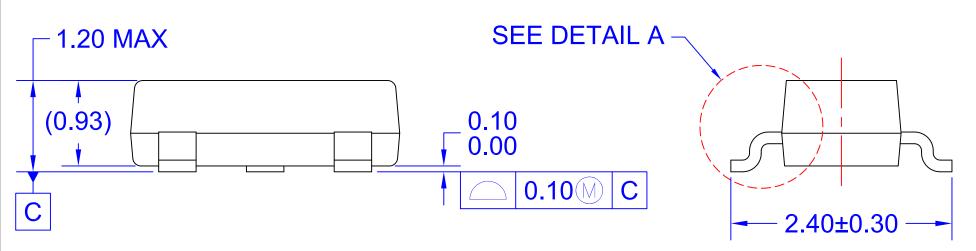
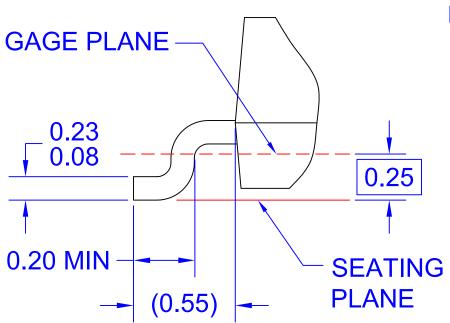


Figure 9. Power Derating Curve







DETAIL A
SCALE: 2X

NOTES: UNLESS OTHERWISE SPECIFIED

- A) REFERENCE JEDEC REGISTRATION TO-236, VARIATION AB, ISSUE H.
- B) ALL DIMENSIONS ARE IN MILLIMETERS.
- C) DIMENSIONS ARE INCLUSIVE OF BURRS, MOLD FLASH AND TIE BAR EXTRUSIONS.
- D) DIMENSIONING AND TOLERANCING PER ASME Y14.5M 2009.
- E) DRAWING FILE NAME: MA03DREV11







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Definition of Terms

Definition of Terms			
Datasheet Identification	Product Status	Definition	
Advance Information	Formative / In Design	Datasheet contains the design specifications for product development. Specifications may change in any manner without notice.	
Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.	
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