

# Michael Zuzovski

052-7564010 • giga123@gmail.com • Rishon Le-Zion, Israel

---

## Summary & Personal Information

- B.Sc. Physics, B.Sc. Electrical Engineering
- Experience in Chip Design (CPU Sub-system)
- Team building and leadership
- Algorithms, Simulation & Programming
- Research and analysis
- I'm always looking to learn and participate in new fields

**Languages:** Hebrew, Russian (Mother tongues), English (Fluent), Chinese (HSK 3)

---

## Education

- **B.Sc. Physics, Tel-Aviv University**  
Graduated 2015. Grades Average: 95.26, with highest distinction.  
Specialization in Electronic Devices (Solid State) and Computers (Networks, OS'es, Algorithms).
  - **B.Sc. Electrical Engineering, Tel-Aviv University**  
Graduated 2015. Grades Average: 92.46, with distinction.
- 

## Work History

- **Chip Designer** **Amazon (Annapurna Labs – AWS)** **04/18 – Now**  
Chip designer in the CPU design team. My responsibilities included taking complex modules from the initial stages of architecture and specification stage, through implementation from scratch and integration with other designs, to verification and finally successful synthesis.  
During the work, I participated heavily in CV verification (end-to-end verification of the system using real world code on an emulator), UVM verification, and in support the backend team to deliver the best product possible. During the work, I also obtained experience and knowledge in the ARM platform, including the protocols (AXI, CHI, etc.) and the ARM cores themselves, including the interaction between SW and HW.
  - **Research Assistant** **Tel-Aviv University (Dr. Amir Natan)** **10/13-1/16, 9/17-03/18**  
My responsibility was to analyze, prototype, and implement novel algorithms in the field of Quantum Chemistry, specifically Density Functional Theory. I worked on a code called PARSEC, a product of an international collaboration of research in the field of Quantum Chemistry. An example of my work is the 'Non-Uniform Grid' quick Poisson solver, which accelerated the calculation of the electronic potential given an array of sources (See (1)). The implementation was built from the ground up.  
The work was done both alone and with researched both part of my research ground & outside of it.
  - **Army service** **IDF, Air force** **1/16-7/17**  
Programming, Signal Analysis, Conducting field experiments, and using physical principles & computer modeling for problem solving.
- 

## Additional Skills

- **Programming languages & Knowledge**  
Solid knowledge and experience with Python, C, C# and Fortran.  
Good basis on ASM (ARM, x86). I am also experienced with tcl, OOP and scripting (shell).  
Good knowledge on Linux (Drivers, Processes and context switching, Boot process, etc.).  
Experience in writing drivers, HAL, and HW tests in C.  
Knowledgeable and experienced with using & implementing the correct data structures, algorithms, and with the general algorithm design flow.
  - **Simulation (Both physics simulation and HW simulation)**  
**Physics:** Experience in numerical analysis, numerous types of algorithms and their implementation (Real space, reciprocal space, function space, different basis sets, etc).  
**HW:** Experience in functional, end-to-end system tests in C code of emulators, some experience in UVM and writing testbenches.
- 

## • **Publications**

(1) "An auxiliary grid method for the calculation of electrostatic terms in density functional theory on a real-space grid", M. Zuzovski, A. Boad, A. Natan, Tel-Aviv University, 2015, in Physical Chemistry Chemical Physics.

## • **References**

Dr. Amir Natan, Phone: 03-640-8635, 052-434-3222. Email: amirnatan@post.tau.ac.il