

## Lab 1 Marking Guide

	Student Name	ID Number	Group No.
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<b>Abstract</b>		<b>5</b>
Brief and summarizes the results of the lab		1
Mention the voltage regulation number for the positive rail		1
Mention the voltage regulation number for the negative rail		1
Mention the ripple voltage number for the positive rail		1
Mention the ripple voltage number for the negative rail		1
<b>Objectives</b>		<b>5</b>
Mention $\pm 10V$ and $\pm 9V$ DC 25 mA supply		1
Mention $\pm 5\%$ voltage regulation		2
Mention voltage ripple $< 0.5\%$		2
<b>Design</b>		<b>20</b>
Explain how and why the design was split up (rectifier, filter and regulator stages)		5
Explain how testing must occur for each stage (1k resistor for filter stage, no load, full load, etc.)		3
Explain and justify why the rectifier design was chosen		2
Explain and justify why the filter design was chosen (LPF)		2
How do the simulation and results from earlier inform later design work? (Picking capacitor amounts, picking $R_{Ballast}$ , etc.)		5
Explain the Zener diode design including $R_{Ballast}$ calculations		3
<b>Simulation for <math>\pm 10 V</math> output (Individual)</b>		<b>10</b>
Simulation for the transformer showing the results for both sides of the transformer		1
Simulation for the Rectifier - positive rail		1
Simulation for the Rectifier - negative rail		1
Simulation for the filters - positive rail (should be loaded with $1 k\Omega$ )		1
Simulation for the filters - negative rail (should be loaded with $1 k\Omega$ )		1
Simulation for the positive rail regulator - no load voltage		1
Simulation for the positive rail regulator - full load voltage (should be loaded with $400 \Omega$ )		1
Simulation for the negative rail regulator - no load voltage		1
Simulation for the negative rail regulator - full load voltage (should be loaded with $400 \Omega$ )		1
Simulation showing the voltage ripple of the both rails (should be loaded with $400 \Omega$ )		1
<b>Simulation for <math>\pm 9 V</math> output (Individual)</b>		<b>10</b>
Simulation for the transformer showing the results for both sides of the transformer		1
Simulation for the Rectifier - positive rail		1
Simulation for the Rectifier - negative rail		1
Simulation for the filters - positive rail (should be loaded with $1 k\Omega$ )		1
Simulation for the filters - negative rail (should be loaded with $1 k\Omega$ )		1
Simulation for the positive rail regulator - no load voltage		1

Simulation for the positive rail regulator - full load voltage (should be loaded with 400 $\Omega$ )		1
Simulation for the negative rail regulator - no load voltage		1
Simulation for the negative rail regulator - full load voltage (should be loaded with 400 $\Omega$ )		1
Simulation showing the voltage ripple of the both rails (should be loaded with 400 $\Omega$ )		1
<b>Results</b>		<b>10</b>
Comparison of design, simulation, and results; discrepancies discussed/explained; shows judgement/evaluation of design performance		4
Voltage regulation calculations for both simulations		2
Voltage ripple for both the positive and negative rails for both simulations		2
All schematics should be neat, labeled and correct		2
<b>Discussions</b>		<b>10</b>
1. What type of filter was used (BPF, BSF, LPF, or HPF)? Why is this the appropriate choice for this application?		2
2. Was additional filtering required, how will the filter need to be adjusted?		1
3. What are the inefficiencies of the power supply? What would be better design solutions?		2
4. Is there a difference between calculations and simulations? Why is there a difference?		3
5. Were there any other difficulties you faced? What steps could you do to address these issues (even if you didn't actually do them or could not do them)		2
<b>Conclusions</b> (properly summarizes results and findings)		<b>5</b>
<b>Figures</b> (figures are labeled, of good quality and readable)		<b>5</b>
<b>Lab Report Total</b>		<b>80</b>
Lab 1a check in		5
Lab 1a check out		5
Lab 1b check in		5
Lab 1b check out		5
<b>Lab Mark Total</b>		<b>20</b>
<b>Lab 1 Total</b>		<b>100</b>

Marked by: \_\_\_\_\_ Signature: \_\_\_\_\_ Date: \_\_\_\_\_