

root  
: Root

system  
: System

system\_port

membus  
: SystemXBar

master

slave

mem\_ctrls  
: DDR3\_1600\_8x8

port

l2  
: L2Cache

mem\_side

cpu\_side

icache\_port

dcache\_port

cpu  
: MinorCPU

itb  
: ArmITB

walker  
: ArmTableWalker

port

dtb  
: ArmDTB

walker  
: ArmTableWalker

port

icache  
: L1\_ICache

cpu\_side

mem\_side

dcache  
: L1\_DCache

cpu\_side

mem\_side

tol2bus  
: L2XBar

master

slave