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MBSE Implement

ASN.1 and AADL Generators Software User Manual

MBSE-N7S-UM-19003 rev. 1.5

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Change Record

Issue	Date	Changes
1.0	2019-06-19	Initial release
1.1	2019-09-12	Updated information relevant to Expression handling
		Updated information relevant to AADL generation
		Updated general installation information
1.2	2019-09-18	Updated plugin version
		Updated error messages
1.3	2019-09-20	Added best practices relevant to AADL generation
		Added Capella features supported by AADL generator
1.4	2020-01-31	Updated with information relevant to plugin version 1.3.4 compatible
		with Kazoo
1.5	2020-02-18	Added information regarding GUI merging
		Updated information regarding Capella version compatibility



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1. Introduction

1.1. Scope

This document constitutes the software user manual for the following software deliverables from "MBSE Implement" project:

- ASN.1 Generator;
- AADL Generator.

The reader should be familiar with:

- Capella and Arcadia method [RD1];
- TASTE and ASN.1 [RD2].

The following introduction provides a short description of the project objectives.

1.2. Project objectives

One of the major objectives of the project is the development of ASN.1 and AADL Generators capable of converting data and architecture models created in Capella into models compatible with TASTE.

Capella [RD1] is an open-source modelling environment for Model Based Systems Engineering (MBSE), originally created in 2007 by Thales and then released in 2015 as an Eclipse project by PolarSys. It supports the Arcadia modelling method, also designed by Thales. The method guides the user through the following "working levels", each of which is supported by Capella:

- Operational Analysis;
- Analysis of the System Needs;
- Logical Architecture;
- Physical Architecture;
- End Product Breakdown Structure and Integration Contracts.

At each working level, the model can be visualized and manipulated through a variety of diagrams, including:

- breakdown diagrams;
- interaction diagrams;
- scenario diagrams;
- role diagrams;
- architecture diagrams;
- class diagrams,
- mode and state machine diagrams.

The important feature of Capella is that although different diagrams provide different views, within each project there is a single, coherent underlying model.

TASTE [RD2] is a model-driven tool-chain targeting heterogeneous, embedded systems. It was created in 2008 under the initiative of the European Space Agency and consists of various tools facilitating creation of systems using formal models and automatic code generation. The key technologies involved are AADL for architecture definition, ASN.1 for data modelling and SDL for behaviour specification. Supplementary languages and technologies include, but are not limited to:



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- Simulink;
- SCADE;
- Ada;
- C/C++;
- Python;
- CHEDDAR;
- MAST;
- MSC.

The basic TASTE workflow includes:

- definition of data models using ASN.1;
- definition of a logical architecture using an Interface View description in AADL;
- definition of a physical architecture using a Deployment View description in AADL;
- definition of behaviour using various technologies, such as SDL.

ASN.1 and AADL Generators developed within the scope of this project aim to translate the relevant part of the Capella model (which can be expressed via class diagrams and various architecture diagrams) into data and architecture models understood by TASTE. It must be noted that the ASN.1 dialect supported by TASTE is a subset of the ASN.1 standard. Additionally, in order to properly interact with the user via a GUI, TASTE introduces additional properties to the supported AADL dialect.

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2. Applicable and reference documents

2.1. Applicable documents

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2.2. Applicable standards

ID Title Reference Rev.	
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2.3. Reference documents

ID	Title	Reference	Rev.
RD1	Capella Wiki	https://wiki.polarsys.org/Capella	N/A
RD2	TASTE community page	https://taste.tuxfamily.org	N/A
RD3	Capella downloads page	https://www.polarsys.org/capella/down load.html	N/A



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3. Terms, definitions and abbreviated terms

This document extra specific acronyms and abbreviations are listed here under.

AADL Architecture Analysis and Design Language

ASN1SCC ESA's ASN.1 compiler

ASN.1 Abstract Syntax Notation One, dialect supported by ASN1SCC



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4. Installation

4.1. General

Both ASN.1 and AADL Generators are delivered as a single Capella plugin – Capella-TASTE Plugin. Capella is provided for Windows, Linux and Mac operating systems. The technical choices made during the plugin development did not introduce any additional platform dependencies and consequently the plugin should work on the same platforms as Capella. However, according to Capella download page [RD3], both Linux and Mac versions of Capella have not been tested. Consequently the plugin is developed and extensively tested only on Windows platform. However, due to the dependency of TASTE on Linux, some plugin tests have been also performed on Linux platform. This document contains screens from both Windows and Linux Capella versions.

4.2. Prerequisites

Table 1 lists all Capella-TASTE Plugin prerequisites.

Table 1 - Capella-TASTE Plugin prerequisites

Item	Version	Description
Capella	1.3.1	Eclipse based IDE that hosts the plugin

Default Capella version 1.3.0 installation contains all the required Java dependencies. The required Java bundles are as follows:

- org.eclipse.ui;
- org.eclipse.core.runtime;
- org.polarsys.capella.core.platform.sirius.ui.navigator;
- org.junit;
- org.eclipse.sirius.diagram.ui;
- org.eclipse.gef;
- org.eclipse.gmf.runtime.diagram.ui.

The required Java packages are as follows:

- org.eclipse.core.resources;
- org.eclipse.emf.common;
- org.eclipse.emf.common.util;
- org.eclipse.emf.ecore;
- org.eclipse.emf.ecore.resource;
- org.polarsys.capella.common.data.helpers.modellingcore;
- org.polarsys.capella.common.data.helpers.modellingcore.utils;
- org.polarsys.capella.common.data.modellingcore;
- org.polarsys.capella.common.data.modellingcore.impl;
- org.polarsys.capella.common.data.modellingcore.provider;
- org.polarsys.capella.common.data.modellingcore.util;
- org.polarsys.capella.core.data.capellacore;
- org.polarsys.capella.core.data.capellacore.util;
- org.polarsys.capella.core.data.information;
- org.polarsys.capella.core.data.information.datatype;
- org.polarsys.capella.core.data.information.datatype.util;
- org.polarsys.capella.core.data.information.datavalue;
- org.polarsys.capella.core.data.information.datavalue.util.



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4.3. Installation procedure

Capella-TASTE Plugin is delivered as a single Jar file that should be placed inside the "plugins" subdirectory of the Capella installation directory. E.g. if the path to eclipse.exe of the Capella installation is

"C:\Capella\eclipse\eclipse.exe",

then the plugin should be placed inside

"C:\Capella\eclipse\plugins\"

directory.

It must be ensured that only a single version of the Capella-TASTE plugin is present within the plugins directory. The Jar file is named as follows:

capellatasteplugin.\$CAPELLA_MAJOR_VERSION.\$CAPELLA_MINOR_VERSION.\$PLUGIN_VERSION.\$BUILD_ID.jar

\$CAPELLA MAJOR VERSION indicates the major version of the target host IDE. Should be "1".

\$CAPELLA MINOR VERSION indicates the minor version of the target host IDE. Should be "3".

\$PLUGIN_VERSION indicates Capella-TASTE plugin version. This manual refers to version "4".

\$BUILD ID indicates build ID.

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5. Configuration

Capella-TASTE Plugin adds "TASTE" preferences page (presented in Figure 1), which is accessible at the standard preferences location, under Window->Preferences.

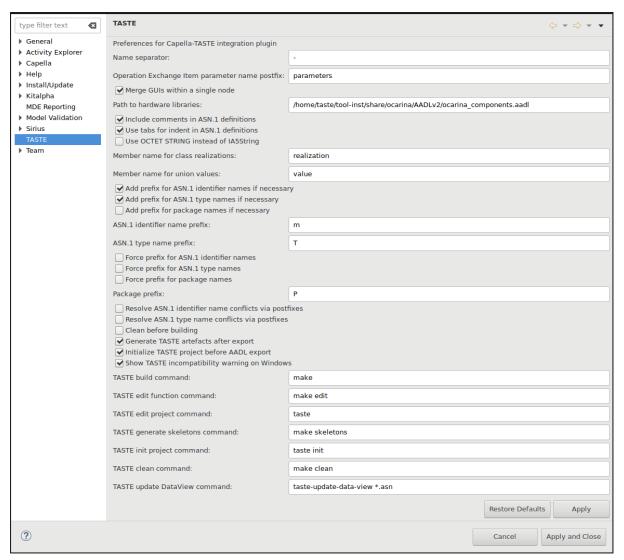


Figure 1 - Plugin preferences page

The preferences page contains the following configuration options:

- Name separator:
 Separator used for concatenating parts into the resulting names. E.g.:

 FirstNamePart_SecondNamePart
- Operation Exchange Items parameter name postfix:
 A single Exchange Item with exchange mechanism set to Operation may have subitems assigned to different categories, such as IN, OUT, INOUT, RETURN or EXCEPTION. Such Exchange Items are translated into several ASN.1 sequences, one for each category, as well as an additional ASN.1 sequence that contains one member for each of these categories. E.g.:

```
OperationExchangeItem-IN ::= SEQUENCE {
     element SomeType
}
```



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```
OperationExchangeItem ::= SEQUENCE {
     iN-<u>Parameters</u> OperationExchangeItem-IN
}
```

• Merge GUIs within a single node

Whether or not to merge all GUIs within a single node into a single master GUI. The merge will not be attempted if it would lead to change in the number of interfaces.

• Path to hardware libraries:

Path to AADL definitions of available components.

• *Include comments in ASN.1 definitions*:

Transfer Capella element summaries and descriptions into ASN.1 comments of the relevant ASN.1 data types.

• Use tabs for indent in ASN.1 definitions:

Indent ASN.1 definitions using tabs instead of spaces.

• *Member name for class realizations*:

ASN.1 sequence member name used for class realizations. E.g.:

```
VehicleType ::= SEQUENCE {
    realization CHOICE {
        descendant1 CarType,
        descendant2 SpacecraftType
    }
}
```

Member name for union values:

ASN.1 sequence member name used for union realizations. E.g.:

```
SomeUnionType ::= SEQUENCE {
    value CHOICE {
    variant1 SomeType,
    variant2 SomeOtherType
  }
}
```

• ASN.1 identifier name prefix:

Optional prefix which may be appended to ASN.1 identifier names. E.g.:

```
SomeStruct ::= SEQUENCE {
    <u>m</u>MemberIdentifier1 SomeType,
    <u>m</u>MemberIdentifier2 SomeOtherType
}
```

• *ASN.1 type name prefix*:

Optional prefix which may be appended to ASN.1 type names. E.g.:

```
<u>T</u>SomeStruct ::= SEQUENCE {
    memberIdentifier1 <u>T</u>SomeType,
    memberIdentifier2 <u>T</u>SomeOtherType
}
```

• Package prefix:

Optional prefix which may be appended to package names. E.g.: <u>PExampleDataPackage DEFINITIONS AUTOMATIC TAGS</u> ::= BEGIN

• Add prefix for ASN.1 identifier names if necessary:

Append the selected prefix to an ASN.1 identifier name if the given name conflicts with a reserved word.



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• Add prefix for ASN.1 type names if necessary:

Append the selected prefix to an ASN.1 type name if the given name conflicts with a reserved word.

• Add prefix for package names if necessary:

Append the selected prefix to a package name if the given name conflicts with a reserved word.

• Force prefix for ASN.1 identifier names:

Append the selected prefix to all identifier names.

• Force prefix for ASN.1 type names:

Append the selected prefix to all type names.

• Force prefix for package names:

Append the selected prefix to all package names.

• Resolve ASN.1 identifier name conflicts via postfixes:

If there is a naming conflict between 2 or more identifier names, resolve it by adding postfixes.

• Resolve ASN.1 type name conflicts via postfixes:

If there is a naming conflict between 2 or more type names, resolve it by adding postfixes.

• Generate TASTE artefacts after export:

Automatically run the update DataView and generate code skeletons commands after a physical architecture export.

• Show TASTE incompatibility warning on Windows:

Show warning regarding TASTE incompatibility with Windows when a Linux only command is executed on a platform detected as Microsoft Windows.

• *TASTE build command:*

Command to be invoked to build a TASTE project.

• TASTE clean command:

Command to be invoked to clean a TASTE project.

• *TASTE init project command:*

Command to be invoked to initialize a TASTE project.

• TASTE edit project command:

Command to be invoked to edit a TASTE project.

• *TASTE edit function command;:*

Command to be invoked to edit a TASTE function code.

• *TASTE generate skeletons command:*

Command to be invoked to generate code skeletons for a TASTE project.

• *TASTE update DavaView command:*

Command to be invoked to update a TASTE project's DataView.aadl file.

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6. Usage

6.1. Data Model

Capella-TASTE Plugin provides a new command - "Export to TASTE" - which can be used to export the selected Capella data model to ASN.1. It is available both in a dedicated menu in the menu bar (as illustrated in Figure 2) and in the context menu for the Capella Project Explorer (as illustrated in Figure 3).



Figure 2 - Export to TASTE command in the menu bar

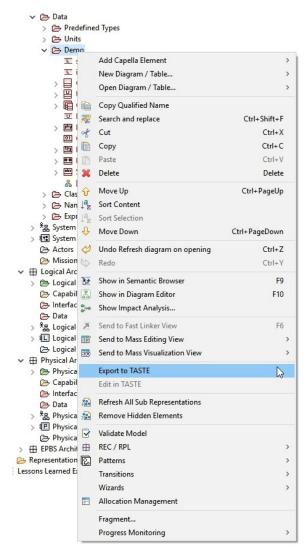


Figure 3 - Export to TASTE command in the Capella Project Explorer



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For the purpose of demonstration, a simple two-package data model (illustrated in Figure 4) will be used.

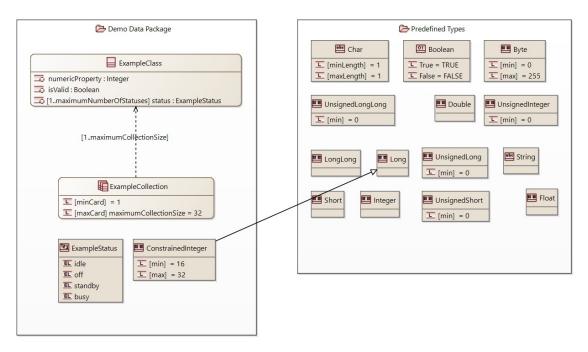


Figure 4 - Sample data model

In order to export a data model, the user should select (e.g. in the Capella Project Explorer) the data packages to be exported and then invoke the "Export to TASTE" command. If correct selection was made, then the plugin will convert the selected subset of the Capella's data model into its own internal representation. If any issue is detected during this process, then the user is notified through a dedicated window listing all detected issues together with their severities (as illustrated in Figure 5).

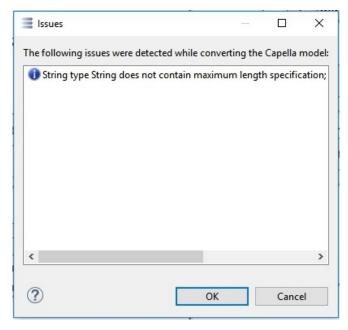


Figure 5 - Window presenting the detected issues

If the user acknowledges the listed issues, or no issues are found, then the plugin will present the list of data types and values ready for export, grouped by packages (as illustrated in Figure 6).

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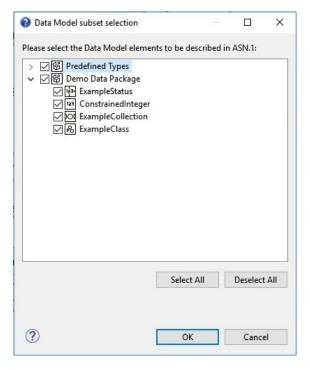


Figure 6 - Data model subset selection window

The user may adjust the initial selection, e.g. by disabling the conversion of some data types or values. After the data model subset selection is confirmed, the user is asked to select the output directory for the ASN.1 model. Upon output directory selection, the data model is exported to ASN.1 files, one for each of the selected data packages (as illustrated in Figure 7 and Figure 8).



Figure 7 – Example of exported ASN.1 files

```
Demo-Data-Package DEFINITIONS AUTOMATIC TAGS ::= BEGIN
IMPORTS
   TBoolean.
   TLong,
   TInteger
       FROM Predefined-Types
ExampleStatus ::= ENUMERATED {
   idle
   off,
   standby
   busy
ConstrainedInteger ::= TLong(16..32)
ExampleClass ::= SEQUENCE {
   numericProperty TInteger,
   isValid TBoolean
   status SEQUENCE(SIZE(1..8)) OF ExampleStatus
END
```

Figure 8 - ASN.1 generated from the example data package



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6.2. Architecture

Capella-TASTE Plugin provides a new command – "Export to TASTE" – which can be used to export the selected Capella Physical Architecture model to AADL. It is available both in a dedicated menu in the menu bar (as illustrated in Figure 2) and in the context menu for the Capella Project Explorer (as illustrated in Figure 9). It must be noted that Physical Architecture needs to be selected in the Capella Project Explorer, not a Physical System or a diagram.



Figure 9 - Physical architecture export

The export process is the same as for ASN.1 – the user is presented with the detected issues, subset selection (as illustrated in Figure 10) and output directory selection. For each physical architecture, the following files are exported:

- DataView.asn based on Exchange Item allocation to Functional Exchanges;
- InterfaceView.aadl based on Physical Functions and Functional Exchanges;
- DeploymentView.aadl based on Node Physical Components, Physical Links/Paths and Physical Function allocation.

These files require the exported ASN.1 data model. Editing in TASTE requires the DataView.asn and data model's ASN.1 files to be converted to DataView.aadl using "taste-update-data-view" command. Details are described in Chapter 6.4.

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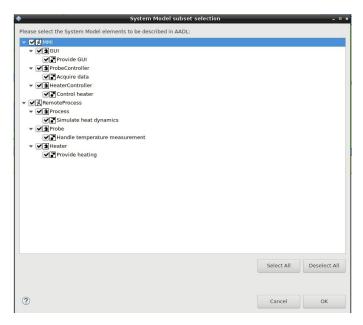


Figure 10 - Architecture subset selection

In order for the architecture to be exportable, it needs to fulfil the following requirements:

- each Physical Function should be allocated to a Behaviour Physical Component;
- each Behaviour Physical Component should be deployed onto a Node Physical Component;
- each Physical Function should have at least one Functional Exchange;
- each Functional Exchange should have at least one Exchange Item assigned;
- all Exchange Items assigned to the same Functional Exchange should have the same Exchange Mechanism set;
- each Functional Exchange should have at most one Exchange Item with Operation Exchange Mechanism;
- an Exchange Mechanism set for an Exchange Item should be one of Operation, Flow or Event;
- all Functional Exchanges between Physical Functions residing on different Node Physical Components should be allocated to Component Exchanges;
- all Component Exchanges between Behaviour Physical Components deployed on different Node Physical Components should be allocated to Physical Links or Physical Paths;

The above formalization allows to translate:

- Capella Physical Functions into TASTE Systems;
- Capella Functional Exchanges into TASTE Subprograms, Features and Connections;
- Capella Node Physical Components into TASTE Systems with Subcomponents and Partitions;
- Capella Physical Links and Physical Paths into TASTE Busses and Connections;
- Physical Ports into TASTE Devices.

The Physical Functions and Functional Exchanges serve as a basis for TASTE Interface View.

Node Physical Components and Physical Links and Paths serve as a basis for TASTE Deployment View.

Behaviour Physical Components are not translated into any TASTE concept, they serve only as allocation points for Physical Functions.



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Component Exchanges between Behaviour Physical Components deployed onto a single Node Physical Component are not taken into account as they are not translated into any TASTE concept; they are also not analysed and no warnings are issued for them, as it is assumed that they may serve to express some high level design concepts.

Exchange Items allocated to Component Exchanges, Physical Links and Physical Paths are not analysed, as they are not translated into any TASTE concept and it is assumed that they may server to express some high level design concepts.

Capella Physical Architecture defines abstract functions, interfaces between them and their deployment onto different nodes. In order to create an executable code, compilation targets need to be established and function behaviour needs to be defined. In order to supply the required information, String Property Values are used.

TASTE allows the user to define a function behaviour in many ways, including, but not limited to:

- SDL diagrams;
- C code;
- Ada code.

The kind of behaviour definition must be specified in Interface View and therefore the plugin requires a Taste::Language property to specify the language used for each Physical Function. The examples of possible values are listed in Table 2.

Compilation targets must be specified in Deployment View. Each TASTE system serving as a deployment node contains a processor class declaration, which indicates the compilation target. Therefore each Node Physical Component requires a Taste::Processor property to specify the aforementioned class. The examples of possible values are listed in Table 3.

The method of communication over a Bus in TASTE is defined by its class. Therefore each Physical Link and Physical Path requires a Taste::Bus property to specify the aforementioned class. The examples of possible values are listed in Table 4.

A deployment node communicates over a Bus through a Device. A TASTE Device is created from a Physical Port, and therefore each Physical Port requires the following properties to be defined:

- Taste::Device to specify the kind of Device used;
- Taste::ConfigurationSchema to specify the configuration schema;
- Taste::Configuration to specify the Device's configuration.

The examples of possible values for Taste::Device are listed in Table 5. The examples of possible values for Taste::ConfigurationSchema are listed in Table 6. The examples of possible values for Taste::Configuration are listed in Table 7.

The complete list of supported TASTE properties is listed in Table 8. Please note that all properties must be used with a "Taste::" prefix.

 $Table\ 2\ \hbox{-} Example\ values\ of\ Taste::Language$

Taste::Language property value	Comment
С	Function behaviour is defined in C code
Ada	Function behaviour is defined in Ada code
SDL	Function behaviour is defined in SDL code/diagram
Simulink	Function behaviour is defined in a Simulink model



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Table 3 - Example values of Taste::Processor

Taste::Processor property value	Comment
ocarina_processors_x86::x86.linux	Target is Linux on x86
ocarina_processors_arm::stm32f407_discovery.gnat2017	Target is baremetal STM32F407
ocarina_processors_arm::stm32f429_discovery.gnat2017	Target is baremetal STM32F429
ocarina_processors_leon::gr712rc.rtems51_posix	Target is RTEMS on Leon GR712RC

Table 4 - Example values of Taste::Bus

Taste::Bus property value	Comment
ocarina_buses::ip.i	Communication over TCP/IP connection
ocarina_buses::serial.generic	Communication over serial connection

Table 5 - Example values of Taste::Device

Taste::Device property value	Comment
ocarina_drivers::generic_sockets_ip.pohic	Generic IP socket for POHIC
ocarina_drivers::generic_serial.pohiada	Generic serial device for POHIADA
ocarina_drivers::STM32F4_serial.pohiada	STM32 serial device for POHIADA

Table 6 - Example values of Taste::ConfigurationSchema

Taste::ConfigurationSchema property value	Comment
/home/taste/tool-inst/include/	IP socket configuration schema location for
ocarina/runtime/polyorb-hi-	default TASTE installation
c/src/drivers/configuration/ip.asn	
/home/taste/tool-	Serial configuration schema location for default
inst/include/ocarina/runtime/polyorb-hi-	TASTE installation
c/src/drivers/configuration/serial.asn	

Table 7 - Example values of Taste::Configuration

Taste::Configuration property value	Comment
{devname "/dev/ttyUSB0", speed b115200, bits 8}	Example configuration of a serial device
{devname "eth0", address "127.0.0.1", port 5556}	Example configuration of an IP socket

Table 8 - List of supported TASTE properties

Taste property name	Meaning	Applies to
Language	Function language	Physical Function
	Translates to: Source_Language	
Processor	Compilation target	Node Physical
	Translates to: PROCESSOR	Component
Bus	Communication bus/method	Physical Link
	Translates to: BUS	Physical Path
Device	Communication device	Physical Port
	Translates to: DEVICE IMPLEMENTATION	
	DEVICE CLASS	



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DeviceClass	[OPTIONAL]	Physical Port
	Override of the device class implied by	
	Taste::Device	
	Translates to: DEVICE CLASS (override)	
ConfigurationSchema	Address of an applicable ASN.1 configuration	Physical Port
	schema	
	Translates to: Deployment::Config	
Configuration	Communication device configuration	Physical Port
	Translates to: Deployment::Configuration	
Version	[OPTIONAL]	Physical Port
	Communication device version	
	Translates to: Deployment::Version	
QueueSize	[OPTIONAL]	Functional Exchange
	Internal message queue of an interface	
	Translates to: Taste::Associated Queue Size	
MinimumExecutionTime	[OPTIONAL]	Functional Exchange
	Declared minimum execution time of an	
	interface invocation	
	Translates to: Compute Execution Time	
MaximumExecutionTime	[OPTIONAL]	Functional Exchange
	Declared maximum execution time of an	
	interface invocation	
	Translates to: Compute Execution Time	
Deadline	[OPTIONAL]	Functional Exchange
	Declared deadline for an interface invocation	
	Translates to Taste::Deadline	
ExchangeKind	[OPTIONAL]	Functional Exchange
2	Override of the implied interface type, one of	
	sporadic, protected and unprotected	
	Translates to: Taste::RCMoperationKind	
	(override)	
Timer	[OPTIONAL]	Physical Function
	Timer declaration for an SDL function	(only with
	Translates to: TIMER SUBCOMPONENT	Taste::Language set
		to SDL)
Directive	[OPTIONAL]	Physical Function
	Additional TASTE specific directive (e.g.	
	linker option)	
Encoding	[OPTIONAL]	Functional Exchange
	Encoding to be used for exchanged data items.	
	Can be Native, ACN or UPER.	

The values of aforementioned TASTE properties are not hardcoded in the code and the user is free to use any value not described in this document. However, in order to generate a valid AADL, values compatible with TASTE must be used. For that purpose, TASTE documentation [RD2] needs to be consulted. In order to simplify the process, the plugin is distributed with additional TasteTypes Capella library, which contains a set of predefined properties.



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6.3. Additional TASTE integration tools

TASTE provides tools for model conversion, editing and compilation. For convenience, the plugin exposes the most important commands in the menu bar, as illustrated in Figure 11:

- Generate DataView.aadl to convert all ASN.1 files into DataView.aadl;
- Generate code skeletons to generate code skeletons;
- Edit project in TASTE to launch TASTE GUI for the selected physical architecture;
- Edit function in TASTE to launch OpenGEODE editor for the selected SDL function;
- Build system to start the project building process.

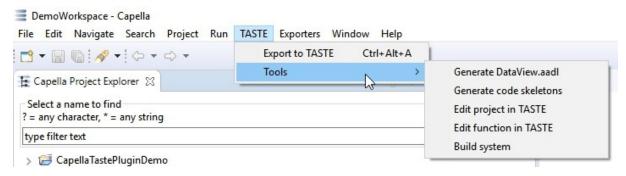


Figure 11 - Additional TASTE integration commands

It must be noted that in order to use any of these commands the physical architecture and the data model must be already exported. Code skeleton generation, project/function editing and project building will work only after DataView.aadl is successfully generated. Function editing will work only after code skeletons are generated. For more details, please refer to TASTE documentation [RD2].

All commands except "Edit function in TASTE" require the Physical Architecture to be selected in the Capella Project Explorer. "Edit function in TASTE" requires the relevant Physical Function to be selected in the Capella Project Explorer.

It must be also noted that TASTE works only in a Linux environment and so the aforementioned commands will work only on Linux.

6.4. Example workflow

The following tutorial presents the recommended workflow for using the plugin, using an example "Monitor" project. The included screenshots are uncut in order not to deprive the user of context which may be helpful in case of doubts. Each of the subsequent chapters corresponds to a single tutorial step.

6.4.1. Define the data model

First, a data model needs to be defined. In order to create a valid AADL DataView, each type must be constrained – strings must have their maximum lengths set, numeric types must be bounded, cardinalities must be finite. It is recommended not to use the PredefinedTypes, as some of them lack the required bounding. The data model should include Exchange Items. Please note that every Exchange Item must have its Exchange Mechanism set. The data model of the example project is illustrated in Figure 12.



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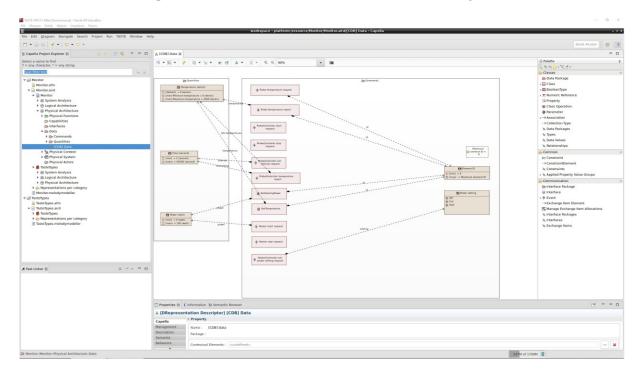


Figure 12 - Monitor data model

6.4.2. Define the architecture

When the data model is established, a physical architecture needs to be defined. It can be created either by hand, or by Capella assisted transition from a logical architecture. It must be noted that while the physical architecture can trace to the logical one, the mapping does not need to be 1:1. As TASTE Interface and Deployment Views operate on the very entities, the choice was made to create both Views from a single architecture kind. The user should ensure that all the modelling requirements described in the previous chapter are met. The physical architecture of the example project is illustrated in Figure 13. Figure 14 illustrates the assignment of Exchange Items to a Functional Exchange.



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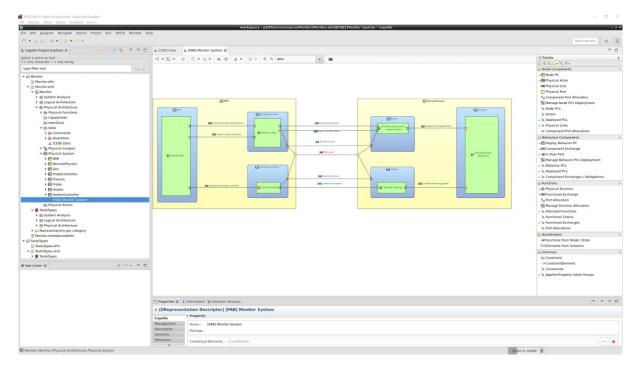


Figure 13 - Monitor physical architecture

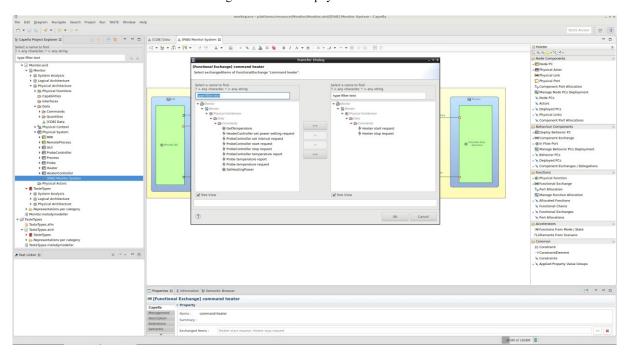


Figure 14 - Exchange Item assignment to a Functional Exchange

6.4.3. Apply the required properties

As described in the previous chapter, the required properties need to be applied to Physical Functions, Node Physical Components, Physical Links, Physical Paths and Physical Ports. In order to facilitate this process, the use of TasteTypes library is recommended. Figure 15 and Figure 16 illustrate the application of the properties.



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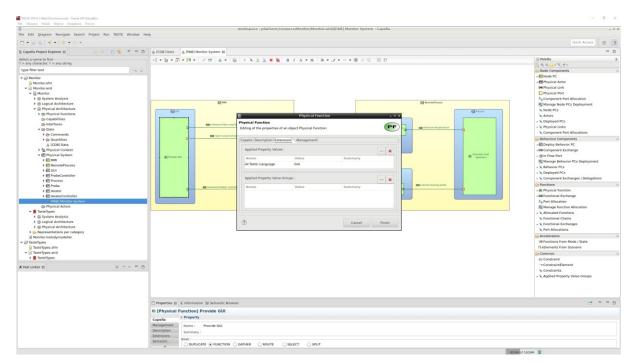


Figure 15 - Application of function properties

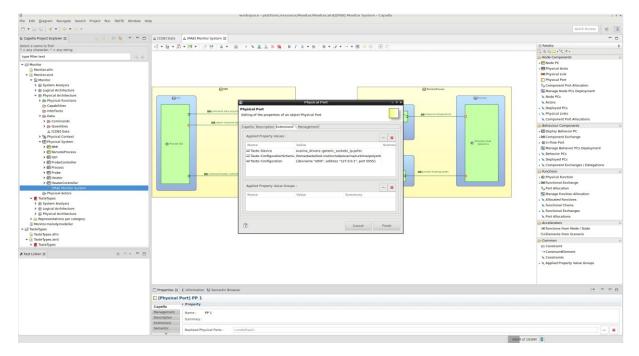


Figure 16 - Application of port properties

6.4.4. Export the data model

When ready, the data model can be exported. It is recommended to create a dedicated folder for the project artefacts. Each Package is exported to a separate file. In case of the example "Monitor" project, this is "Commands.asn" and "Quantities.asn". After selecting the export command (Figure 17), the user must chose the subset to be exported (Figure 18). Select all.



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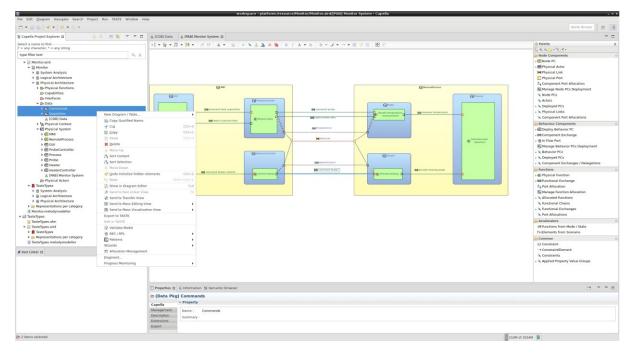


Figure 17 - Data model export

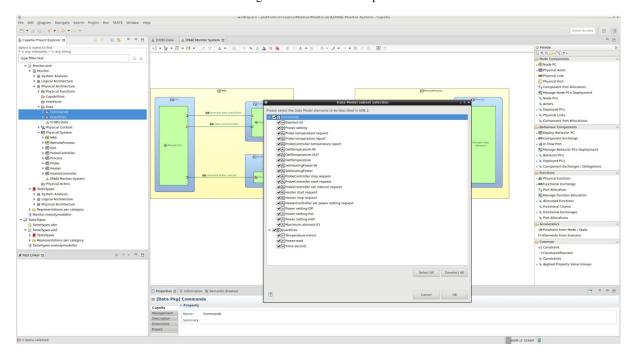


Figure 18 - Data model subset selection

6.4.5. Export the architecture

When ready, the physical architecture can be exported. The target folder must be the same as the one for the data model. The export will create the following files: "InterfaceView.aadl", "DeploymentView.aadl" and "DataView.asn". These files are the same for every project and are not dependent on any project configuration. After selecting the export command (Figure 19), the user must chose the subset to be exported (Figure 20). Select all.



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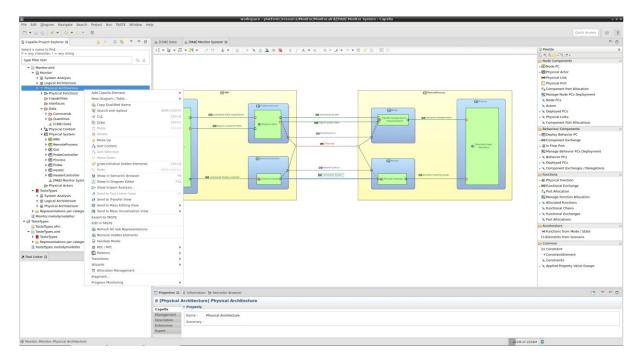


Figure 19 - Architecture export

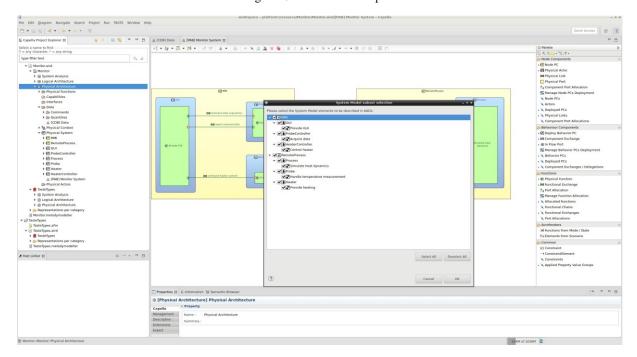


Figure 20 - Architecture subset selection

6.4.6. Postprocessing, editing and compilation

After the data model and the physical architecture are exported, the generated ASN.1 data model must be converted into AADL. This can be done using taste-update-data-view command or asn2aadlPlus directly. Please consult TASTE documentation [RD2] for details. The plugin can invoke the "taste-update-data-view" command directly from the menu bar, as described in Chapter 6.3.

Before a function can be edited, its skeleton must be generated in TASTE. This can be done using "tastegenerate-skeletons" command. The plugin can invoke it directly from the menu bar.

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In the default configuration, on a Linux platform, the plugin performs these two steps automatically after the physical architecture export. For the example "Monitor" project, the generated artefacts are illustrated in Figure 21.

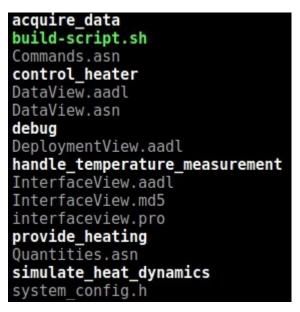


Figure 21 - Generated artefacts

In order to edit the TASTE project in a dedicated TASTE GUI application, "taste-edit-project" command should be used. The plugin can invoke it directly from the menu bar. The generated Interface and Deployment Views are illustrated in Figure 22 and Figure 23 respectively.

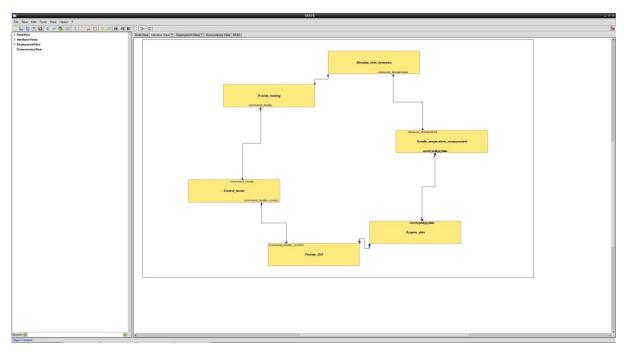


Figure 22 - Generated Interface View

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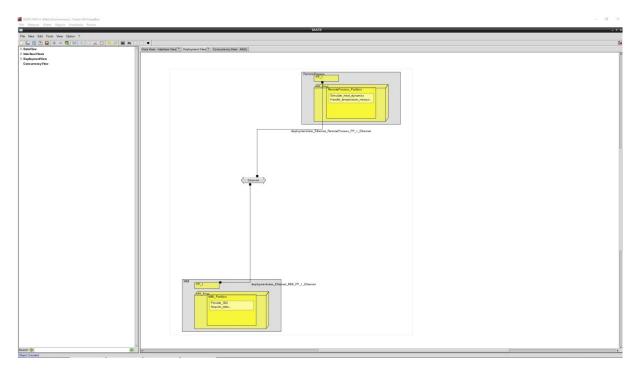


Figure 23 - Generated Deployment View

In order to compile the TASTE project into executables, "taste-build-system" command should be used. The plugin can invoke it directly from the menu bar. For the example "Monitor" project, the "Provide GUI" function with GUI as the set language results in the application illustrated in Figure 24.



Figure 24 - Generated GUI

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7. Supported Capella Features

7.1. Data Model

Capella-TASTE Plugin supports the following Capella data model constructs:

- Data Package (org.polarsys.capella.core.data.information.DataPkg)
- Boolean type (org.polarsys.capella.core.data.information.datatype.BooleanType);
- Numeric type (org.polarsys.capella.core.data.information.datatype.NumericType);
- String type (org.polarsys.capella.core.data.information.datatype.StringType);
- Enumeration type (org.polarsys.capella.core.data.information.datatype.Enumeration);
- Literal Numeric Value (org.polarsys.capella.core.data.information.datavalue.LiteralNumericValue);
- Literal String Value (org.polarsys.capella.core.data.information.datavalue.LiteralStringValue);
- Exchange Item type (org.polarsys.capella.core.data.information.ExchangeItem);
- Collection type (org.polarsys.capella.core.data.information.Collection);
- Class and Union type (org.polarsys.capella.core.data.information.Class);
- Binary Expression (org.polarsys.capella.core.data.information.datavalue.BinaryExpression);
- Unary Expression (org.polarsys.capella.core.data.information.datavalue.UnaryExpression).

The plugin supports the concept of single parent inheritance.

"*" cardinality is supported and interpreted as "MAX", producing valid ASN.1; this however can cause issues with the ASN1SCC compiler due to static memory allocation requirements.

As ASN.1 does not support an equivalent of expressions, the encountered binary and unary expressions are resolved, computed and translated into ASN.1 constants. Expression resolution is limited to expressions operating on integers and derivatives. The following operators are supported:

- NOT (unary): -operand
- PRE (unary): operand 1
- SUC (unary): operand + 1
- ADD: left operand + right operand
- AND: left operand & right operand
- DIV: left operand / right operand
- EQU: left operand == right operand ? 1:0
- IOR: left operand | right operand
- MAX: max(left operand, right operand)
- MIN: min(left operand, right operand)
- MUL: left operand * right operand
- POW: pow(left operand, right operand)
- SUB: left operand right operand
- XOR: left operand ^ right operand.

The expression resolver is aimed to facilitate calculating cardinalities and value ranges.

References within the interpreted types are resolved to concrete types. References are not interpreted as stand-alone entities.



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7.2. System Model

Capella-TASTE Plugin supports the following Capella physical architecture constructs:

- Node Physical Component;
- Physical Actor;
- Physical Link;
- Physical Path;
- Physical Port;
- Behaviour Physical Component;
- Behaviour Physical Component deployment;
- Physical Function;
- Physical Function allocation;
- Functional Exchange;
- Functional Exchange allocation to Physical Links and Paths via Component Exchanges;
- Exchange Items exchanged by Functional Exchanges.

Node Physical Components nested within other Node Physical Components are treated the same way as Behaviour Physical Components. This distinction is not relevant to AADL generation itself, as the system subdivision into components (node or behaviour) only affects the grouping and its hierarchy presented in the system model selection dialog.



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8. Best practices

8.1. Naming convention

Both ASN.1 and AADL have strict naming rules. On the other hand, Capella does not enforce any naming conventions. Therefore, in order to ensure that the generated ASN.1 and AADL contains only valid names, the plugin contains a renaming algorithm for data types. However, this algorithm consists of several basic rules and lacks the insight into the model author's intent. Therefore, for best results, it is good to adopt a naming convention inside Capella, so that the renaming is unnecessary or kept to a minimum. The recommendations for the naming convention are as follows:

- start type names with an upper-case letter;
- start member or value name with a lower-case letter;
- do not use special characters in names;
- do not start names with numbers;
- avoid using the same name in different places; in particular, do not define different data types or members with the same name;
- use unique names for physical architecture elements.

8.2. Inheritance

ASN.1 does not support the same class inheritance concept as Capella. Therefore Capella class hierarchies are flattened and translated into sequences with choices. While this solution provides data type declarations which – in many applications – can be used as classes with hierarchy, this abstraction is not transparent. Therefore, it is recommended to avoid class hierarchies if possible.

8.3. Cardinality

Due to static memory allocation requirements, the defined ASN.1 types must have fixed maximum sizes. Therefore the usage of "*" cardinality should be avoided.

8.4. Characters and strings

When defining data types from ground-up, it is possible to define a Character type first (or reuse the one from Capella's "*Predefined Types*" package), and then define various String types as collections of Characters. As IA5String is the native base type for both Strings and Characters, the optimal way to define Strings is to use the base String type and just apply the desired size constraints.

8.5. Nesting

AADL generator flattens the entire component hierarchy, as all components deployed onto a host node are transformed into a single partition. Nesting of behaviour components may make the Capella diagrams more readable and the model subset selection more efficient. However, as nested node components are treated exactly the same by the plugin as behaviour components, while still possibly making the model subset selection more efficient, they may be misleading to a diagram's viewer. Therefore it is recommended to avoid deploying node components onto other node components.

8.6. Communication layers

When designing software, it is common to dedicate some components to interfacing with external nodes via message protocols. This involves, for example, the definition of components such as "C&C



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interface" or "TM/TM interface" and functions such as "Decode TC" or "Encode TM". Given the data model and proper definitions of TASTE function parameters, TASTE automatically provides data encoding and decoding, together with data exchange handling. Therefore such components are redundant and may unnecessarily complicate the implementation. Therefore such constructs should be avoided.

8.7. GUI considerations

First versions of the plugin mapped each Capella GUI function into a separate TASTE GUI function. However, decomposition of larger, more complex projects may lead to definition of many GUI functions, each responsible for a given small task. While this may be a good logical decomposition, it may cause usability and testing issues – starting and managing many windows may be taxing for some users. Additionally, MSCs and Python scripting in TASTE are designed to work in place of a single GUI, which may make integration tests harder. Current version of the plugin presents the user with an option to merge all GUIs within each node into a single "master" GUI. Due to its convenience, it is turned on by default. However, this choice needs to be considered on a per-project basis, taking into account both the target end-user, as well as possible operational and testing scenarios.

8.8. TASTE knowledge and awareness of implementation details

System modelling in Capella is often a multi-stage process, involving possibly many people with different goals and areas of expertise. Definition of basic data types and concepts can be easily done by relevant domain experts. Definition of logical architecture may be kept abstract, without too much concern regarding the implementation details. However, as the physical architecture is translated by the plugin directly into AADL, which in turn is translated, together with the corresponding behaviour definitions, into compilable code and then executable binaries, the expert designing the physical architecture should be familiar with TASTE, its limitations and best practices. He or she should have a solid vision of the system implementation, taking into account the choice of behaviour definition languages and all the functions that the software needs to perform (even the ones not directly implied by the requirements). While it is possible to modify the resulting AADL by a TASTE expert directly in TASTE tools, it may be expensive and error prone in case of an iterative or agile approach (or even a waterfall one, due to the inevitability of changes), because, if any changes are done to the Capella model, then:

- either the changes must be manually reapplied to the generated and tweaked AADL model;
- or the AADL model needs to be regenerated and the TASTE expert's AADL tweaks need to be reapplied again.

It may be also possible that changes to the different representations of the system may be contradictory. Therefore it is recommended not to manually edit the generated AADL and involve a TASTE expert during the physical architecture definition.



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9. Messages

The list of messages generated by the plugin is presented in Table 9.

Table 9 - Messages produced by the Capella-TASTE Plugin

Message	Recommendation
Cannot process the package. Orphan?	Verify the model consistency.
Type \$NAME has multiple super classes -	Avoid multiple inheritance.
multiple inheritance is not supported.	12,010 11.01.14
Could not find package for \$NAME type".	Verify model consistency.
Cannot resolve value of element named	The value type might be not supported by the
\$ELEMENT NAME of type \$TYPE NAME.	plugin (e.g. an expression).
Cannot parse the value of element named	Verify that the value is correct with respect to the
\$ELEMENT_NAME of type \$TYPE_NAME.	value type.
Typed element \$ELEMENT_NAME does not	Verify that the given element has a type assigned.
have a type.	
Collection type <i>\$NAME</i> does not have an element	Verify that element type is assigned for the given
type.	collection type.
Critical error while trying to interpret \$NAME:	The plugin has encountered a critical error. Please
\$EXCEPTION.	report the issue to N7 Space.
Cannot extract the domain value of element	Domain value type may be unsupported or
named \$NAME.	incorrectly defined.
Cannot get type of element named \$NAME.	Verify that proper type is assigned to the given
	element.
String type \$NAME does not contain maximum	Specify a finite maximum length for the given
length specification; this may cause issues with	string type.
ASN.1 compilation.	
Function \$FUNCTION_NAME does not have any	It is recommended for some function types (GUI
input ports	and SDL) to have at least one input port.
Function \$FUNCTION_NAME does not have any	It is recommended for some function types (GUI
output ports	and SDL) to have at least one output port.
Name \$NAME is not unique when transformed	All names used in architecture description should
for AADL use (\$TRANSFORMED_NAME)	be unique.
Could not resolve the value of binary expression	Verify that the expression is supported by the
\$EXPRESSION_NAME	plugin. Verify that the expression is supported by the
Could not resolve the type of binary expression \$EXPRESSION NAME	plugin.
Could not resolve the value of unary expression	Verify that the expression is supported by the
\$EXPRESSION NAME	plugin.
Could not resolve the type of unary expression	Verify that the expression is supported by the
\$EXPRESSION NAME	plugin.
Unary operation \$OPERATION NAME is not	Verify that the expression is supported by the
supported	plugin.
Operation \$OPERATION NAME could not be	Verify that the expression is supported by the
executed on operand <i>\$OPERAND</i> :	plugin and its evaluation does not lead to numeric
\$EXCEPTION	issues.
Operation \$OPERATION NAME could not be	Verify that the expression is supported by the
executed on operands	plugin and its evaluation does not lead to numeric
<i>\$OPERAND</i> and <i>\$OPERAND</i> : <i>\$EXCEPTION</i>	issues (e.g. division by 0).
Operation not set for operands \$OPERAND and	Verify that the expression has its operation
\$OPERAND	correctly set.



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Device class for port \$PORT NAME is undefined Check device implementation definition for the and cannot be derived given port and/or define device class. Only a single one can be defined. Configuration schema for port \$PORT NAME is Define configuration schema for the given port. undefined Only a single one can be defined. Configuration for port \$PORT NAME Define configuration for the given port. Only a undefined single one can be defined. \$EXCHANGE NAME is between Ensure that all exchanges between different Exchange



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