FIRMWARE_14D_MotorTest

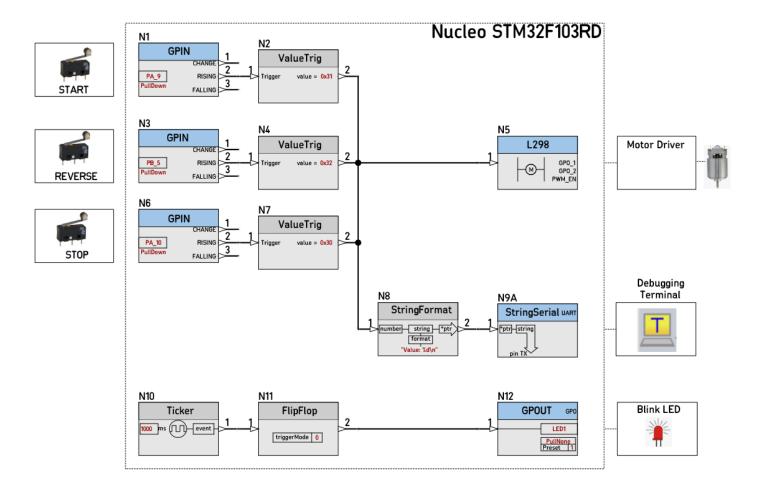
Nucleo F401 + X-NUCLEO-IHM04A1 version of 12D MotorTest

This is a more compact H/W version due to smaller (Nucleo + X-Nucleo) PCBs.

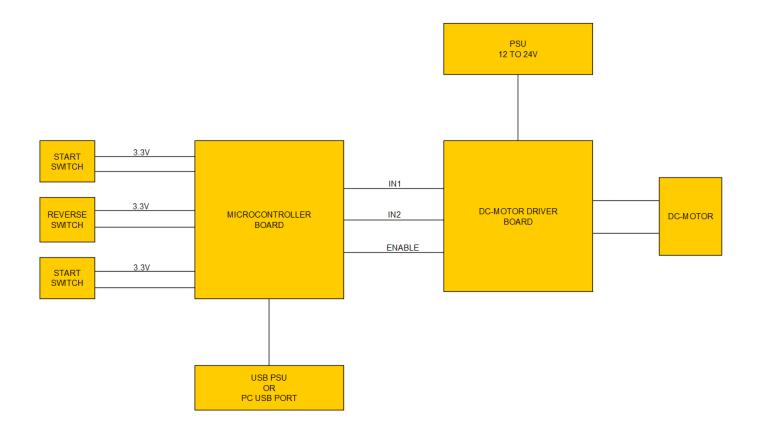
The nBlocksStudio Design is the same with 10D_MotorTest for mbed-LPC1768 + L298 and 12D_MotorTest for Nucleo-F401 + L298, only the paramaters that define the Pins to be used are different.

nBlocksStudio Design

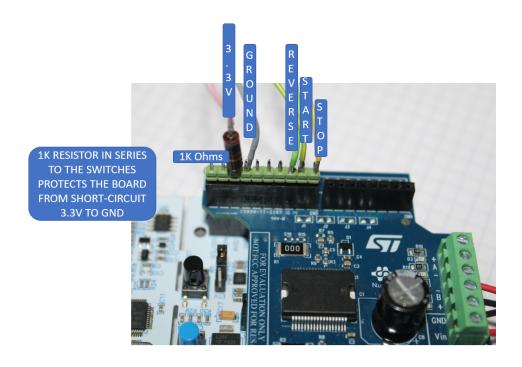
This is the Design of the 12D_MotorTest project. The I/O pins used in this project, are different and are modified in the main.cpp directly without using a Design Schematic.



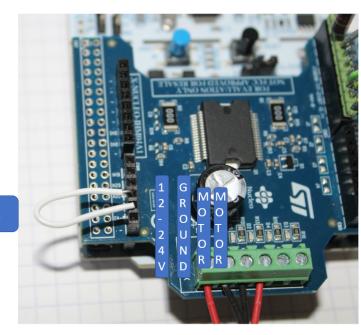
Block Diagram



Switches connection



Motor connection and bridge



BRIDGE MANDATORY

main.cpp

The lines with comment "X-NUCLEO" are updated directly in this file manually

```
#include "nlib\nblocks.h"
#include "nlib\nblocks.h"
#include "nlib\spybsp.h"
//Custom.nodes:
#include "nlib\spybsp.h"
#in
(PA_9, PULLDOWN); // X-NUCLEO-IHM04A1-0A-(0x31); (PB_6, PullDown); // X-NUCLEO-IHM04A1-0A-(0x32); (PA_0, PA_1, PB_0); // X-NUCLEO-IHM04A1--F (PC_7, PullDown); // X-NUCLEO-IHM04A1-0A-
                                                                                                                                                                                                                                                                    (0); (LED1, PullNone, 1);
 nBlockConnection
nBlockConnection
                                                                                               nBlockConnection-
  nBlockConnection
nBlockConnection
                                                                                               n_conn4( &nb_nBlockNode2_GFIN,
n_conn5( &nb_nBlockNode1_ValueTrig,
n_conn6( &nb_nBlockNode1_ValueTrig,
n_conn7( &nb_nBlockNode3_ValueTrig,
n_conn8( &nb_nBlockNode3_ValueTrig,
n_conn9( &nb_nBlockNode3_ValueTrig,
n_conn9( &nb_nBlockNode6_ValueTrig,
n_conn10( &nb_nBlockNode6_ValueTrig,
n_conn11( &nb_nBlockNode0_GPIN,
                                                                                                                                                                                                                                                                                                0, 8nb_nBlockNode4_L298, 0);
0, 8nb_nBlockNode7_StringFormat, 0);
                                                                                                                                                                                                                                                                                                0, & Shb_nBlockNode4_L298, 0);
0, & Shb_nBlockNode4_L298, 0);
0, & Shb_nBlockNode4_L298, 0);
0, & Shb_nBlockNode4_L298, 0);
  nBlockConnection
nBlockConnection
  nBlockConnection
                                                                                                                                                                                                                                                                                                   1, 8nb_nBlockNode1_ValueTrig,
  //--*-*--Main-function--*-*-
int-main(void)-{
                     SetupWorkbench();
while(1) {
                                         ProgressNodes();
```