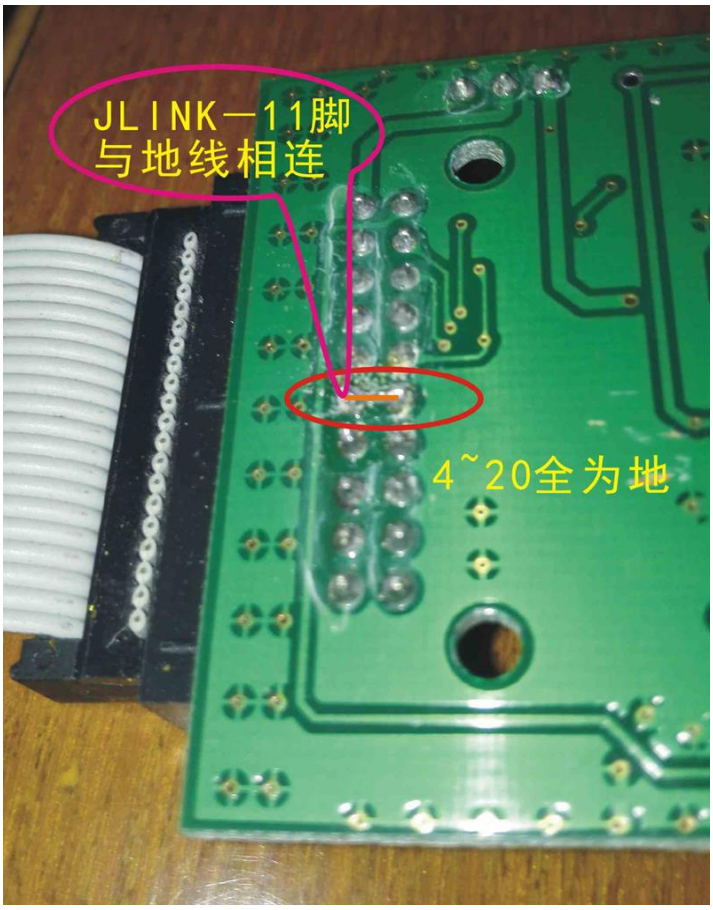
1． nMotion 板上有一个接插件 J2 端口，对 nMotion 烧写程序需要将 JLINK 的 20 芯中的 1,3,5…单数一排针与 J2 对接，1 脚对 1 脚，另外需要将 JLink 的地线与 nMotion 的外接地连接。

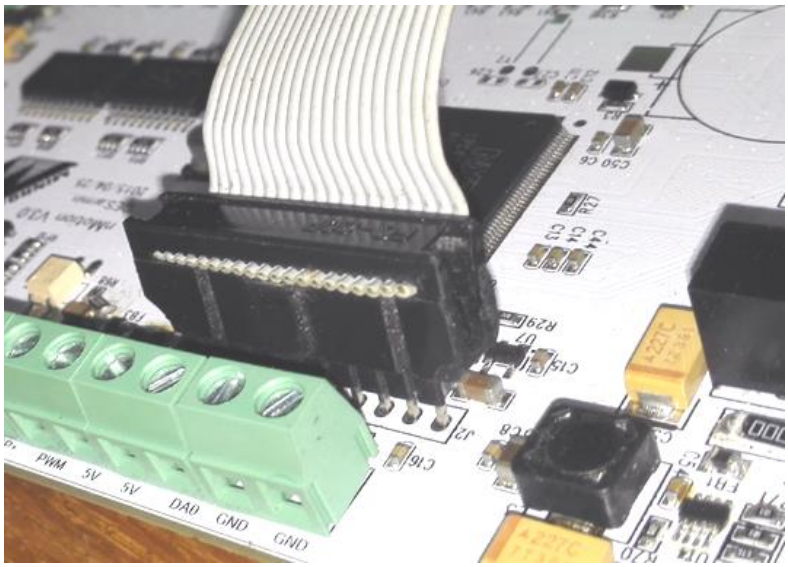
2. 另一个方便的做如如下：

将 JLINK 的 11 脚 接地 （nMotion 对应此脚为地线），此脚 JLINK 是很少用，可以这么做，没什么问题！



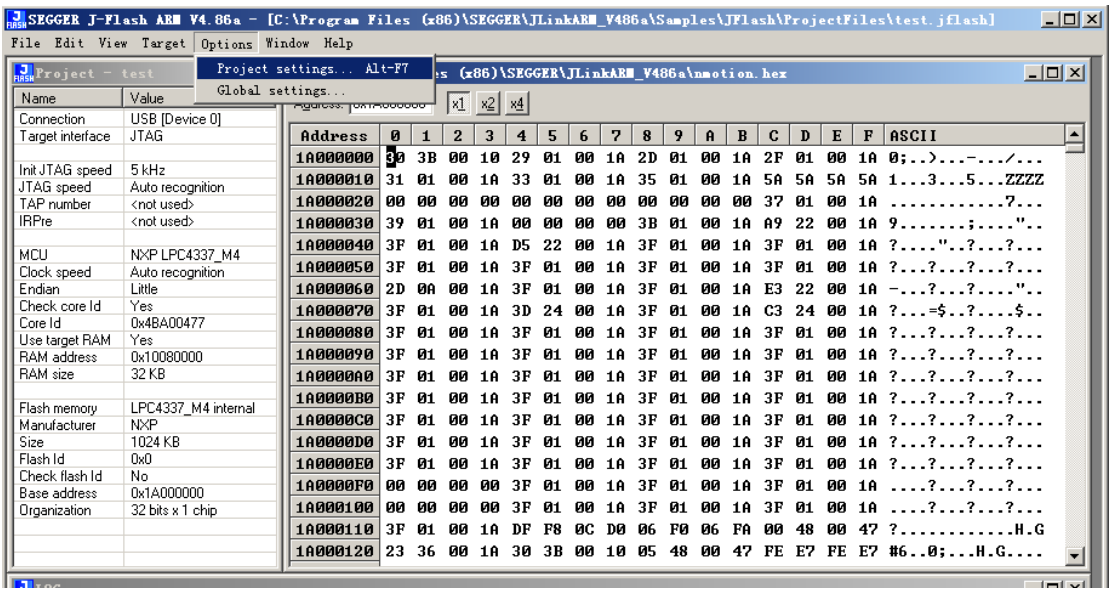
JLINK 的 20PIN 排线接口插入 2.54mm 间距的单排针，如下图

再将露出的针和 nMotion 板对接。

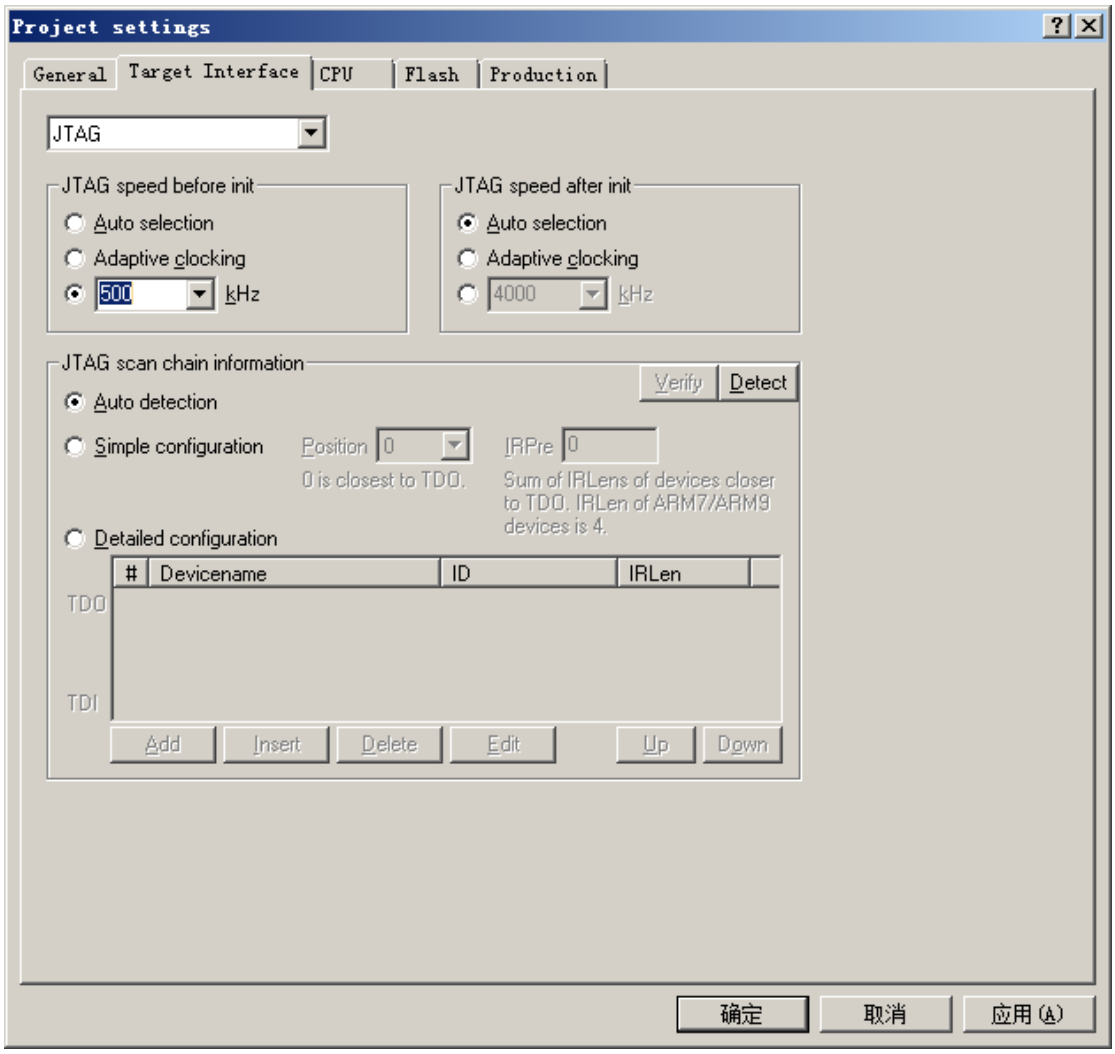


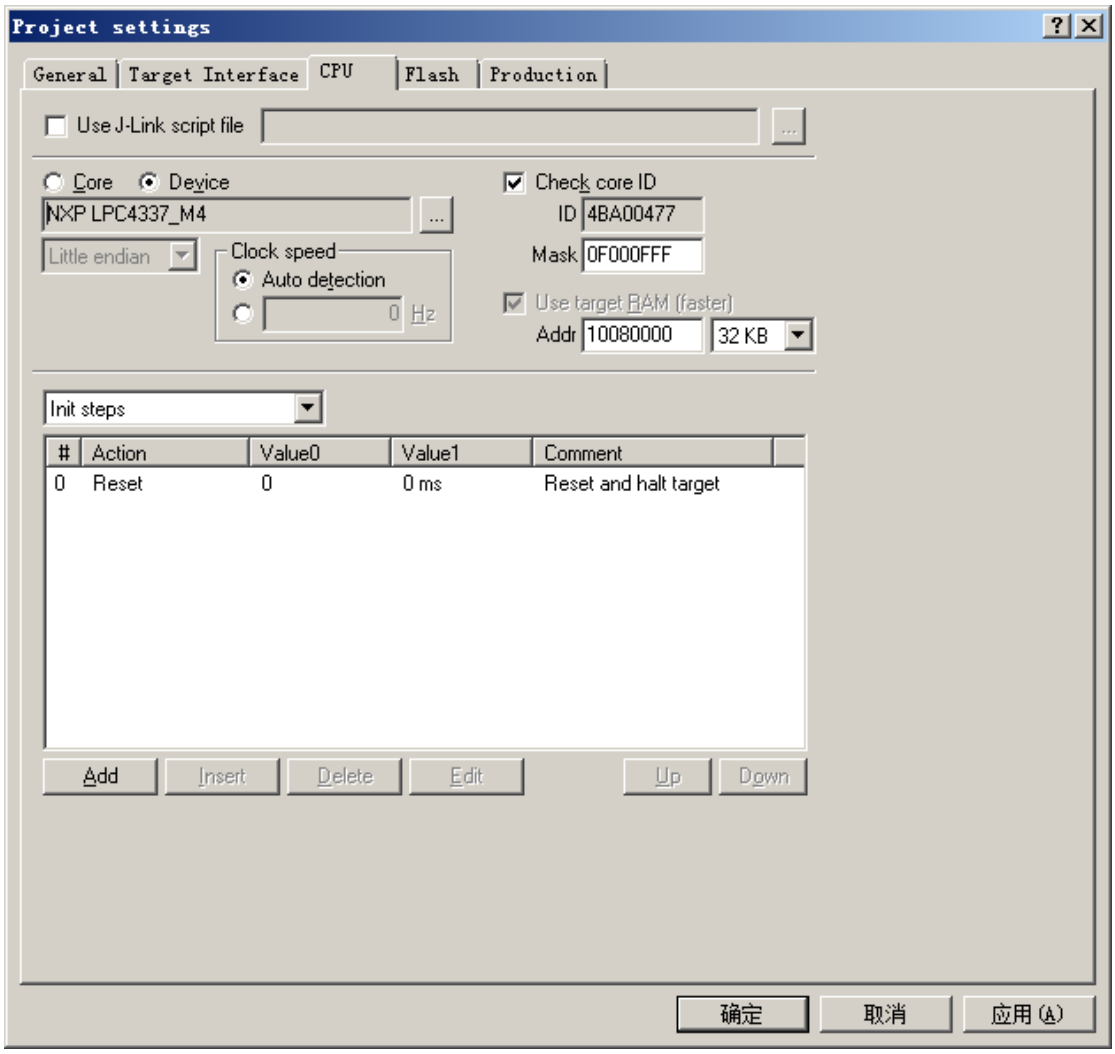
3. 打开 J-Flash

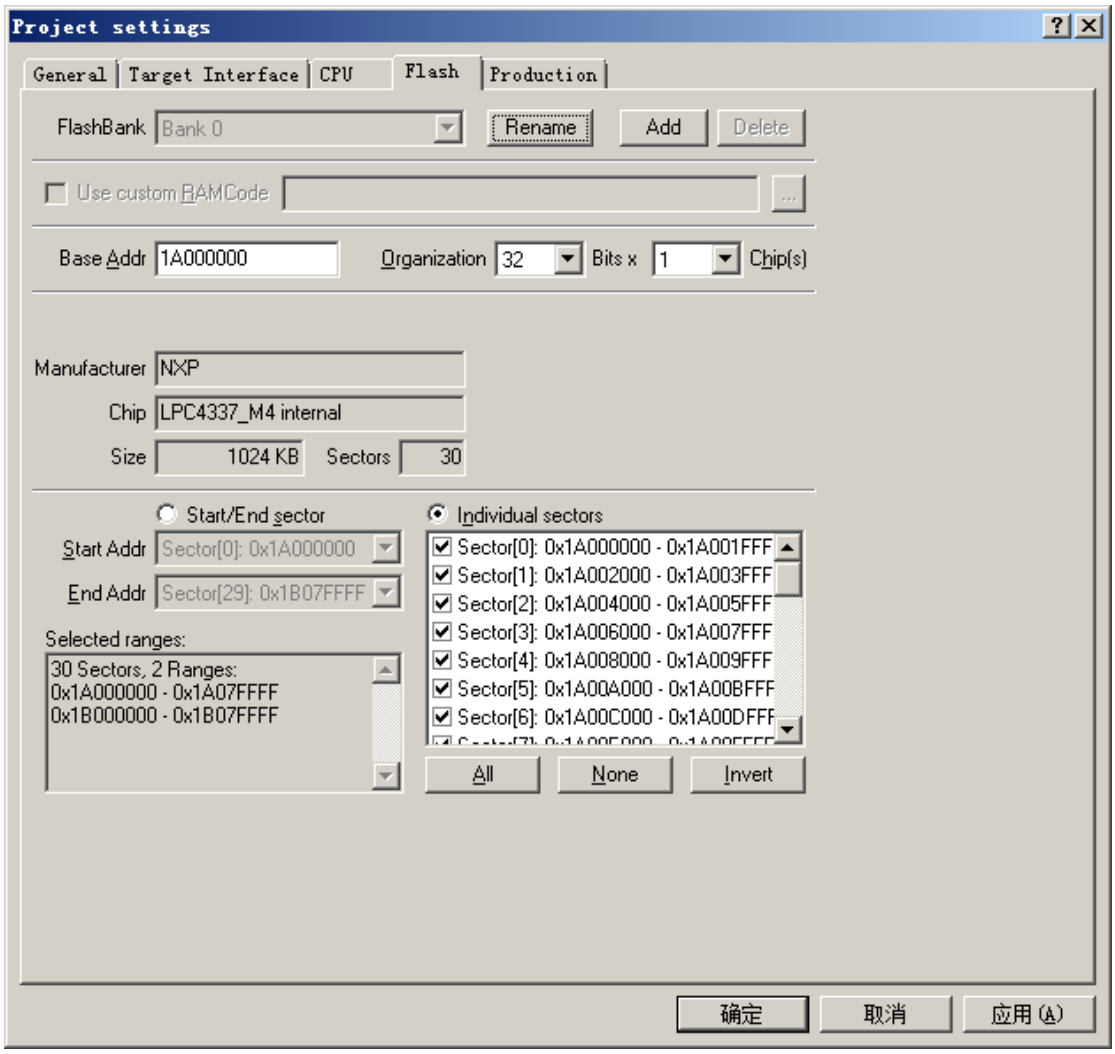
Options->Project settings



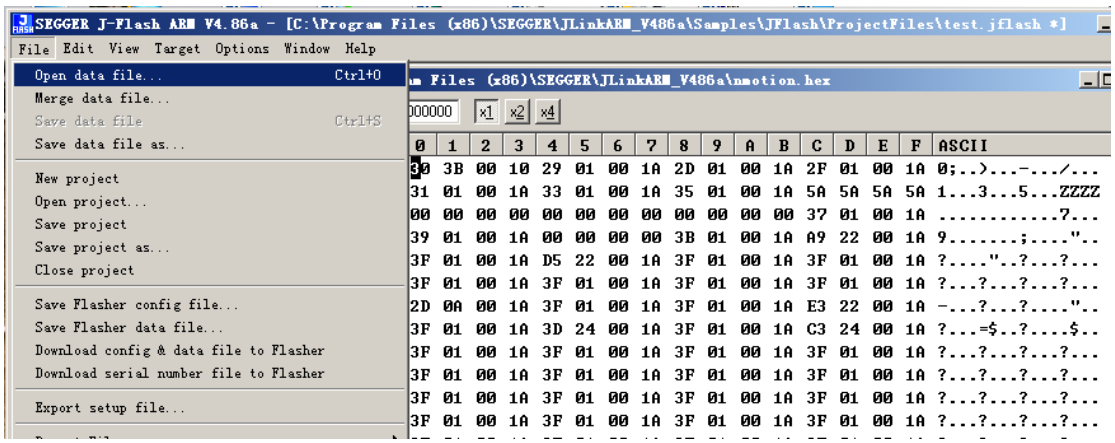
如下配置

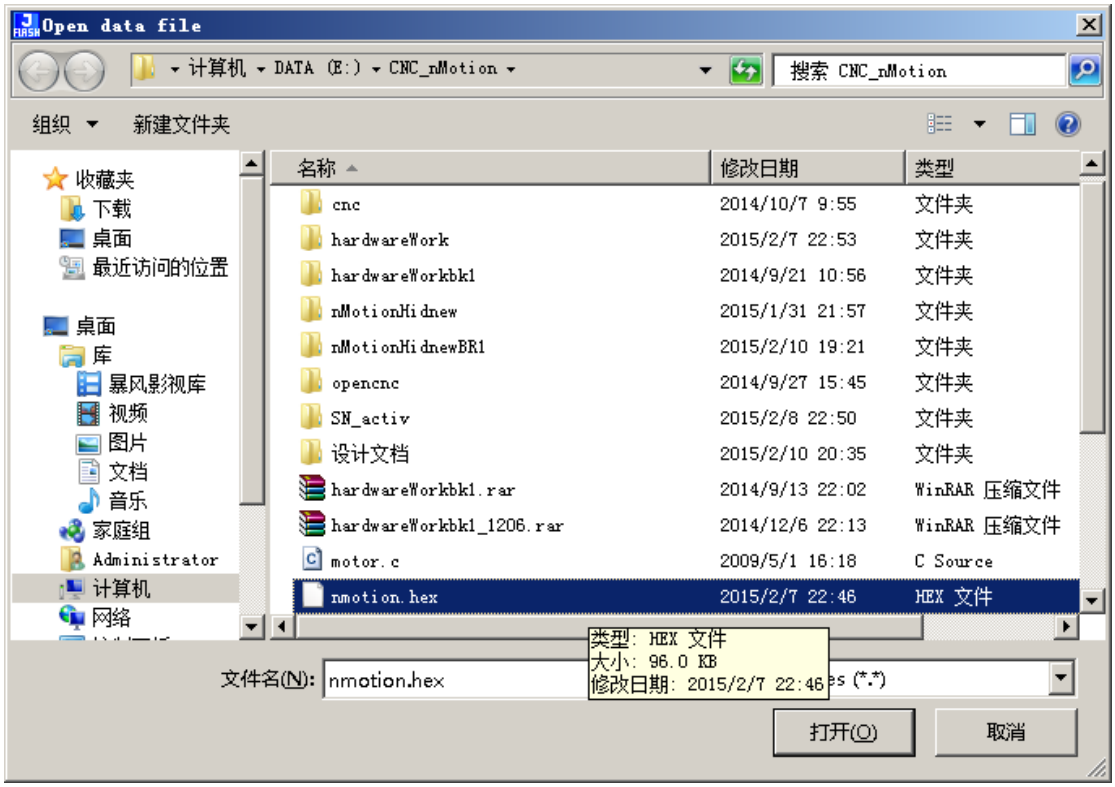

Project settings
General
Target Interface
CPU
Flash
Production
JTAG
JTAG speed before init
Auto selection
Adaptive clocking
500
kHz
JTAG speed after init
Auto selection
Adaptive clocking
4000
kHz
JTAG scan chain information
Verify
Detect
Auto detection
Simple configuration
Position
0
0 is closest to TDO.
IRPre
0
Sum of IRLens of devices closer to TDO. IRLen of ARM7/ARM9 devices is 4.
Detailed configuration
#
Devicename
ID
IRLen
TDO
TDI
Add
Insert
Delete
Edit
Up
Down
确定
取消
应用 (A)


Project settings
General
Target Interface
CPU
Flash
Production
Use J-Link script file
Core
Device
NXP LPC4337_M4
Little endian
Clock speed
Auto detection
0 Hz
Check core ID
ID 4BA00477
Mask 0F000FFF
Use target RAM (faster)
Addr 10080000
32 KB
Init steps
Action Value0 Value1 Comment
0 Reset 0 0 ms Reset and halt target
Add
Insert
Delete
Edit
Up
Down
确定
取消
应用 (A)

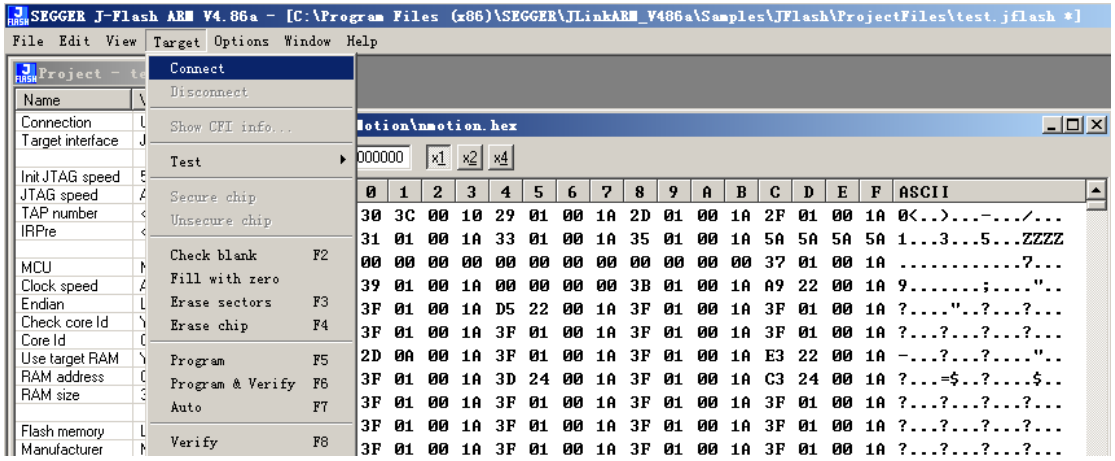


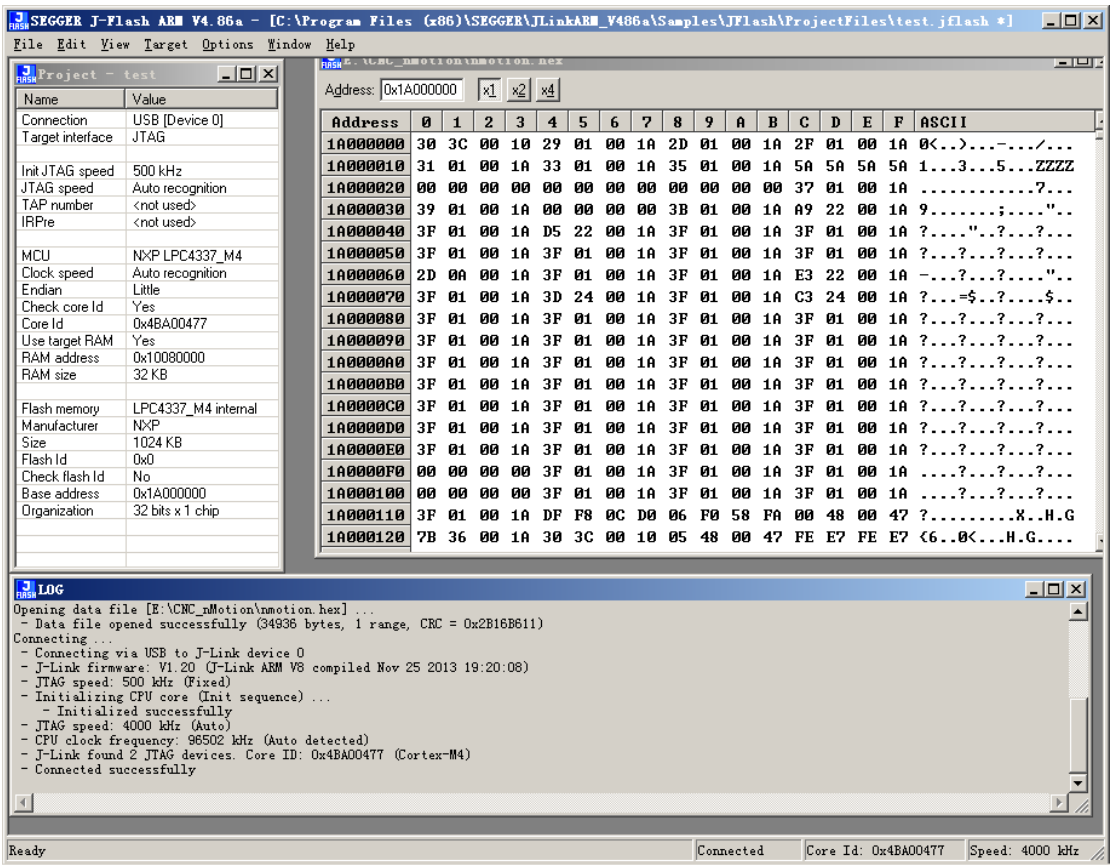
打开要烧写的 nMotion?.hex 文件



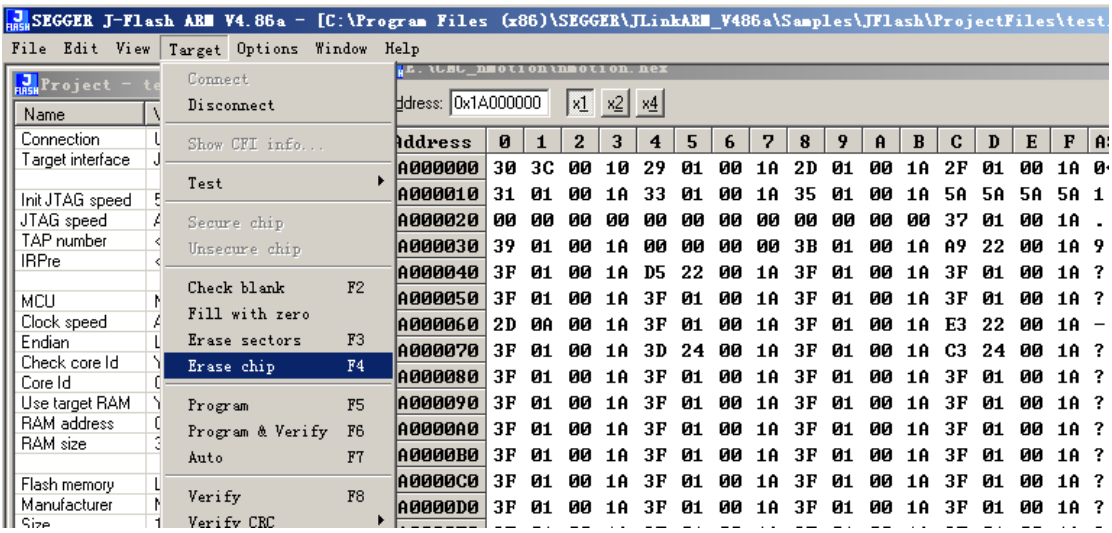


打开 Target->connect
连接上运动控制卡后如图

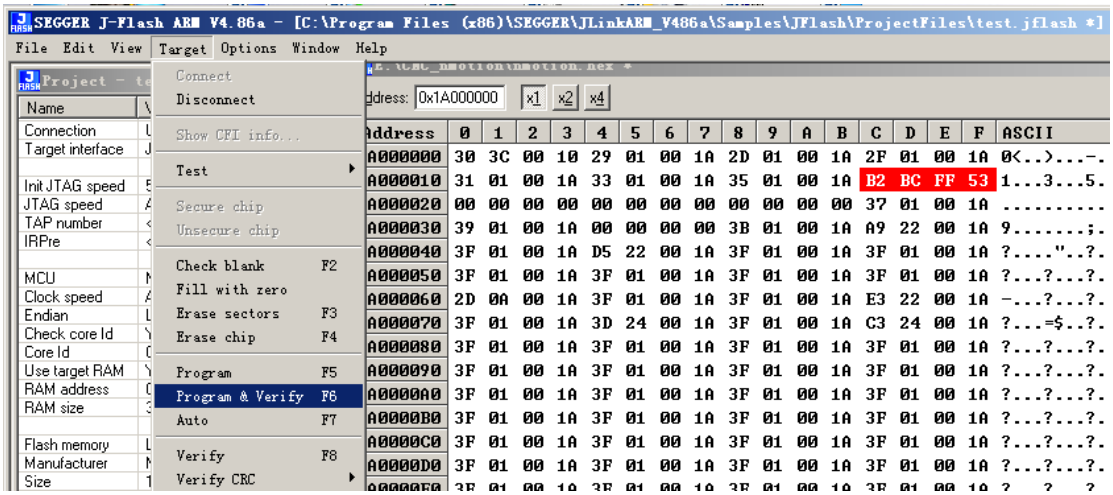


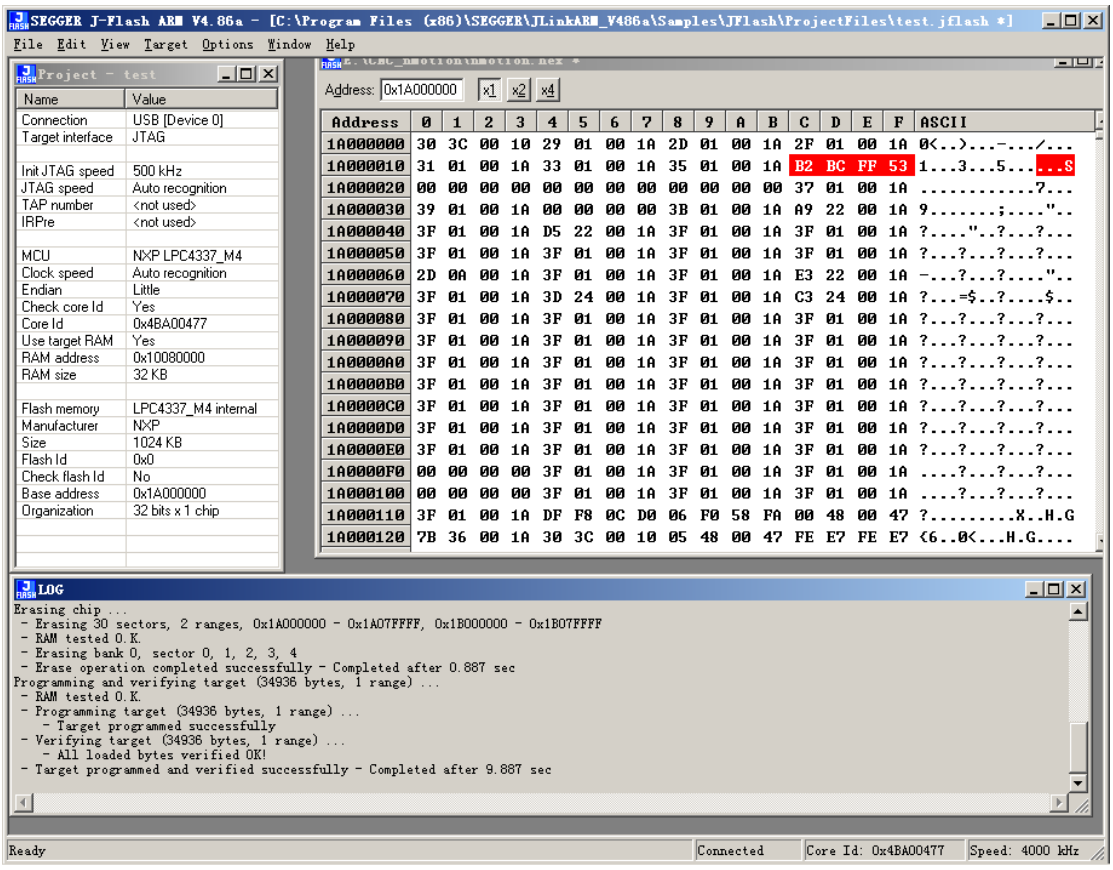


擦除芯片内部 Flash



接下来编程，出现提示要更改一段字节，点确认即可


SEGGER J-Flash ARM V4.86a - [C:\Program Files (x86)\SEGGER\JLinkARM_V486a\Samples\JFlash\ProjectFiles\test.jflash *]
File Edit View Target Options Window Help
Connect
Disconnect
Show CFI info...
Test
Secure chip
Unsecure chip
Check blank F2
Fill with zero
Erase sectors F3
Erase chip F4
Program F5
Program & Verify F6
Auto F7
Verify F8
Verify CRC
Address: 0x1A000000
x1 x2 x4
Address 0 1 2 3 4 5 6 7 8 9 A B C D E F ASCII
A000000 30 3C 00 10 29 01 00 1A 2D 01 00 1A 2F 01 00 1A
A000010 31 01 00 1A 33 01 00 1A 35 01 00 1A B2 BC FF 53
A000020 00 00 00 00 00 00 00 00 00 00 00 00 37 01 00 1A
A000030 39 01 00 1A 00 00 00 00 3B 01 00 1A A9 22 00 1A
A000040 3F 01 00 1A D5 22 00 1A 3F 01 00 1A 3F 01 00 1A
A000050 3F 01 00 1A 3F 01 00 1A 3F 01 00 1A 3F 01 00 1A
A000060 2D 0A 00 1A 3F 01 00 1A 3F 01 00 1A E3 22 00 1A
A000070 3F 01 00 1A 3D 24 00 1A 3F 01 00 1A C3 24 00 1A
A000080 3F 01 00 1A 3F 01 00 1A 3F 01 00 1A 3F 01 00 1A
A000090 3F 01 00 1A 3F 01 00 1A 3F 01 00 1A 3F 01 00 1A
A0000A0 3F 01 00 1A 3F 01 00 1A 3F 01 00 1A 3F 01 00 1A
A0000B0 3F 01 00 1A 3F 01 00 1A 3F 01 00 1A 3F 01 00 1A
A0000C0 3F 01 00 1A 3F 01 00 1A 3F 01 00 1A 3F 01 00 1A
A0000D0 3F 01 00 1A 3F 01 00 1A 3F 01 00 1A 3F 01 00 1A


SEGGER J-Flash ARM V4.86a - [C:\Program Files (x86)\SEGGER\JLinkARM_V486a\Samples\JFlash\ProjectFiles\test.jflash *]
File Edit View Target Options Window Help
Project - test
Name Value
Connection USB [Device 0]
Target interface JTAG
Init JTAG speed 500 kHz
JTAG speed Auto recognition
TAP number <not used>
IRPre <not used>
MCU NXP LPC4337_M4
Clock speed Auto recognition
Endian Little
Check core Id Yes
Core Id 0x4BA00477
Use target RAM Yes
RAM address 0x10080000
RAM size 32 KB
Flash memory LPC4337_M4 internal
Manufacturer NXP
Size 1024 KB
Flash Id 0x0
Check flash Id No
Base address 0x1A000000
Organization 32 bits x 1 chip
Address: 0x1A000000
x1 x2 x4
Address 0 1 2 3 4 5 6 7 8 9 A B C D E F ASCII
1A000000 30 3C 00 10 29 01 00 1A 2D 01 00 1A 2F 01 00 1A
1A000010 31 01 00 1A 33 01 00 1A 35 01 00 1A B2 BC FF 53
1A000020 00 00 00 00 00 00 00 00 00 00 00 00 37 01 00 1A
1A000030 39 01 00 1A 00 00 00 00 3B 01 00 1A A9 22 00 1A
1A000040 3F 01 00 1A D5 22 00 1A 3F 01 00 1A 3F 01 00 1A
1A000050 3F 01 00 1A 3F 01 00 1A 3F 01 00 1A 3F 01 00 1A
1A000060 2D 0A 00 1A 3F 01 00 1A 3F 01 00 1A E3 22 00 1A
1A000070 3F 01 00 1A 3D 24 00 1A 3F 01 00 1A C3 24 00 1A
1A000080 3F 01 00 1A 3F 01 00 1A 3F 01 00 1A 3F 01 00 1A
1A000090 3F 01 00 1A 3F 01 00 1A 3F 01 00 1A 3F 01 00 1A
1A0000A0 3F 01 00 1A 3F 01 00 1A 3F 01 00 1A 3F 01 00 1A
1A0000B0 3F 01 00 1A 3F 01 00 1A 3F 01 00 1A 3F 01 00 1A
1A0000C0 3F 01 00 1A 3F 01 00 1A 3F 01 00 1A 3F 01 00 1A
1A0000D0 3F 01 00 1A 3F 01 00 1A 3F 01 00 1A 3F 01 00 1A
1A0000E0 3F 01 00 1A 3F 01 00 1A 3F 01 00 1A 3F 01 00 1A
1A0000F0 00 00 00 00 3F 01 00 1A 3F 01 00 1A 3F 01 00 1A
1A000100 00 00 00 00 3F 01 00 1A 3F 01 00 1A 3F 01 00 1A
1A000110 3F 01 00 1A DF F8 0C D0 06 F0 58 FA 00 48 00 47
1A000120 7B 36 00 1A 30 3C 00 10 05 48 00 47 FE E7 FE E7
LOG
Erasing chip ...
- Erasing 30 sectors, 2 ranges, 0x1A000000 - 0x1A07FFFF, 0x1B000000 - 0x1B07FFFF
- RAM tested O.K.
- Erasing bank 0, sector 0, 1, 2, 3, 4
- Erase operation completed successfully - Completed after 0.887 sec
Programming and verifying target (34936 bytes, 1 range) ...
- RAM tested O.K.
- Programming target (34936 bytes, 1 range) ...
- Target programmed successfully
- Verifying target (34936 bytes, 1 range) ...
- All loaded bytes verified OK!
- Target programmed and verified successfully - Completed after 9.887 sec
Ready
Connected
Core Id: 0x4BA00477
Speed: 4000 kHz