

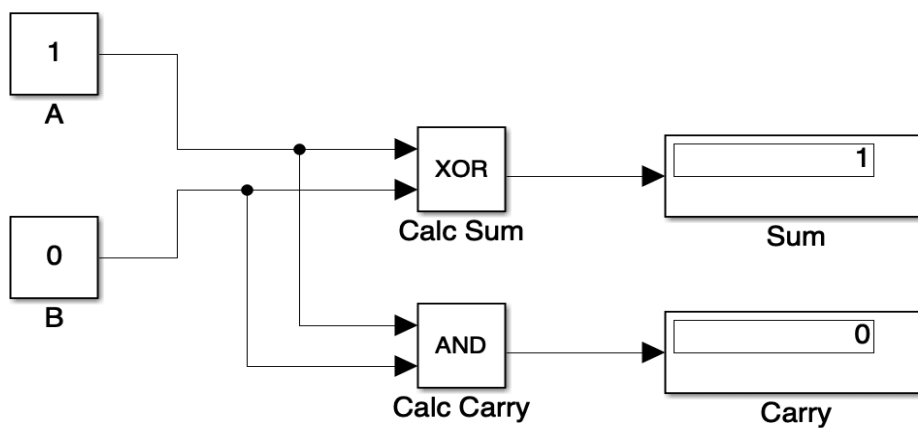
EECE2160 Spring 2020 Lab #6

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Pre-Lab Assignment

Include a screenshot of your resulting Simulink model as part of the pdf submitted in your pre-lab writeup.



Write a complete truth table for (i) a half adder, and (ii) a full adder.

1. half adder

A	B	S	C_{out}
0	0	0	0
0	1	1	0
1	0	1	0
1	1	0	1

2. full adder

A	B	C_{in}	S	C_{out}
0	0	0	0	0
0	0	1	1	0
0	1	0	1	0
0	1	1	0	1
1	0	0	1	0
1	0	1	0	1
1	1	0	0	1
1	1	1	1	1