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# CS419 Compiler Construction

## Lecture 1

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# Course Information

- **Course Number: CS419**
- **Course Name: Compiler Construction**
- **Teaching Method: Face-to-Face**

# Course Outline

- Ch. 1: Introduction
- Ch. 2: A simple syntax-directed translator
- Ch. 3: Lexical analysis
- Ch. 4: Syntax analysis
- Ch. 5: Syntax-directed translation
- Ch. 6: Intermediate code generation
- Ch. 7: Run-time environments
- Ch. 8: Code generation
- Ch. 9: Code optimization

# Course Requirement

- **Prerequisites:** Algorithms and Data Structures (CS 222 & CS223)
- **Textbook:** “Compilers: Principles, Techniques, and Tools” by A.V. Aho, M. S. Lam, R. Sethi, and J.D. Ullman

# Course Overview

- Understand the design and construction principles of programming languages' compilers
- Learn new concepts, techniques, and tools that can be applied to build compilers
- Be familiar with compiler analysis and optimization techniques

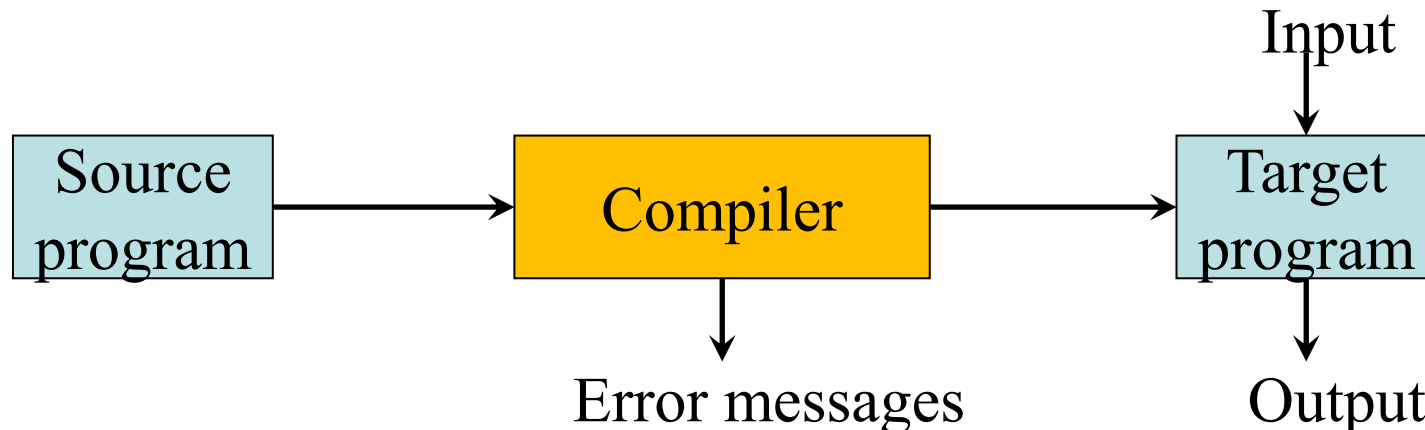
# Compilers and Interpreters

- Both compilers and interpreters are language processors
- The target program produced by a compiler is faster than an interpreter
- An interpreter provides better error diagnostics than a compiler (executes the source code)

# Compilers

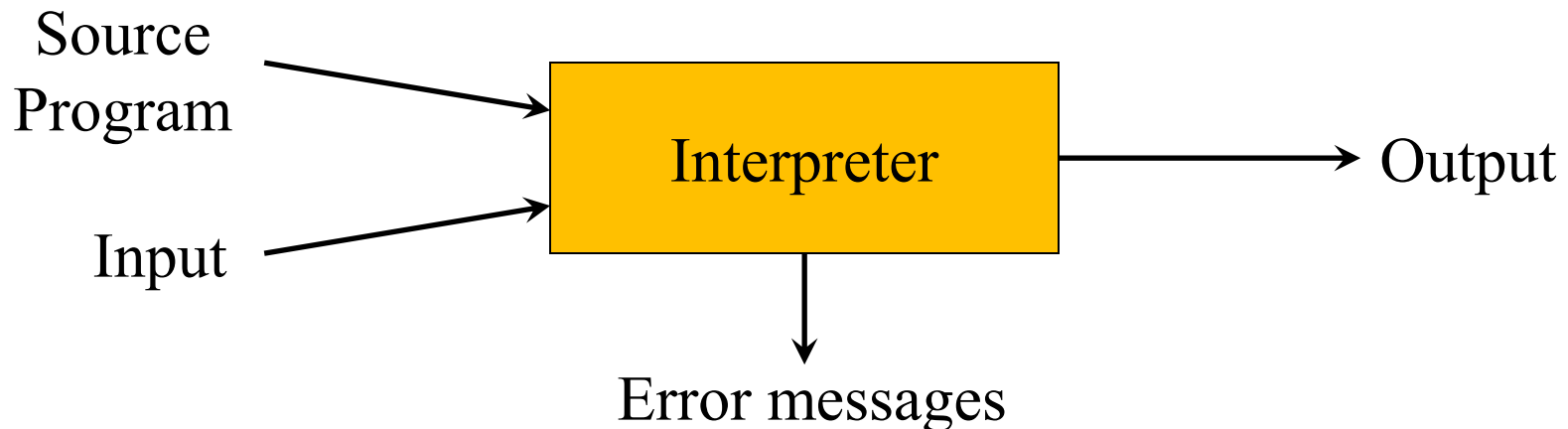
- **Compiler**

- A software (program) that translates a program in the *source* language into a semantically equivalent program in the *target* language (machine language)
- Reports the source program errors that are detected during translation



# Interpreters

- Interpreter
  - Directly performs the operations implied by the source program without producing a target program

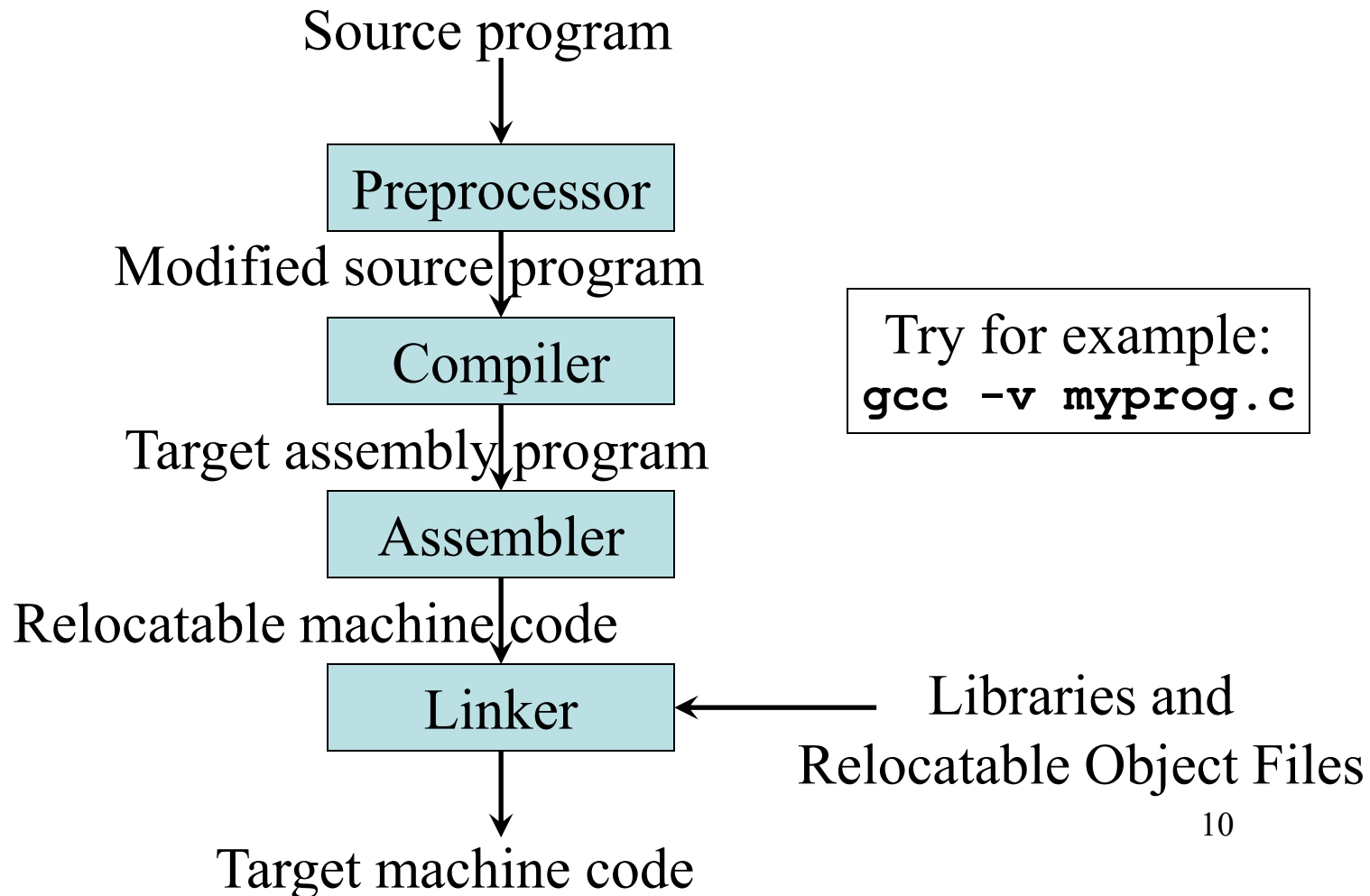




# Preprocessors, Compilers, Assemblers, and Linkers

- If the source program is divided into modules stored in separate files, the **preprocessor** collects the source program
- The **compiler** translates the source program to produce an assembly-language program
- The assembly-language program processed by an **assembler** to produce relocatable machine code
- The **linker** links the relocatable machine code with other object files and library files to produce a code that runs on the machine

# Preprocessors, Compilers, Assemblers, and Linkers



# The Analysis-Synthesis Model of Compilation

- There are two main parts of compilation:
  - *Analysis* breaks up the source program into basic pieces with grammatical structure. This structure is then represented as a tree.
  - *Synthesis* takes the tree structure and translates its operations into the target program

# The Phases of a Compiler

- The compilation process operates as a sequence of phases
- Each compilation phase transforms one form of the source program to another representation form

# The Phases of a Compiler

Phase	Output	Sample
<i>Programming</i>	Source string	<b>A=B+60</b>
<i>Lexical analysis (scanning)</i>	Token string	<b>'A', '=', 'B', '+', '60'</b> And <i>symbol table</i> for identifiers
<i>Syntax analysis (parsing)</i> (creates a tree representation that depicts the grammatical structure of the token string)	Syntax tree (each interior node represents an operation and the children of the node represent the arguments of the operation)	<pre>       =      / \     A   +      / \     B   60           </pre>
<i>Semantic analysis</i> (checks the source program for semantic consistency with the language definition: type checking and type correction)	Syntax tree	<pre>       =      / \     A   +      / \     B   inttofloat                     60           </pre>
<i>Intermediate code generation</i> (generates a machine-like representation of the source program. This representation should be easy to translate into the target machine)	Three-address code	<b>t1 = inttofloat(60)</b> <b>t2 = B + t1</b> <b>A = t2</b>

# The Phases of a Compiler ...Continued

Phase	Output	Sample
<i>Code optimization</i> (improves the intermediate code to make it faster, shorter , etc)	Three-address code, quads, or RTL	<b>t1 = inttofloat(60)</b> <b>A = B + t1</b>
<i>Code generation</i> (maps the intermediate code into the target language)	Assembly code	<b>MOVF #60.0,r1</b> <b>ADDF r1,r2</b> <b>MOVF r2,A</b>
<i>Peephole optimizer</i>	Assembly code	<b>ADDF #60.0,r2</b> <b>MOVF r2,A</b>