Digital Logic Design (CS302)

Assignment # 01

Fall 2025

Virtual University

Total marks = 20
Deadline
6th of November 2025

Please carefully read the following instructions before attempting the assignment.

RULES FOR MARKING

It should be clear that your assignment would not get any credit if:

- The assignment is submitted after the due date.
- The submitted assignment does not open or the file is corrupt.
- Strict action will be taken if the submitted solution is copied from any other student or the internet.

You should consult the recommended books to clarify your concepts as handouts are not sufficient.
You are supposed to submit your assignment in Doc or Docx format.
Any other formats like scan images, PDF, ZIP, RAR, PPT, BMP, etc. will not be accepted.

Topic Covered:

- Introduction & Overview to Digital Logic
- Number Systems
- Number Systems & Codes
- Logic Gates
- Logic Gates & Operational Characteristics
- Digital Circuits and Operational Characteristics
- Boolean Algebra and Logic Simplification

Topic Covered
Lecture 01 to Lecture 09

NOTE

No assignment will be accepted *after the due date via email in any case* (whether it is the case of load shedding or internet malfunctioning etc.). Hence refrain from uploading assignments in the last hour of the deadline. It is recommended to upload the solution at least two days before its closing date.

If you people find any mistake or confusion in the assignment (Question statement), please consult with your instructor before the deadline. After the deadline, no queries will be entertained in this regard.

For any query, feel free to email me at:
cs302@vu.edu.pk

Consider the following Floating Point Number:

$$-3105.8565$$

Convert the number into 32-bit Binary Floating Point Number (IEEE Standard 754).

Note: Perform all steps of this method like conversion of bases, additions or subtraction if required. In case of missing steps, marks will be deducted.