



Old embeeded - question bank

Embedded System (Pokhara University)



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POKHARA UNIVERSITY

Level: Bachelor

Programme: BE

Course: Embedded System

Semester: Fall

Year . : 2015

Full Marks: 100

Pass Marks: 45

Time : 3hrs.

Candidates are required to give their answers in their own words as far as practicable.

The figures in the margin indicate full marks.

Attempt all the questions.

- | | | |
|----|----------------------------------------------------------------------------------------------------------------------------------------------------------------|-----|
| 1. | a) Explain the components of embedded system hardware | 7 |
| | b) Explain with an example how to optimize custom single purpose processors. | 8 |
| 2. | a) Explain in brief about combinational and sequential logic? | 7 |
| | b) Define Target and Development processor. Explain three ways to test the program intended for Embedded System. | 8 |
| 3. | a) What is Memory Hierarchy. Explain the write ability and Storage performance of Memory devices | 7 |
| | b) Sketch the internal design of 8X4 ROM Memory. Explain different types of ROM. | 8 |
| 4. | a) Why DMA is used? Explain in detail. | 8 |
| | b) Describe wireless communication protocols used in embedded system. | 7 |
| 5. | a) List three ways in which an RTOS handles the ISRs in a multitasking environment. | 9 |
| | b) Write down the truth table and VHDL code for 3 bit counter. | 6 |
| 6. | a) Write an assembly language program based on 8051 to display the number of bottles filled with milk by a bottlers company. Also show the Connection Diagram. | 7 |
| | b) Write a VHDL code for octal to binary encoder. | 8 |
| 7. | Write short notes on: (Any two) | 2×5 |
| | a) Direct mapping | |
| | b) Cross-Compiler | |
| | c) Task, task states and task Scheduling | |

POKHARA UNIVERSITY

Level: Bachelor
 Programme: BE
 Course: Embedded System

Semester: Fall

Year : 2016
 Full Marks: 100
 Pass Marks: 45
 Time : 3hrs.

Candidates are required to give their answers in their own words as far as practicable.

The figures in the margin indicate full marks.

Attempt all the questions.

- | | | |
|---------------------------------|--------------------------------------------------------------------------------------------------------------------------------------------------------|-----|
| a) | Define Embedded System? Explain the different characteristics of Embedded System. Give few application areas. | 7 |
| b) | Design a synchronous sequential machine that produces output 1 when input sequence is 1011 using JK flip – flop. | 8 |
| a) | Design a custom single – purpose processor to calculate GCD between two integers. | 8 |
| b) | Explain general purpose processor design with a suitable diagram. | 7 |
| a) | Design 4KX8ROMs using 1KX8 ROMs. (1K=1024 words) | 7 |
| b) | What is arbitration? Explain the steps used in Daisy - Chain arbitration with a block diagram. | 8 |
| a) | Describe the major functions of real – time kernel. | 8 |
| b) | Explain Vectored Interrupt with a neat diagram. | 7 |
| a) | Define Debugger, Downloader and Cross – Assembler. | 7 |
| b) | Write an assembly level program for 8051 to transfer a word "POKHARA" stored in ROM starting at location 250H to RAM starting at locations 50H onward. | 8 |
| a) | Explain different modeling styles in VHDL. | 7 |
| b) | Write a VHDL code which results an output '1' when a sequence "1101" is detected else an output results a '0'. | 8 |
| Write short notes on: (Any two) | | 2x5 |
| a) | DMA | |
| b) | Clocking communication and Task synchronization | |
| c) | Combinational logic | |

POKHARA UNIVERSITY

Level: Bachelor
Programme: BE
Course: Embedded System

Semester: Spring

Year: 2016
Full Marks: 100
Pass Marks: 45
Time: 3 hrs.

Candidates are required to give their answers in their own words as far as practicable.

The figures in the margin indicate full marks.

Attempt all the questions.

- 1) What is embedded system? Explain the components of embedded hardware. 7
- 1) Explain with an example how to optimize custom single purpose processors. 8
- 1) Draw the combinational logic design for three inputs a, b & c and two outputs y & z. The output 'y' is such that y is 1 if a is 1 or b & c is 1 and z is 1 if b or c is 1 but not both. 7
- 1) How does a programmer view a microprocessor based embedded system? What are his/her concern? 8
- 1) Compose $2^{(k+1)} \times 2^n$ memory using $2^k \times n$ memory modules. 7
- 1) What is interrupt? Explain the steps used in data transfer using vector interrupt along with its flowchart. 8
- 1) Explain any two arbitration techniques that implemented to communicate with peripheral devices from the microprocessor. 7
- 1) List out the difference between process and thread? Explain various state of process. 8
- 1) Define Real Time Operating System. Differentiate between clocking communication and task synchronization. 7
- 1) Explain briefly the architecture of 8051 microcontroller with the aid of block diagram. 8
- a) Give an introduction to VHDL and explain the basic structure of a VHDL file with examples. 7
- b) Write a VHDL program for which output will be 1 when the sequence 101 is detected. 8

Write short notes on: (Any two)

2x5

- a) Task, task states and task scheduling
- b) Cross compiler and cross assembler
- c) VHDL program to simulate binary adder

Candidates are required to give their answers in their own words as far as practicable.

The figures in the margin indicate full marks.

Attempt all the questions.

with necessary state transition in system with state diagram & write the VHDL code for those system for testing purpose.
Write short notes on: (Any two)
Cross Compiler and Cross Assembler
Memory write and Storage Permanence
VHDL code for 2-bit multiplier

What is embedded system? Explain essential components of 7 embedded system?

Why single purpose processor is required when general purpose processors can execute a variety of programs and are readily available? Give reasons.

How can you design general purpose processor? Explain with 7 necessary steps & diagram.

How does a programmer view a microprocessor based embedded 8 system? what are his/her concern?

Explain briefly the different cache mapping techniques with suitable 7 diagrams

Explain the benefits that an interrupt address table has over the fixed 8 and vectored interrupt methods.

What do you mean by arbitration? Explain the daisy chain and 7 network-oriented arbitration briefly

What are task states? Describe task scheduling.

What is semaphore? How semaphore can be used for global resource 7 sharing?

Draw the block diagram of 8051 microcontroller also write an 8 assembly level program to interface a seven segment display with 8051 microcontroller. (You can give any example)

Discuss the various styles of modeling used in architecture body of 7 hardware description in VHDL with suitable example

Suppose customer 'A' need to detect 1101 by his/her system. Then, 8 design a system, which can fulfill the customer need. Also, explain

1

2

POKHARA UNIVERSITY

Level: Bachelor
Programme: BE
Course: Embedded Systems

Semester: Spring

Year : 2017
Full Marks: 100
Pass Marks: 45
Time : 3hrs.

Candidates are required to give their answers in their own words as far as practicable.

The figures in the margin indicate full marks.

Attempt all the questions.

- a) What is an Embedded System? List and define the three main characteristics of embedded systems that distinguish embedded system from other computing systems. 7
- b) What is optimization? Explain optimization of single purpose processor in detail. 8
- a) Design a processor that calculates the GCD of two numbers. Show the design of datapath only and construct the diagram of controller. 7
- b) How does a programmer view a microprocessor based embedded system? what are his/her concern? 8
- a) Compose $1K \times 8$ ROM into an $2K \times 16$ ROM. 7
- b) What is Direct Memory Access? Why such circuitry is needed? Explain with its block diagram. 8
- a) What is Arbitration. Explain Daisy-Chain arbitration. 7
- b) In an RTOS environment different tasks may share same variables and functions. Explain the problems faced due to this type of sharing and also suggest the solutions. 8
- a) Define Real time operating System? Explain various stages of task. 7
- b) Explain the architecture of 8051 microcontroller with the aid of its block diagram. Also explain the addressing modes of 8051. 8
- a) Write a VHDL code to simulate 8×1 MUX. 7
- b) Give the VHDL implementation of full adder using both behavioral and structural architecture. And then point out the difference between the two architectures. 8

Write short notes on: (Any two)

2×5

- a) Types of Memory
- b) Debuggers and downloader
- c) VHDL code to simulate shift register

POKHARA UNIVERSITY

Level: Bachelor
Programme: BE
Course: Embedded Systems

Semester: Fall

Year : 2018
Full Marks: 100
Pass Marks: 45
Time : 3hrs.

Candidates are required to give their answers in their own words as far as practicable.

The figures in the margin indicate full marks.

Attempt all the questions.

- | | |
|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|-----|
| What is an Embedded system? Justify, how automatic fuel machine is a good example of embedded system? | 7 |
| Design a circuit to implement 2-bit gray counter. | 8 |
| Suppose, you are appointed as an officer in a criminal investigation department, are provided a lot of phone record for analysis. How you will solve this problem & which processor is suitable for this analysis purpose? | 7 |
| Explain the programmer view in the embedded system. | 8 |
| Describe a way to fulfill a requirement of 18 memory locations each 8 bit wide using 16X4 memory chips. | 7 |
| Why we need DMA? Explain the working principle of DMA. | 8 |
| Compare and contrast bus-based I/O and port-based I/O. | 7 |
| In an RTOS environment different tasks may share same variables and functions. Explain the problems faced due to this type of sharing and also suggest the solutions. | 8 |
| Explain memory fragmentation? How problem related to memory fragmentation can be solved in embedded systems? | 8 |
| Explain, with necessary diagrams and equations, how a MOSFET can be used as a switch. | 7 |
| Write an ALP in 8051 to implement seven segment and implement counter that counts two digit hexadecimal number. | 8 |
| Write a VHDL program for a 4-bit full adder. | 7 |
| Write short notes on: (Any two) | 2x5 |
| () Associative cache mapping | |
| () Control Blocks | |
| () Structural and Behavioural Model | |

POKHARA UNIVERSITY

Level: Bachelor	Semester: Fall	Year : 2019
Programme: BE		Full Marks: 100
Course: Embedded System		Pass Marks: 45
		Time : 3 hrs.

Candidates are required to give their answers in their own words as far as practicable.
The figures in the margin indicate full marks.

Attempt all the questions.

1. a) Define Embedded System. Explain essential components of Embedded System? 7
- b) Design an overlapping sequence detector for the sequence 1010 8
2. a) Design a sequential circuit to implement 2-bit counter with an input that controls up (increase value) or down (decrease value) count. 8
- b) Explain the firmware development process with necessary block diagram? 7
3. a) Explain different types of RAM with RAM variation. 8
- b) What are methods that can be employed to find the address of interrupt service routine? Discuss a generalized DMA process. 7
4. a) Explain Daisy-chain arbitration in detail. What is the significant of Multilevel bus architecture 8
- b) Define Real time operating System? Explain Round Robin and Pre-emptive Scheduling Policies. 7
5. a) Explain, with necessary diagrams and equations, how a MOSFET can be used as a switch. 8

OR

- What are cross compilers and assemblers? Why do we require such type of compilers and assemblers while designing embedded system?
- b) Write an assembly language program based on 8051 to send data "Hello" through UART (with baud rate of 9600, 8 bit data, no parity, 1 stop bit and no handshake) in every 500 ms using internal timer of the microcontroller. Show the necessary calculation and connection diagram. 7

6. a) Write a VHDL program for which output will be 1 when the sequence 7

- 1011 is detected considering overlapping condition.
- b) Write down the VHDL code for an adder that takes in two 4-bit number and gives their sum as output. 8
7. Write short notes on: (Any two) 2x5
- a) Fan out
 - b) Memory Management
 - c) Multi-level bus architecture

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Con - 4

Level: Bachelor **Semester:** Spring **Year :** 2019
Programme: BE **Full Marks:** 100 **Pass Marks:** 45
Course: Embedded System **Time :** 3 hrs.

Candidates are required to give their answers in their own words as far as practicable.

The figures in the margin indicate full marks.

Attempt all the questions.

1. a) List and define the three main characteristics of embedded system that distinguish such systems from other computing system. 7
- b) State the differences between combinational and sequential circuits? 8
2. a) Define sequential circuit design process with a suitable example. 7
- b) Define datapath and control units with its sub operations. 7
3. a) Design a custom single-purpose processor to calculate LCM (least common multiple) between two integers. 8
- b) Why is cache memory needed? Explain the principle of operation of EPROM with necessary illustrations. 8
4. a) Describe how the memory modules can be composed. Use a suitable example describing all the three cases. 7
- b) What do you understand by interrupt handler? Explain three method by which RTOS handles interrupt. 8
5. a) What do you mean by kernel? Describe the types of RT kernel. 7
- b) Define Debugger, Downloader and Cross-Assembler. 8
6. a) Write an assembly language program for 8051 micro controller to display the number from 0 to 9 in a seven segment display so that the number is updated in every 1 sec. User internal timer of the 8051 to generate 1 sec delay. Show necessary connection diagram and calculation. 8
- a) Write a VHDL program for which output will be 1 when the sequence 1101 is detected considering overlapping condition. 8
- b) Write a program in VHDL implement 8:1 multiplexer. 7
7. Write short notes on: (Any two)
 - a) Cross compiler and Native Compiler 6
 - b) DMA
 - c) Types of memory

Level: Bachelor **Semester/Fall** **Year :** 2020
Programme: BE **Full Marks:** 100 **Pass Marks:** 45
Course: Embedded Systems **Time :** 3 hrs.

Candidates are required to give their answers in their own words as far as practicable.

The figures in the margin indicate full marks.

Attempt all the questions.

1. a) What is an embedded system? What are the main constraints that we should know to develop a firmware in embedded system? 7
- b) Define Combinational and Sequential Circuit. Design a NOR gate using CMOS transistor. 8
2. a) Define Optimization. Explain the different optimization opportunities. 8
- b) What are the key factors a programmer needs to consider when choosing a general-purpose controller? 7
3. a) What do you understand by cache memory? Discuss about cache-replacement policy and cache write techniques. 8
- b) Compose 2Kx16 ROM using 1Kx8 ROM. 7
4. a) Define Arbitration. Explain daisy chain arbitration with necessary diagram. 8
- b) List out the differences between Process and Thread. 7
5. a) What do you understand by TCB in RTOS? What are the information contents of TCB? 8
- b) Write an assembly language program for 8051 microcontroller to blink a LED in every one second. User internal timer interrupt to generate the required delay. Show necessary connection diagram and calculation. 7
6. a) Explain different Modeling Styles with an example in VHDL. 9
- b) Write a VHDL code for 4:1 MUX using structural Modeling style. 6

POKHARA UNIVERSITY

Level: Bachelor

Semester: Fall

Year : 2021

Programme: BE

Full Marks: 100

Course: Embedded Systems

Pass Marks: 45

Time : 3hrs.

Candidates are required to give their answers in their own words as far as practicable.

The figures in the margin indicate full marks.

Attempt all the questions.

- | | | |
|----|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|-----|
| 1. | a) State Moore's Law. Explain different characteristics that differentiate embedded system from other computing. | 7 |
| | b) Design a 3-bit gray code counter using JK flip – flop. | 8 |
| 2. | a) Define target and development processor. Explain any three ways of testing the program intended for embedded system. | 7 |
| | b) Explain the optimization of single purpose processor in detail. | 8 |
| 3. | a) Combine 2kx4 ROM to get 6kx8 ROM | 7 |
| | b) Differentiate interrupt driven I/O using fixed and vectored interrupt. | 8 |
| 4. | a) What are the advantages of interrupt over DMA? Explain the operation of peripheral to memory transfer with DMA | 8 |
| | b) What is binary semaphore? Explain the usage of semaphore and mutex with proper example. | 7 |
| 5. | a) Why RTOS are preferred in embedded system? Differentiate between clocking communication and task synchronization. | 7 |
| | b) Write an assembly language program for 8051 microcontroller to blink LED 3 times if user sends character 'a' through UART, and blink LED 5 times if user sends 'b' through UART. Show necessary calculation and connection diagram. | 8 |
| 6. | a) Write a program in VHDL implement 4:1 multiplexer. | 7 |
| | b) What are the advantages of using VHDL instead of any other HDL? Explain different modelling styles in VHDL with illustration. | 8 |
| 7. | Write short notes on: (Any two) | 2×5 |
| | a) Cross Assemblers | |
| | b) Set associative Cache mapping | |
| | c) Arbitration | |

POKHARA UNIVERSITY	
Level: Bachelor	Semester: Spring
Programme: BE	
Course: Embedded System	
Year	
Full Marks : 2021	
Pass Marks: 100	
Time : 3 hrs.	

Candidates are required to give their answers in their own words as far as practicable.
The figures in the margin indicate full marks.
Attempt all the questions.

1. a) How embedded design differs from conventional computing? Explain the basic structure of an embedded system. 7
- b) Design 1101 sequence detector (Overlapping) using D flip-flop. 7
2. a) Design custom single-purpose processor to find the greatest among three numbers. Your design should include algorithm, FSDM and data path. 8
- b) Define control unit of general-purpose processor? Explain sub-operations of the instruction cycle. 8
3. a) How can we get 32x12 ROM from 16x8 ROM? 6
- b) What is arbitration? Explain Daisy chain arbitration with necessary diagram. 8
4. a) Explain RTOS task and its states. 7
- b) Define Monolithic, Micro and Exo kernel. 8
5. a) Why RTOS are preferred in embedded system? Differentiate between clocking communication and task synchronization. 7
- b) Write a program for 8051 microcontrollers to provide the on/off time to three traffic lights (Green, Yellow, and Red) and two pedestrian signs (WALK and DON'T WALK), as shown in table below 8

Lights	On Time
1. Green	15 seconds
2. Yellow	5 seconds
3. Red	20 seconds
4. WALK	15 seconds
5. DON'T WALK	25 seconds

The Green, DON'T WALK sign should be turned on together, and the Red and DON'T WALK sign should be turned on together, the Red and the turn on time should be turned on together, and the Yellow and the turn on time should be turned on together. The Red and the turn on time should be turned on together, and the Yellow and the turn on time should be turned on together. Use timer as much as possible.

Differentiate between advantages of using VHDL and Verilog.

Write short notes on: (Any two)

- Cross Assemblers
- Address Decoding
- Storage permanence and write ability of memories

What are the advantages of using VHDL instead of any other HDL?

- Cross Assemblers
- Address Decoding
- Storage permanence and write ability of memories

2x5

POKHARA UNIVERSITY

Level: Bachelor **Semester:** Fall **Year :** 2022
Programme: BE **Full Marks:** 100 **Pass Marks:** 45
Course: Embedded Systems **Time :** 3hrs.

Candidates are required to give their answers in their own words as far as practicable.

The figures in the margin indicate full marks.

Attempt all the questions.

1. a) What is an embedded System? List and define the three main characteristics of embedded system that distinguish embedded system from other computing systems. 7
 1. b) Design a synchronous sequential machine that produce output when input sequence is 1101 using RS flip-flop. 8
 2. a) Design a custom single purpose processor that generates Fibonacci series up to n places. Start with a function computing the desired result. translate it into a state diagram, and sketch a probable data path. 9
 2. b) Define Moore's law. Design an XOR gate using CMOS technology. Explain its working with the help of truth table. 6
 3. a) Given the following three cache designs. find the one with the best performance by calculating the average cost of access. Show all calculations. 9
 3. i. 4 Kbyte, 8-way set associative cache with a 6% miss rate; cache hit costs one cycle, cache miss costs 12 cycles. 7
 3. ii. 8 Kbyte, 4-way set-associative cache with a 4% miss rate; cache hit costs two cycles, cache miss costs 12 cycles. 7
 3. iii. 16 Kbyte, 2-way set-associative cache with a 2% miss rate; cache hit costs three cycles, cache miss costs 12 cycles. 7
 4. a) Design 4K \times 8 ROM using 1K \times 8 ROMs. 6
 4. b) What do you mean by arbitration? Explain the daisy chain and network oriented arbitration briefly. 8
 5. a) Define task and explain its states. What does TCB contain? Explain. 7
5. a) What is semaphore? How semaphore can be used for global resource sharing? 8
- b) Write an assembly language program for 8051 to transfer a word "POKHARA" stored in ROM starting at location 240H to RAM starting at locations 50H onward. 7
6. a) What are different styles of modeling technique used in VHDL? Describe them. 8
- b) Write a VHDL code for 8:1 MUX using structural Modeling style. 7
7. Write short notes on: (Any two) 2x5
7. a) Vector interrupt
 7. b) Cross compiler and cross assembler
 7. c) The Real-time kernel