PIC HOA 6

MAR 3 1 2021

 $f = 1kH_2 \quad T = 1ms$ $T_{DAC-UPDATE} = \frac{1ms}{3.90625 \text{ MS}} = 3.90625 \text{ MS}$ $f_{act} = (4\text{MS}) \cdot (256) = 976.5626$

Flow Chart

DACIDATLED

Delay for

4 US

(rounding up)

Increase DACIDATL

output by 1

Note: Since the.

DAC1 is an 8-bit
register, the rollover
will cause the
register to loop
back to 0x00