



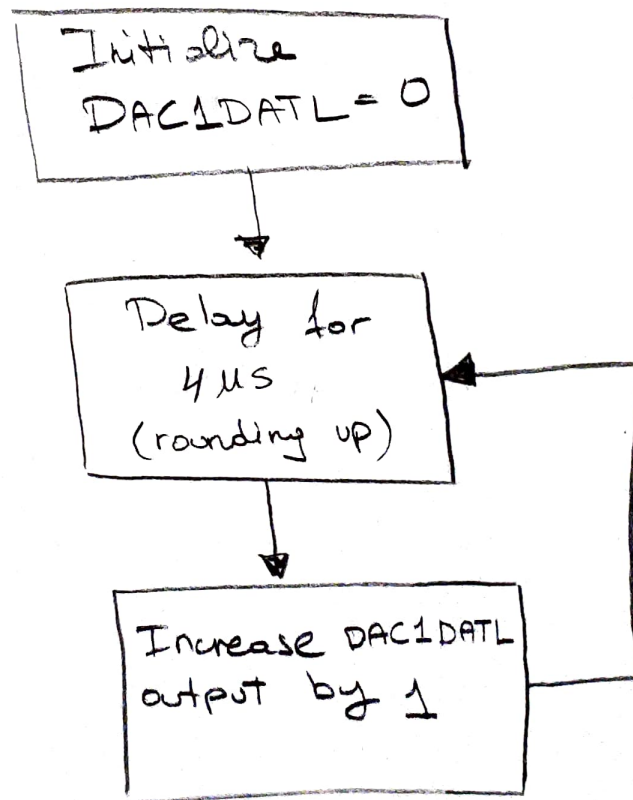
256

$$f = 1\text{kHz} \quad T = 1\text{ms}$$

$$T_{\text{DAC-UPDATE}} = \frac{1\text{ms}}{256} = 3.90625\mu\text{s}$$

$$f_{\text{act}} = \frac{1}{(4\mu\text{s}) \cdot (256)} = 976.5625$$

Flow Chart



Note: Since the DAC1 is an 8-bit register, the rollover will cause the register to loop back to 0x00