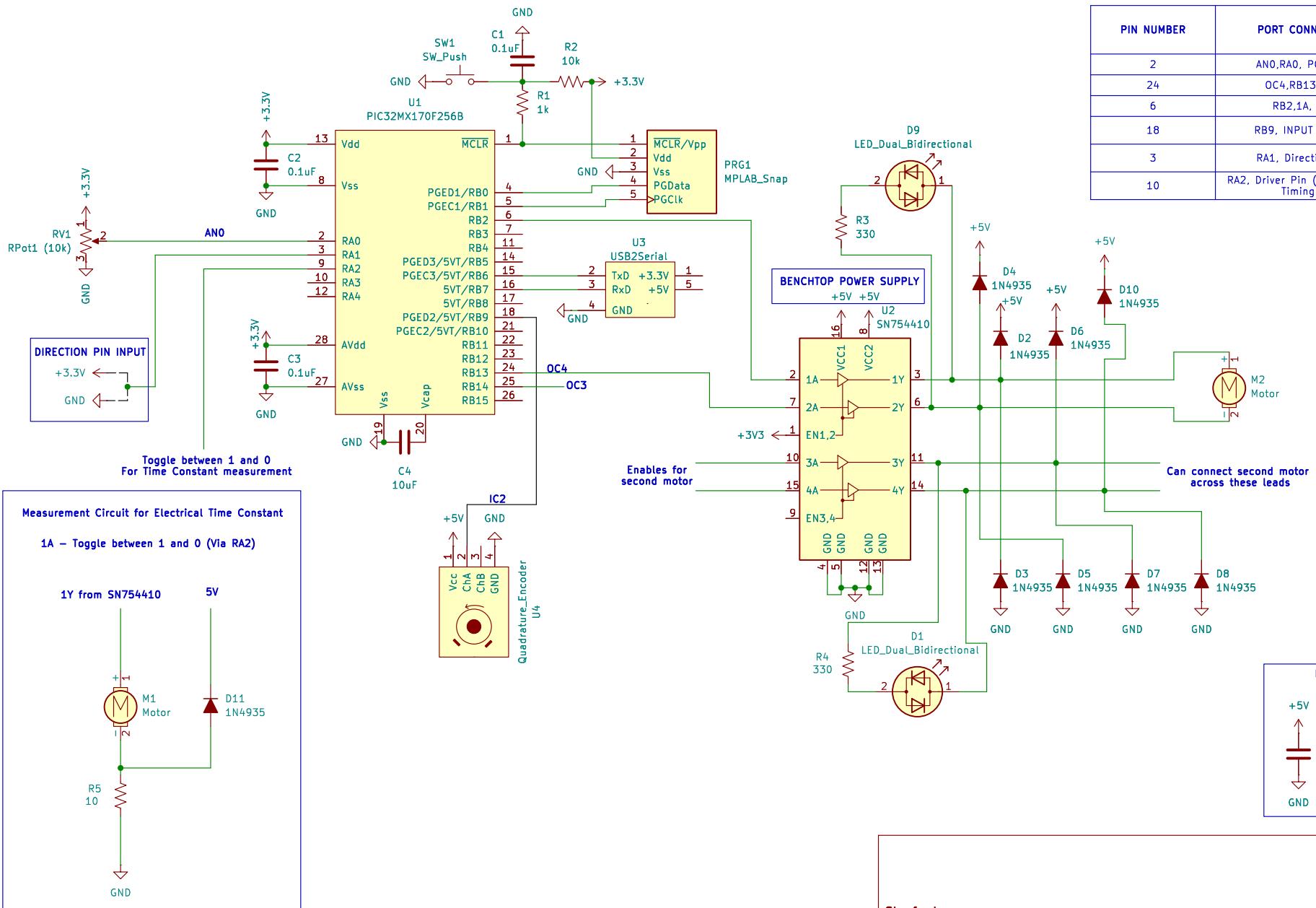


PIN NUMBER	PORT CONNECTIONS
2	AN0,RA0, POT INPUT
24	OC4,RB13, IN 2A
6	RB2,1A, IN 1A
18	RB9, INPUT CAPTURE
3	RA1, Direction Input
10	RA2, Driver Pin (Square Wave), Timing Pin



Stanford

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Title: Lab 7

Size: A4 Date: 2026-01-28  
KiCad E.D.A. 9.0.7

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