

Ejercicio 3:

Complete la tabla con el estado de las señales sin mirar el libro.

Indique con X las condiciones no-importa.

Instr	Reg2Loc	ALUSrc	MemtoReg	RegWrite	MemRead	MemWrite	Branch	AluOp1	AluOp0
R-type	0	0	0	1	0	0	0	1	0
LDUR	K	1	1	1	1	0	0	0	0
STUR	1	1	K	0	0	1	0	0	0
CBZ	1	0	K	0	0	0	1	0	1

Tipo R

$R[Rd] = R[Rn] + R[Rm]$

R opcode 21 20 Rm 16 15 shamt 10 9 Rn 5 4 Rd 0

Instr.	Reg2Loc	ALUSrc	MemtoReg	RegWrite	MemRead	MemWrite	Branch	ALUOp
Tipo R	0	0	0	1	0	0	0	10

LDUR

$R[Rt] = M[R[Rn] + DTAddr]$

D opcode 21 20 DT address 12 11 10 9 op 8 5 Rn 4 Rt 0

Instr.	Reg2Loc	ALUSrc	MemtoReg	RegWrite	MemRead	MemWrite	Branch	ALUOp
LDUR	X	1	1	1	1	0	0	00

CBZ

if(R[Rt]==0)
PC = PC + CondBranchAddr

CB Opcode 31 24 23 COND BR address 5 4 Rt 0

Instr.	Reg2Loc	ALUSrc	MemtoReg	RegWrite	MemRead	MemWrite	Branch	ALUOp
CBZ	1	0	X	0	0	0	1	01