ARM:

400464:	f8627aa0	ldr	x0, [x21, x2, lsl #3]	
400468:	91000673	add	x19, x19, #0x1	
40046c:	ca5c201c	eor	x28, x0, x28, lsr #8	
400470:	aa1403e0	mov	x0, x20	
400474:	94002261	bl	408df8 <_IO_getc>	
400478:	ca000382	eor	x2, x28, x0	
40047c:	3100041f	cmn	w0, #0x1	
400480:	92401c42	and	x2, x2, #0xff 400464 <main+0x74> //</main+0x74>	
400484:	54ffff01	b.ne	b.any	
RISCV:				
10226:	6398	ld	a4,0(a5)	
10226: 10228:	6398 405		a4,0(a5) s0,s0,1	
		addi		
10228:	405	addi xor	s0,s0,1	
10228: 1022a:	405 00d74d33 8526	addi xor mv	s0,s0,1 s10,a4,a3	
10228: 1022a: 1022e:	405 00d74d33 8526	addi xor mv jal	s0,s0,1 s10,a4,a3 a0,s1	
10228: 1022a: 1022e: 10230:	405 00d74d33 8526 039060ef	addi xor mv jal xor	s0,s0,1 s10,a4,a3 a0,s1 ra,16a68 <_IO_getc>	
10228: 1022a: 1022e: 10230: 10234:	405 00d74d33 8526 039060ef 00ad47b3 0ff7f793	addi xor mv jal xor	s0,s0,1 s10,a4,a3 a0,s1 ra,16a68 <_IO_getc> a5,s10,a0	
10228: 1022a: 1022e: 10230: 10234: 10238:	405 00d74d33 8526 039060ef 00ad47b3 0ff7f793	addi xor mv jal xor andi	s0,s0,1 s10,a4,a3 a0,s1 ra,16a68 <_IO_getc> a5,s10,a0 a5,a5,255	
10228: 1022a: 1022e: 10230: 10234: 10238:	405 00d74d33 8526 039060ef 00ad47b3 0ff7f793	addi xor mv jal xor andi slli add	s0,s0,1 s10,a4,a3 a0,s1 ra,16a68 <_IO_getc> a5,s10,a0 a5,a5,255 a5,a5,0x3	