

ARM and MIPS:

```
int
_IO_feof (fp)
_IO_FILE* fp;
{
    int result;
    CHECK_FILE (fp, EOF);
    _IO_flockfile (fp);
    result = _IO_feof_unlocked (fp);
    _IO_funlockfile (fp);
    return result;
}
```

RISCV:

```
int
_IO_feof (_IO_FILE *fp)
{
    int result;
    CHECK_FILE (fp, EOF);
    if (!_IO_need_lock (fp))
        return _IO_feof_unlocked (fp);
    _IO_flockfile (fp);
    result = _IO_feof_unlocked (fp);
    _IO_funlockfile (fp);
    return result;
}
```