opcode	Opcode Hex	Operation	Type	Reg Dst	Jump	branch	bneq	mem Read	mem to reg	Mem write	Alu src	op	Alu op	Reg write	1st Byte	2nd Byte	3rd Byte	EPROM code
0	0000	lw	I	1	0	0	0	1	1	0	1	add	0	1	8	D	1	8D1
1	0001	add	R	0	0	0	0	0	0	0	0	add	0	1	0	0	1	001
2	0010	addi	I	1	0	0	0	0	0	0	1	add	0	1	8	1	1	811
3	0011	nor	R	0	0	0	0	0	0	0	0	nor	4	1	0	0	9	009
4	0100	bneq	I	1	0	0	1	0	0	0	0	sub	1	0	9	0	2	902
5	0101	subi	I	1	0	0	0	0	0	0	1	sub	1	1	8	1	3	813
6	0110	sub	R	0	0	0	0	0	0	0	0	sub	1	1	0	0	3	003
7	0111	ori	I	1	0	0	0	0	0	0	1	or	3	1	8	1	7	817
8	1000	beq	I	1	0	1	0	0	0	0	0	sub	1	0	A	0	2	A02
9	1001	sll	S	1	0	0	0	0	0	0	1	sll	5	1	8	1	B	81B
10	1010	srl	S	1	0	0	0	0	0	0	1	srl	6	1	8	1	D	81D
11	1011	and	R	0	0	0	0	0	0	0	0	and	2	1	0	0	5	005
12	1100	jump	J	1	1	0	0	0	0	0	0	add	0	0	C	0	0	C00
13	1101	andi	I	1	0	0	0	0	0	0	1	and	2	1	8	1	5	815
14	1110	or	R	0	0	0	0	0	0	0	0	or	3	1	0	0	7	007
15	1111	sw	I	1	0	0	0	0	0	1	1	add	0	0	8	3	0	830

add	0
sub	1
and	2
or	3
nor	4
sll	5
srl	6