Simplification Using Boolean Algebra & Universal Gates

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$$\overline{(AB+c)(A+Bc)} = \overline{(AB+c)} + \overline{(A+Bc)}$$

$$= \overline{(AB)}\overline{c} + \overline{A}\overline{(Bc)}$$

$$= \overline{(A+B)}\overline{c} + \overline{A}\overline{(B+c)}$$

$$\overline{(A+B+C)D} = \overline{A+B+C} + \overline{D}$$

$$= \overline{A} \overline{B} \overline{Z} + \overline{D}$$

$$\overline{AB} + \overline{C}D + EF = (\overline{AB})(\overline{C}D)(\overline{EF})$$

$$= (\overline{A} + \overline{B})(\overline{C} + \overline{D})(\overline{E} + \overline{F})$$

$$= (\overline{A} + B)(C + \overline{D})(\overline{E} + \overline{F})$$

$$\overline{(A+B)} + \overline{c} = \overline{(A+B)}(\overline{c}) = \overline{(A+B)}c$$

$$\overline{(A+B)} = \overline{(A+B)} = \overline{(A+B)} = \overline{b}$$

$$= \overline{(A+B)} + \overline{c} + \overline{b}) = F$$

$$= \overline{(A+B)} + \overline{c} + \overline{b}) = F$$

$$= \overline{(A+B)} + \overline{c} + \overline{b}) = F$$

$$\overline{A+B\overline{c}} + D(\overline{E+\overline{F}}) = (\overline{A+B\overline{c}}) (\overline{D}(\overline{E+\overline{F}}))$$

$$= (A+B\overline{c}) (\overline{D} + \overline{E+\overline{F}})$$

$$= (A+B\overline{c}) (\overline{D} + E+\overline{F})$$

a)
$$AB + A(B+c) + B(B+c) = AB + AB + Ac + BB + Bc$$

 $= AB + Ac + B + Bc$
 $= AB + Ac + B(1+c)$
 $= AB + Ac + B$
 $= B(A+1) + Ac$
 $= B + Ac$

b)
$$\overline{AB} + \overline{AC} + \overline{ABE} = \overline{A} + \overline{B} + \overline{A} + \overline{E} + \overline{ABE}$$

$$= \overline{A} + \overline{B} + \overline{E} + \overline{ABE} = \overline{A}$$

$$= \overline{A} + \overline{B} + \overline{E} + \overline{ABE}$$

$$= \overline{A} + \overline{B} + \overline{E}$$

$$= \overline{ABC}$$

e)
$$\begin{bmatrix} A\overline{B} (C+BD) + \overline{A}\overline{B} \end{bmatrix} C = (A\overline{B}C + A\overline{B}BD) + \overline{A}\overline{B} C$$

$$= (A\overline{B}C + A.O.D + \overline{A}\overline{B}) C \begin{bmatrix} B\overline{B}B = 0 \end{bmatrix}$$

$$= (A\overline{B}C + O + \overline{A}\overline{B}) C \begin{bmatrix} A.O = 0 \end{bmatrix}$$

$$= (A\overline{B}C + \overline{A}\overline{B}) C \begin{bmatrix} A + O = 0 \end{bmatrix}$$

$$= A\overline{B}CC + \overline{A}\overline{B}C$$

$$= A\overline{B}C C + \overline{A}\overline{B}C \begin{bmatrix} CC = C \end{bmatrix}$$

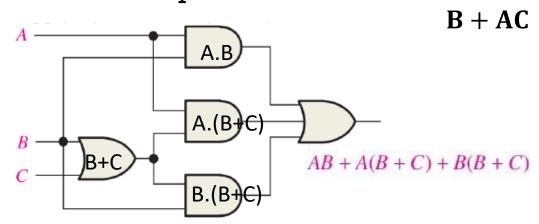
$$= \overline{B}C(A+\overline{A}) [A+\overline{A}=1]$$

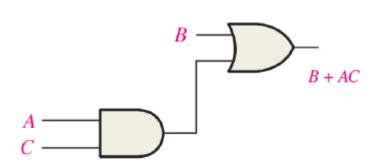
$$= \overline{B}C [A^{-1} = \overline{A}]$$

- A large Boolean Logic Expression can often be simplified to a simpler and shorter Boolean Logic Expression. This is done by applying the laws and rules of Boolean Algebra.
- Using Boolean algebra techniques, simplify this expression:

$$AB + A(B + C) + B(B + C)$$

This can be simplified to:





LOGIC SIMPLIFICATION & IMPLEMENTATION

Simplify the Boolean expression $A\overline{B} + A(\overline{B+C}) + B(\overline{B+C})$.

Simplify the following Boolean expression:

$$[A\overline{B}(C + BD) + \overline{A}\overline{B}]C$$

Simplify the following Boolean expression:

$$\overline{A}BC + A\overline{B}\overline{C} + \overline{A}\overline{B}\overline{C} + A\overline{B}C + ABC$$

Simplify the following Boolean expression:

$$\overline{AB + AC} + \overline{ABC}$$

Simplify the Boolean expression $\overline{AB} + \overline{AC} + \overline{ABC}$.

1. Simplify the following Boolean expressions:

(a)
$$A + AB + A\overline{B}C$$
 (b) $(\overline{A} + B)C + ABC$ (c) $A\overline{B}C(BD + CDE) + A\overline{C}$

2. Implement each expression in Question 1 as originally stated with the appropriate logic gates. Then implement the simplified expression, and compare the number of gates.

$$ar{A}BC + (A + ar{A})ar{B}ar{C} + AC(B + ar{B})$$
 $ar{A}BC + ar{B}ar{C} + AC$
 $AC + ar{A}BC + ar{B}ar{C}$
 $C(A + ar{A}B) + ar{B}ar{C}$
 $C(A + B) + ar{B}ar{C}$ As $(A + ar{A}B) = A + B$
 $AC + BC + ar{B}ar{C}$

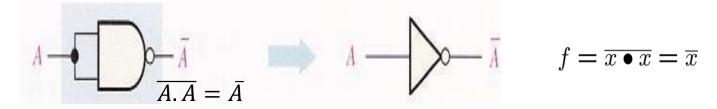
Universal Gates

- Although we can implement **any circuit** with **AND/OR/NOT**, we can also implement **any circuit** with **only NAND** or **NOR** gates.
- We might want to do this because of technology considerations, that is, these gates might be cheaper to implement in silicon or they might be the only type of gates we have available.
- Since we can always use only NAND or NOR gates, these gates are sometimes called **universal gates**.
- The "trick" (if you want to call it that) is to see that we can implement the three basic gates (AND, OR, NOT) in terms of NAND or NOR gates.

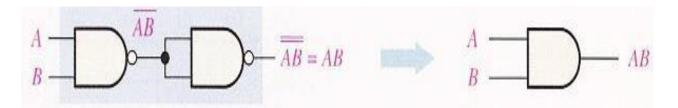
Universal Property of NAND and NOR gate

The NAND gate as a Universal logic gate:

• 1) One NAND gate used as an inverter (NOT gate):



• 2) Two NAND gates used as an AND gate



•) Three NAND gates used as an OR gate

$$A - \overline{G_1} \circ \overline{\overline{A}} = A + B$$

$$B - \overline{\overline{A}} = A + B$$

$$\overline{\overline{A}} \cdot \overline{\overline{B}} = A + B$$

$$A - \overline{\overline{A}} = A + B$$

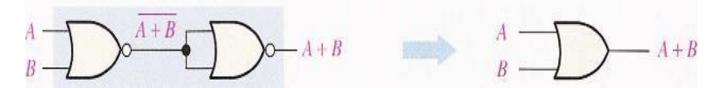
$$\overline{\overline{A}} \cdot \overline{\overline{B}} = A + B$$

The NOR gate as a Universal logic gate:

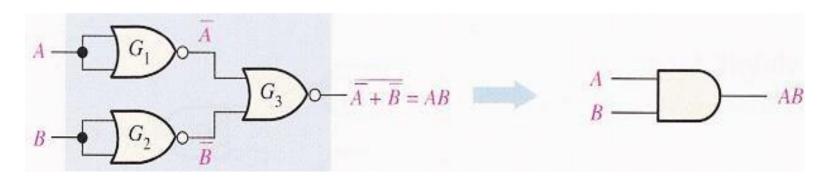
• 1) One NOR gate used as an inverter (NOT gate):



• 2) Two NOR gates used as an **OR gate**:



• 3) Three NOR gates used as an AND gate:



Steps for Construction of Logic circuits Using Universal Gates

- 1) First construct the logic expression using basic gates (AND/OR/NOT),
- 2)Replace each basic gate with its equivalent universal gate implementation,
- 3) Cancel two consecutive NOT gates (according to Boolean algebra),
- 4) Redraw the final circuit.

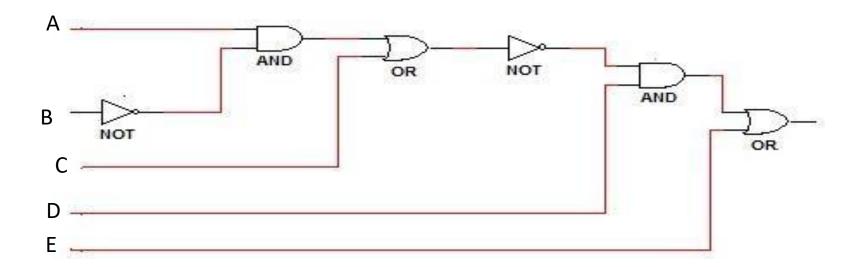
Example: Implement the following expression with (a) NAND gates ONLY, (b) NOR gates ONLY.

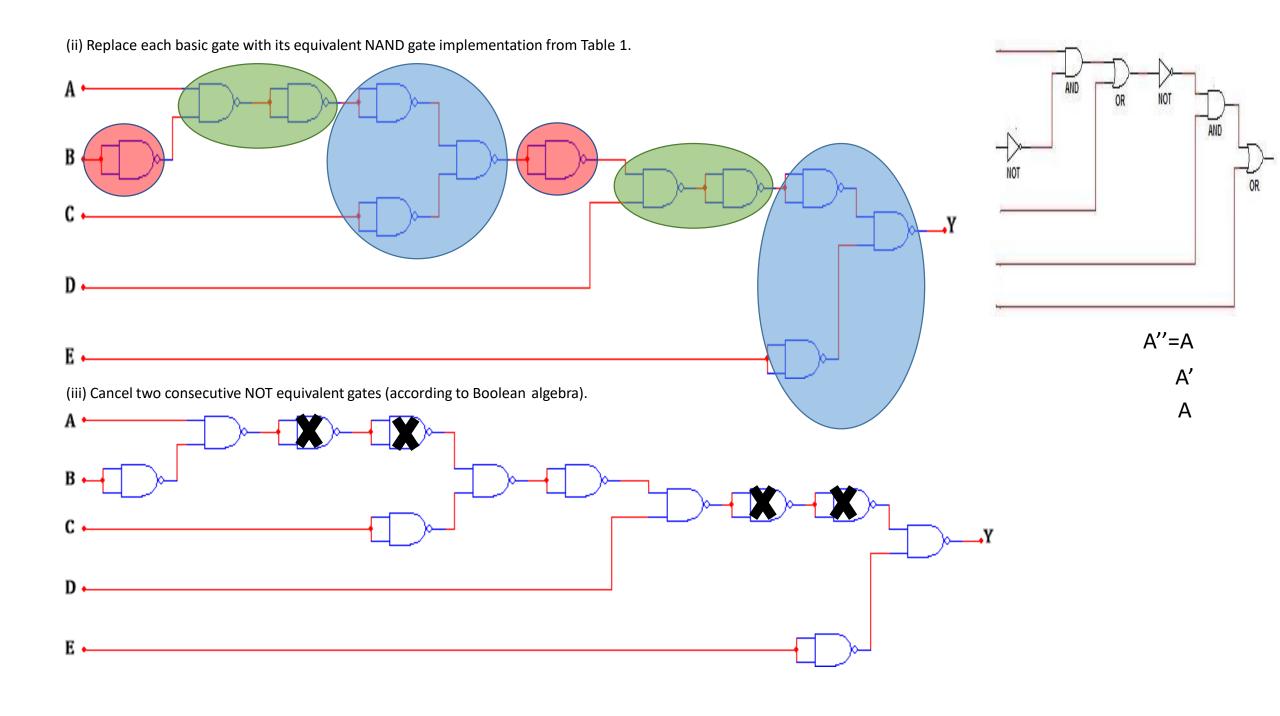
Y=(AB'+C)'D+E=
$$\overline{[A.\overline{B}+C]}.D+E$$

Solution:

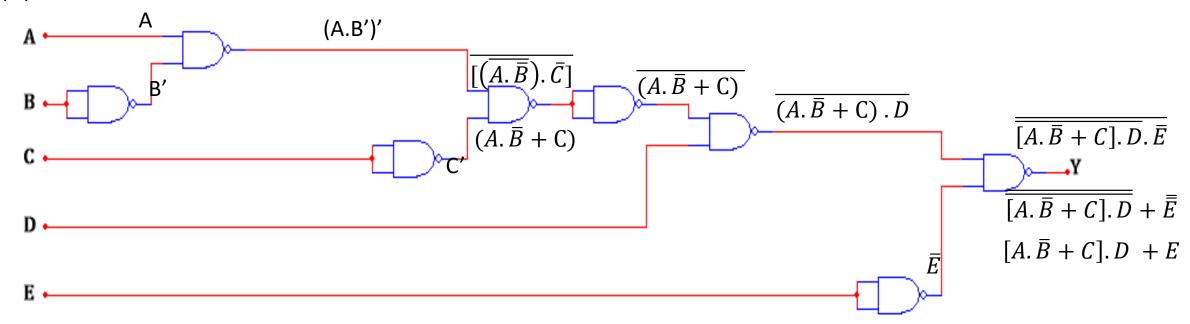
Implementation using NAND gates ONLY:

i) Implementation with Basic gates:



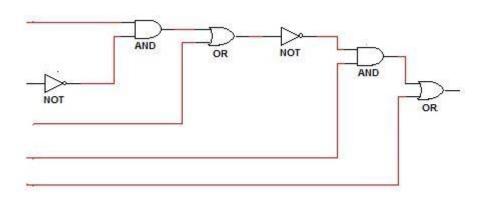


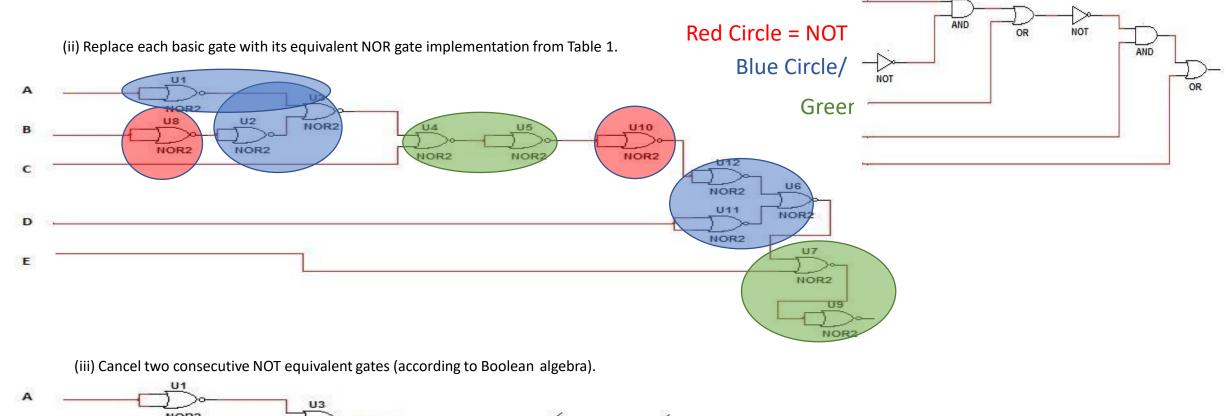
(iv)Redraw the final circuit



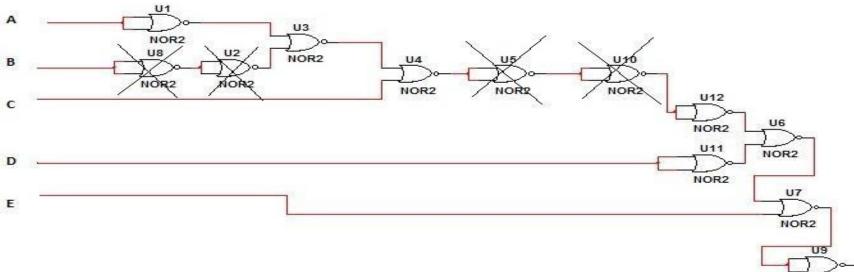
(b) Implementation using NOR gates ONLY:

(i) Implementation with Basic gates:

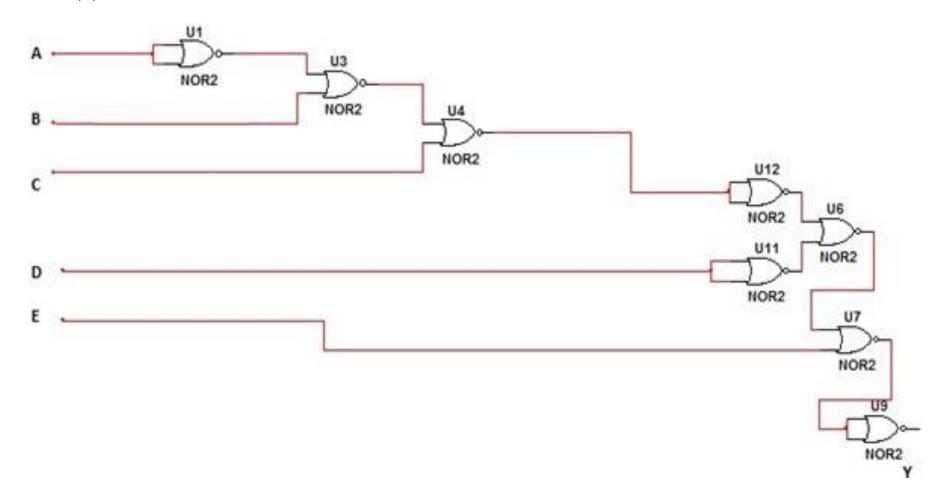




NOR2



(iv)Redraw the final circuit.

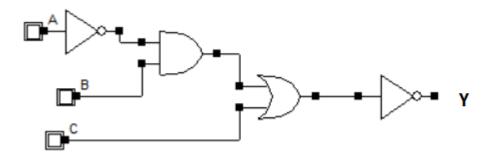


In Class Activity

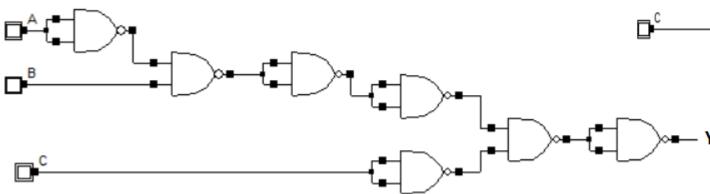
- Design the following equation using i) NAND gates only ii) NOR gates only
- Count the number of gates for each design and comment on which one is better.

$$\bullet$$
Y=(A'.B+C)'

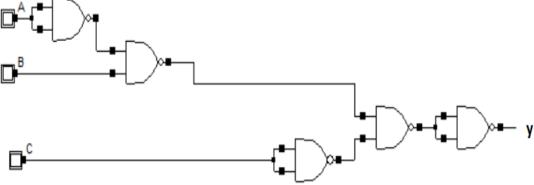
i) Basic Gate Design



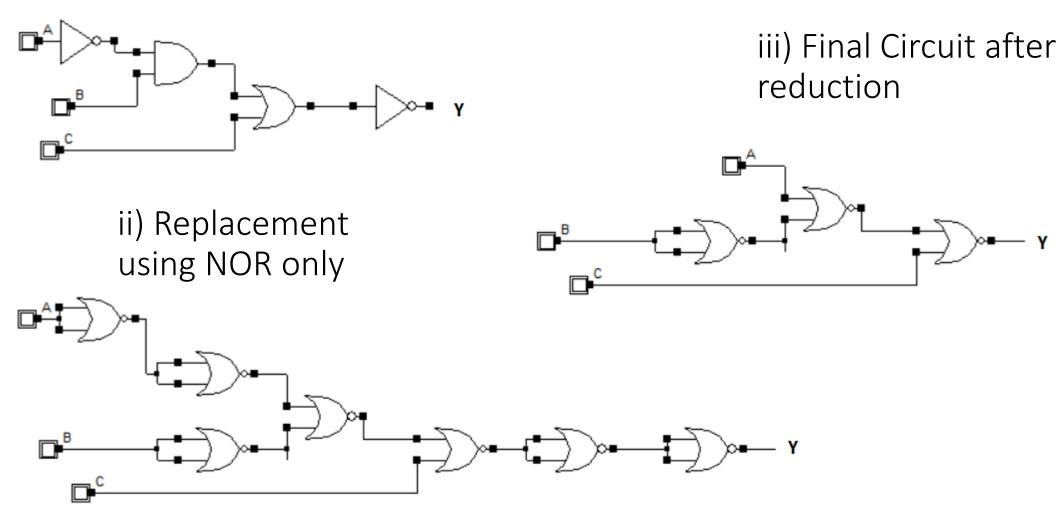
ii) Replacementusing NAND only



iii) Final Circuit after reduction



i) Basic Gate Design



Comment

• Since after design both the circuits using NAND and NOR gates it is found that NAND gate design requires 5 gates in the final design while, the NOR gate design requires only 3 gates. So in my opinion, NOR gate design is more preferred as it has lesser number of gates and cost effective.