



# WEST BENGAL STATE COUNCIL OF TECHNICAL & VOCATIONAL EDUCATION AND SKILL DEVELOPMENT

(Formerly WEST BENGAL STATE COUNCIL OF TECHNICAL EDUCATION)

"Karigori Bhavan", 4th Floor, Plot No. B/7, Action Area-III, New Town, Rajarhat, Kolkata-700160

## DIPLOMA GRADE REPORT

Serial No. S3/09188/3-21

**SEMESTER - 3 HELD IN MARCH, 2021**

**REGISTRATION NO.: D192002760 ROLL NO.: DBELCSTS3**

**10003052**



**NAME OF THE CANDIDATE : NAFISA HOSSAIN**

**FATHER'S/MOTHER'S NAME : SK. ALTAF HOSSAIN**

**BRANCH OF STUDY : COMPUTER SCIENCE & TECHNOLOGY**

**NAME OF THE INSTITUTION : BEHALA GOVERNMENT POLYTECHNIC**

SUBJECTS	FULL MARKS	CREDITS	GRADE OBTAINED
<b>Theoretical</b>			
<b>DISCRETE MATHEMATICS (FOR CST)</b>	<b>100</b>	<b>3</b>	<b>A</b>
<b>C PROGRAMMING</b>	<b>100</b>	<b>3</b>	<b>A</b>
<b>DIGITAL LOGIC DESIGN (FOR CST)</b>	<b>100</b>	<b>3</b>	<b>B</b>
<b>DATA STRUCTURE (FOR CST)</b>	<b>100</b>	<b>3</b>	<b>E</b>
<b>COMPUTER ORGANIZATION &amp; ARCHITECTURE</b>	<b>100</b>	<b>3</b>	<b>A</b>
<b>ELECTRONICS DEVICES &amp; CIRCUITS</b>	<b>100</b>	<b>3</b>	<b>A</b>
<b>Sessional</b>			
<b>C PROGRAMMING LAB</b>	<b>50</b>	<b>2</b>	<b>B</b>
<b>DIGITAL LOGIC DESIGN (FOR CST) LAB</b>	<b>50</b>	<b>1</b>	<b>A</b>
<b>DATA STRUCTURE (FOR CST) LAB</b>	<b>100</b>	<b>2</b>	<b>E</b>
<b>ELECTRONIC DEVICES &amp; CIRCUITS LAB</b>	<b>50</b>	<b>1</b>	<b>E</b>
<b>PROFESSIONAL PRACTICE-I (PC MAINTENANCE)</b>	<b>50</b>	<b>2</b>	<b>E</b>
<b>TOTAL</b>	<b>900</b>		
<b>BACK PAPER(S):</b>	<b>GRADE POINT AVERAGE</b>	<b>PERCENTAGE OF MARKS</b>	<b>RESULTS</b>
<b>NIL</b>	<b>8.1</b>	<b>77.1</b>	<b>PASS</b>

PART-I	PART-II	GRADE	OVERALL GRADE POINT	OVERALL PERCENTAGE OF MARKS	CLASS
<b>GRADE POINT OF SEMESTER-2</b>	<b>GRADE POINT OF SEMESTER-3</b>	<b>GRADE POINT OF SEMESTER-4</b>	<b>GRADE POINT OF SEMESTER-5</b>	<b>GRADE POINT OF SEMESTER-6</b>	

Dated, Kolkata, the 30th June, 2021

**SENIOR ADMINISTRATIVE OFFICER  
(EXAMINATIONS)**