

DI YU

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Education

The University of Hong Kong

Ph.D., Electrical and Electronic Engineering

Advised by Prof. [Chao Xiang](#)

2023 – Present

Hong Kong, China

Delft University of Technology

M.S., Applied Physics

Advised by Prof. [Kobus Kuipers](#)

2021 – 2023

Delft, The Netherlands

University of Electronic Science and Technology of China

B.S., Mathematics and Physics

Grade: Rank 1st in Yingcai Honors College

2017 – 2021

Chengdu, China

Relevant Skills

- Python & MATLAB
- RF Synthesizer Design
- Optimization
- Laser Engineering
- COMSOL & Lumerical

Experience

Baidu, Inc

Research Intern in Quantum AI

Mentored by Prof. [Kun Fang](#) & Prof. [Xin Wang](#)

2023.4 – 2023.10

Beijing, China

- Developed a fast quantum circuit compilation algorithm that utilizes symmetries in hardware architecture.
- Demonstrated the circuit compilation algorithm's optimality in terms of its time complexity.
- Outcomes: 1 [research paper](#) & 3 [patents](#)

Publications

- **D. Yu**, K. Fang (2024). Symmetry-based quantum circuit mapping. *[Physical Review Applied](#)*.
- **D. Yu**, S. Arora, and L. Kuipers (2024). Impact of transforming interface geometry on edge states in valley photonic crystals. *[Physical Review Letters](#)*.

Patents

- K. Fang, **D. Yu** (2023). Quantum circuit mapping method and device and electronic equipment. [CN117436537A](#).
- K. Fang, **D. Yu** (2023). Quantum circuit mapping method and device and electronic equipment. [CN117436536A](#).
- K. Fang, **D. Yu** (2023). Quantum circuit mapping method and device and electronic equipment. [CN117436535A](#).

Awards

Excellent Student Scholarship

University of Electronic Science and Technology of China

2020

Excellent Student Scholarship

University of Electronic Science and Technology of China

2019

Model Student Scholarship

University of Electronic Science and Technology of China

2018