**Oregon Institute of Technology**

**CST456 / EE407 – Embedded Systems Testing**

**Course Syllabus**

**1 Contact Information**

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**2 Course Overview**

* Course Title: CST456 – Embedded Systems Testing.
* Prerequisites: CST136, CST204, and CST231 if registered for CST456; CST116, EE331, and EE335 if registered for EE407.
* Textbooks: SystemVerilog for Verification: A Guide to Learning the Testbench Language Features, 3rd edition. Test Driven Development for Embedded C.
* Class Meeting: Lecture is Friday from 6:00 PM to 8:50 PM. Optional lab on Saturday.

**3 Assessment and Grading**

* Labs: 40 %. Homework: 30 %. Final Project 30 %.
* Grading Scale (A: 100 to 90), (B: 89 to 80), (C: 79 to 70), (D: 69 to 60), (F: < 60).

**4 Academic Integrity**

Students are expected to demonstrate their knowledge with honesty and integrity. OIT considers academic dishonesty to be an unacceptable practice. The complete OIT Student Academic Integrity Policy is available on the OIT web site.

Cheating, plagiarism, or any other form of academic dishonesty will not be tolerated, and will cause a student or group of students to receive a 0 in a specific assignment and be reported to the Dean of Students. Reported incidents of academic dishonesty are placed on the student's record and may lead to temporary or permanent suspension.