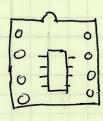
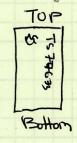
Voltage Regulator Design.

TI 77633 IC

- · 5v input from uC to 3.3 (exact) output.
- · Max Isat = 500 mA1
- Breakout board built with soldered chip.

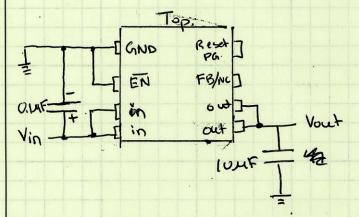
Breakout Board (which is top)





· Board has a ridge at top.

Schematic -> Regulator is a fixed 3.3 V version so Vin = 5V.



Note: O.I.M.F input capacitor not required. It is there to clean up signal.

- · louf capacitor needed to Stabilize the control loop.
- · Regulator runs a loop to check if Vo is 3.3 or not.
- -> Feedback sensing system.