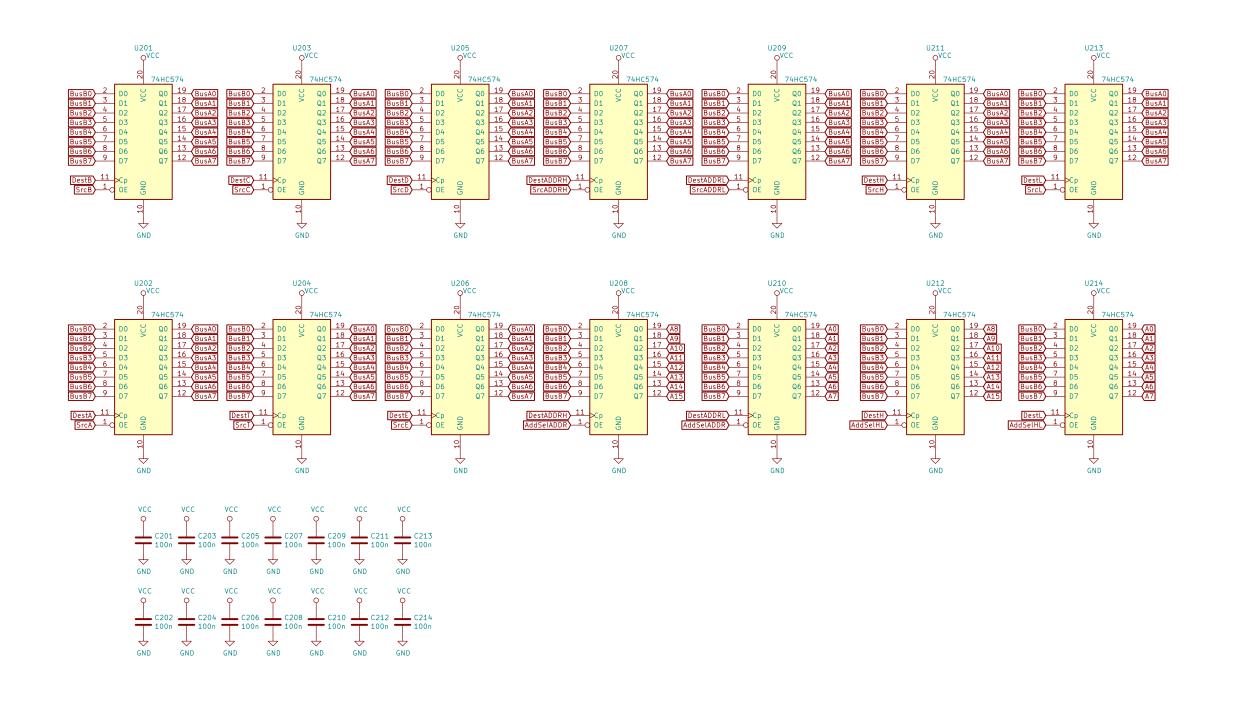
Sheet: ALU Sheet: Flags Sheet: Program Counter Sheet: Stack pointer Sheet: Registers File: programcounter.sch File: registers.sch File: alu.sch File: Flags.sch File: Stack pointer.sch Sheet: Condition Tester Sheet: Memory interface instruction latch Sheet: Interrupt Vector Sheet: Phase Counter Sheet: Interrupt state machine File: interrupt vector.sch File: condition tester.sch File: Phase counter.sch File: memoryinterface.sch File: interruptSM.sch Sheet: Opcode Select Sheet: Phase and Latch decoder Sheet: Source Select Logic Sheet: Destination Select Logic Sheet: Mode Selection File: opcode select.sch File: phaselatch.sch File: source select logic.sch File: dest select logic.sch File: mode select.sch Sheet: Address Selecter Sheet: Restart Trigger Sheet: Stack Control Sheet: External Connections Sheet: Program Counter Control File: addressselect.sch File: restart.sch File: stack control.sch File: ioconnect.sch File: pccontrol.sch **DNB Projects** Sheet: / File: cisc-8.sch Title: Cisc-8 - master page Date: 2019-02-24 Size: A4 Rev: 1 KiCad E.D.A. kicad 5.1.4-e60b26684ubuntu18.04.1 ld: 1/21



DNB Projects
Sheet: /Registers/
File: registers.sch
 Title: Main and shadow registers

 Size: A3
 Date: 2019-02-25

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 Rev: 1 Id: 2/21

