COMPUTER ARCHITECTURE ASSIGNMENT - 2

We have implemented the following programs:

- 1. Finding the factorial of a number
- 2. Sorting an array using selection sort
- 3. Searching a number in an array using Binary Search

FACTORIAL OF A NUMBER

INTRODUCTION:

Factorial of a number is defined as the product of all positive integers less than or equal to a given positive integer (Factorial of 0 is defined as 1).

Our program calculates the factorial of the given number. Assumption:

The input is a non-negative number.

PROGRAM EXPLANATION:

In our program we have four labels: main, check, loop and exit.

main: Stores the necessary values of the input, memory address of input, the value of the iterator \$s1 and stores the value 1 in \$s2 which will be used to compute factorial. **check:** Check if the iterator is less than our number or not and branch accordingly.

loop: Multiplies the current product value(\$s2) to the iterator \$s1 and then increments the iterator.

exit: Stores the result of factorial in data memory and exits from the program.

Instructions Used:

○ Load Word (lw) ○ Store
 Word (sw) ○ Add immediate
 (addi) ○ Multiply (mul) ○
 Branch on not equal (bne) ○
 Jump (j)

ASSEMBLY CODE:

```
main:
 1
2
3
4
5
                addi $t0, $0, 10
addi $t1, $0, 268500992
sw $t0, 0($t1)
lw $s0, 0($t1)
addi $s1, $0, 1
addi $s2, $0, 1
ii chek
                                                                    #t0 = n = 10
                                                                    #t1 = memory address where n will be stored
                                                                   #M[t1] = t0 = 10

#s0 = M[t1] = n = 10
 6
                                                                   #s1 = i = 1
 8
                                                                   \#s2 = fact(n) = 1
                j check
 9
10
11 check:
                           #checks the loop condition
12
                bne $s1, $s0, loop
mul $s2, $s2, $s0
                                                        #if (i!=n) jump to loop
13
14
                                                        #else fact = fact * n because the loop function will execute only till (n-1)
                j exit
15
16
     loop:
                mul $s2, $s2, $s1
addi $s1, $s1, 1
                                                        # fact = fact * i
19
                                                        # i = i+1
                j check
20
21
22
23
     exit:
                addi $t0, $0, 268501024
sw $s2, 0($t0)
addi $v0,$0,10
                                                        #t0 = address where the result will be stored
24
25
                                                        \#M[t0] = s2
                                                        # return 0
                syscall
26
```

TEXT SEGMENT:

kpt	Basic	Address	Code			Source	
	addi \$8,\$0,10	4194304	0x2008000a	3:	addi \$t0, \$0, 10	#t0 = n = 10	
	lui \$1,4097	4194308	0x3c011001	4:	addi \$t1, \$0, 268500992	#tl = memory address where n will be st	
	ori \$1,\$1,0	4194312	0x34210000				
9	add \$9,\$0,\$1	4194316	0x00014820				Į
100	sw \$8,0(\$9)	4194320	0xad280000	5:	sw \$t0, 0(\$t1)	#M[t1] = t0 = 10	
100	lw \$16,0(\$9)	4194324	0x8d300000	6:	lw \$s0, 0(\$t1)	#s0 = M[t1] = n = 10	
	addi \$17,\$0,1	4194328	0x20110001	7:	addi \$sl, \$0, l	#sl = i = l	
22	addi \$18,\$0,1	4194332	0x20120001	8:	addi \$s2, \$0, l	#s2 = fact(n) = 1	
	j 4194340	4194336	0x08100009	9:	j check		
	bne \$17,\$16,2	4194340	0x16300002	13:	bne \$s1, \$s0, loop	#if (i!=n) jump to loop	ļ
92	mul \$18,\$18,\$16	4194344	0x72509002	14:	mul \$s2, \$s2, \$s0	<pre>#else fact = fact * n because the loop function</pre>	
23	j 4194364	4194348	0x0810000f	15:	j exit		Ī
2	mul \$18,\$18,\$17	4194352	0x72519002	18:	mul \$s2, \$s2, \$s1	# fact = fact * i	
20 3	addi \$17,\$17,1	4194356	0x22310001	19:	addi \$sl, \$sl, l	# i = i+1	Ī
21	j 4194340	4194360	0x08100009	20:	j check		
20	lui \$1,4097	4194364	0x3c011001	23:	addi \$t0, \$0, 268501024	#t0 = address where the result will be stored	
22	ori \$1,\$1,32	4194368	0x34210020				
2 - 5	add \$8,\$0,\$1	4194372	0x00014020				
22	sw \$18,0(\$8)	4194376	0xad120000	24:	sw \$s2, 0(\$t0)	#M[t0] = s2	
9	addi \$2,\$0,10	4194380	0x2002000a	25:	addi \$v0,\$0,10	# return 0	
24	syscall	4194384	0x0000000c	26:	syscall		

DATA SEGMENT:

ata Segment								
Address	Value (+0)	Value (+4)	Value (+8)	Value (+12)	Value (+16)	Value (+20)	Value (+24)	Value (+28)
268500992	10	0	0	0	0	0	0	
268501024	3628800	0	O	0	0	0	0	
268501056	0	0	0	0	0	0	0	
268501088	0	0	0	0	0	0	0	
268501120	0	0	0	0	0	0	0	
268501152	0	0	0	0	0	0	0	
268501184	G	0	0	0	0	0	0	
268501216	0	0	0	0	0	0	0	
268501248	G	0	0	0	0	0	0	
268501280	0	0	0	0	0	0	0	
268501312	G	0	G	0	0	0	0	
268501344	0	0	0	0	0	0	0	
268501376	0	0	0	0	0	0	0	
268501408	0	0	0	0	0	0	0	
268501440	0	0	0	0	0	0	0	
268501472	0	0	0	0	0	0	0	

• MEMORY:

Registers	Coproc 1	Coproc 0	
Name		Number	Value
\$zero	Į.	0	0
\$at	- 22	1	268501024
\$v0		2	10
\$vl	2)	3	0
\$a0		4	0
\$al	22	5	0
\$a2		6	0
\$a3	2)	7	0
\$t0		8	268501024
\$t1	2)	9	268500992
\$t2		10	0
\$t3	2)	11	0
\$t4		12	0
\$t5	2)	13	0
\$t6		14	0
\$t7		15	0
\$s0		16	10
\$sl		17	10
\$s2		18	3628800
\$s3		19	0
\$s4		20	0
\$s5		21	0
\$s6		22	0
\$s7		23	0
\$t8		24	0
\$t9	- 5	25	0
\$k0		26	G
\$k1		27	0
\$gp		28	268468224
\$sp		29	2147479548
\$fp		30	0
\$ra		31	0
pc			4194388

SELECTION SORT

INTRODUCTION:

Selection sort is a sorting algorithm that works by repeatedly selecting the smallest (or largest) element from the unsorted portion of the array and moving it to the sorted portion of the array.

Our program sorts the array using the selection sort method.

PROGRAM EXPLANATION:

Our program has 5 parts: main, outer_loop, cont_out, inner_loop, cont_in and exit.

main: Stores the necessary values, the integer 4 for alignment, the size of the array, the base address of the array and the iterator for the outer_loop which is \$s0.

outer_loop: Checks if you have iterated till the end of the loop. If yes, then branches to exit, else it stores the index of the minimum value obtained through inner_loop in \$s7. It also increments the iterator for the inner_loop which is \$s2.

cont_out: This swaps the minimum element to the desired place in the array (which is the outer loop iterator).

inner_loop: It calculates the next minimum element and then goes to cont_out which then swaps it. **cont_in:** Increments the inner iterator which is \$s2 and proceeds to inner_loop.

exit: Exits from the program.

Instructions Used:

- Load Word (lw) Store Word (sw) ○ Add immediate (addi) ○
 Multiply (mul) ○ Add (add)
- Branch on greater than equal (bge)Jump (j)

ASSEMBLY CODE:

```
1 .data
2
           arr: .word 23,31,-9,180,99
3 .text
4
5 addi $s5,$zero,4
                                             #store value of 4 to be used later for alignment
                                             #store size of array in $t0
6 addi $t0,$0,5
                                            #store base address of array
7
   la $t1,arr
8
9 #selection sorting code
                                           #counter for outer loop (i=0)
               add $s0,$zero,$zero
10
               addi $s1,$t0,-1
                                            # store len-1 in $s1
11
                                           #check if i>=len-1 then break
12 outer_loop: bge $s0,$s1, exit
                                           #min_pos=i
13
               add $s7,$s0,$zero
                                           #counter for inner loop , j=i+1
14
               addi $s2,$s0,1
               j inner_loop
15
16
               #swap min-pos to place
17
18 cont_out : mul $t3,$s5,$s0
                                            #i*4 for alignment
                                            #store value of index i
               add $t3,$t3,$t1
19
               lw $t4,0($t3)
                                            #tmp= arr[i]
20
21
22
               mul $t6,$s5,$s7
                                            #min pos*4 for alignment
                                           #store min pos address val
23
               add $t6,$t6,$t1
               lw $t5,0($t6)
                                           #tmp2= arr[min_pos]
24
25
               sw $t5,0($t3)
                                           #arr[i] = tmp2 = arr[min pos]
26
27
               sw $t4,0($t6)
                                           #arr[min_pos] = tmp = arr[i]
28
                                           #1++
29
               addi $s0,$s0,1
30
               j outer_loop
31
32 inner_loop: bge $s2,$t0,cont_out
                                           #j>=n , break
33
                                            #j*4 for alignment
34
               mul $t3,$s5, $s2
35
               add $t3,$t3,$t1
                                            #store index j address
               lw $t4,0($t3)
                                            #tmp= arr[j]
36
37
                                            #min_pos*4 for alignment
               mul $t6,$s5,$s7
38
39
               add $t6,$t6,$t1
                                            #store min pos address val
40
               lw $t5,0($t6)
                                           #tmp2= arr[min_pos]
41
                                          #if(arr[j]>=arr[min_pos], no change
               bge $t4,$t5,cont in
42
43
               add $s7,$s2,$zero
                                                #min pos=j;
44
  cont_in:
45
               addi $s2,$s2,1
                                            #1++
46
               j inner_loop
47
48 exit:
               addi $v0,$0,10
                                             #exit program
49
               syscall
50
```

TEXT SEGMENT:

ot Basic	Address	Code			Source
addi \$21,\$0,4	4194304	0x20150004 5:	addi \$s5,\$z	ero,4	#store value of 4 to be used later for alig
addi \$8,\$0,5	4194308	0x20080005 6:	addi \$t0,\$0	,5	#store size of array in \$t0
lui \$1,4097	4194312	0x3c0l100l 7:	la \$tl,arr		#store base address of array
ori \$9,\$1,0	4194316	0x34290000			***
add \$16,\$0,\$0	4194320	0x0000802010:		add \$s0,\$zero,\$zero	#counter for outer loop (i=0)
addi \$17,\$8,-1	4194324	0x2111fffff 11:		addi \$s1,\$t0,-1	# store len-l in \$sl
slt \$1,\$16,\$17	4194328	0x0211082a 12:	outer loop:	bge \$s0,\$sl, exit	#check if i>=len-1 then break
beq \$1,\$0,26	4194332	0x1020001a	1984 1188	10 Total	
add \$23,\$16,\$0	4194336	0x0200b82013:		add \$s7,\$s0,\$zero	#min_pos=i
addi \$18,\$16,1	4194340	0x22120001 14:		addi \$s2,\$s0,1	#counter for inner loop , j=i+l
j 4194388	4194344	0x08100015 15:		j inner_loop	
mul \$11,\$21,\$16	4194348	0x72b0580218:	cont out :	mul \$t3,\$s5,\$s0	#i*4 for alignment
add \$11,\$11,\$9	4194352	0x0169582019:	3000	add \$t3,\$t3,\$t1	#store value of index i
lw \$12,0(\$11)	4194356	0x8d6c0000 20:		lw \$t4,0(\$t3)	#tmp= arr[i]
mul \$14,\$21,\$23	4194360	0x72b77002 22:		mul \$t6,\$s5,\$s7	#min pos*4 for alignment
add \$14,\$14,\$9	4194364	0x01c97020 23:		add \$t6,\$t6,\$t1	#store min pos address val
lw \$13,0(\$14)	4194368	0x8dcd0000 24:		lw \$t5,0(\$t6)	#tmp2= arr[min_pos]
sw \$13,0(\$11)	4194372	0xad6d0000 26:		sw \$t5,0(\$t3)	<pre>#arr[i] = tmp2 = arr[min_pos]</pre>
sw \$12,0(\$14)	4194376	0xadcc0000 27:		sw \$t4,0(\$t6)	#arr[min_pos] = tmp = arr[i]
addi \$16,\$16,1	4194380	0x22100001 29:		addi \$s0,\$s0,1	#1++
j 4194328	4194384	0x08100006 30:		j outer_loop	
slt \$1,\$18,\$8	4194388	0x0248082a 32:	inner_loop:	bge \$s2,\$t0,cont_out	#j>=n , break
beq \$1,\$0,-12	4194392	0x1020fff4			
mul \$11,\$21,\$18	4194396	0x72b25802 34:		mul \$t3,\$s5, \$s2	#j*4 for alignment
add \$11,\$11,\$9	4194400	0x01695820 35:		add \$t3,\$t3,\$t1	#store index j address
lw \$12,0(\$11)	4194404	0x8d6c0000 36:		lw \$t4,0(\$t3)	#tmp= arr[j]
mul \$14,\$21,\$23	4194408	0x72b77002 38:		mul \$t6,\$s5,\$s7	#min_pos*4 for alignment
add \$14,\$14,\$9	4194412	0x01c97020 39:		add \$t6,\$t6,\$t1	#store min_pos address val
lw \$13,0(\$14)	4194416	0x8dcd0000 40:		lw \$t5,0(\$t6)	#tmp2= arr[min_pos]
slt \$1,\$12,\$13	4194420	0x018d082a 42:		bge \$t4,\$t5,cont_in	#if(arr[j]>=arr[min_pos], no change
beq \$1,\$0,1	4194424	0x10200001			
add \$23,\$18,\$0	4194428	0x0240b820 43:		add \$s7,\$s2,\$zero	#min_pos=j;
addi \$18,\$18,1	4194432	0x22520001 45:	cont_in:	addi \$s2,\$s2,1	#j++
j 4194388	4194436	0x08100015 46:	1000	j inner_loop	84.0
addi \$2,\$0,10	4194440	0x2002000a 48:	exit:	addi \$v0,\$0,10	#exit program
syscall	4194444	0x0000000c 49:		syscall	- 20 AN

• DATA SEGMENT:

Address	Value (+0)	Value (+4)	Value (+8)	Value (+12)	Value (+16)	Value (+20)	Value (+24)	Value (+2
268500992	-9	23	31	99	180	0	0	
268501024	0	0	0	0	0	0	0	
268501056	G	0	0	G	0	0	0	
268501088	0	0	0	0	0	0	0	
268501120	0	0	0	0	0	0	0	
268501152	0	0	0	0	0	0	0	
268501184	9	0	0	G	0	0	0	
268501216	0	0	0	0	0	0	0	
268501248	9	0	0	G	0	0	0	
268501280	0	0	0	0	0	0	0	
268501312	0	0	0	0	0	0	0	
268501344	0	0	0	0	0	0	0	
268501376	9	0	0	9	0	0	0	
268501408	0	0	0	0	0	0	0	
268501440	0	0	0	0	0	0	0	
268501472	0	0	0	0	0	0	0	

• MEMORY:

Name	Number	Value
\$zero	0	G
\$at	1	G
\$v0	2	10
\$v1	3	G
\$a0	4	(
\$al	5	(
\$a2	6	(
\$a3	7	(
\$t0	8	5
\$t1	9	268500992
\$t2	10	C
\$t3	11	268501004
\$t4	12	180
\$t5	13	99
\$t6	14	268501008
\$t7	15	(
\$s0	16	4
\$sl	17	4
\$s2	18	5
\$s3	19	(
\$s4	20	(
\$s5	21	4
\$s6	22	(
\$s7	23	4
\$t8	24	(
\$t9	25	(
\$k0	26	(
\$k1	27	(
\$gp	28	268468224
\$sp	29	2147479548
\$fp	30	(
\$ra	31	(
рс		4194448

BINARY SEARCH

• INTRODUCTION:

Binary Search is a searching algorithm which is used in a sorted array by repeatedly dividing the search interval in half.

Our program searches for a particular number in an array of integers using binary search. If the number exists, the result is the corresponding index in the array; if not, the result is -1.

PROGRAM EXPLANATION:

In this program, we have six parts: main, loop, else, found, not found and exit.

main: Stores the necessary values, i.e., size of the array, the number to be searched (key), base address of array and the length of the array. We also declare lb and ub which is essential to perform the binary search. (NOTE: the boundaries of the array that we are working with at one point is defined by lower-bound and upper-bound).

loop: Checks if the lower-bound (lb) has exceeded upper-bound(ub) or not. If it has, our search is over. If not, it calculates the midpoint of the part of the array we are currently working with and stores the address of it in \$t4. If the element at index midpoint is equal to our key, we have found the value and we branch to found. Else if the element is less than key, we update upperbound to be (midpoint-1). Otherwise, we branch to the else label.

else: It changes the lower bound to (mid + 1) and goes back to loop for the next iteration.

found: Stores the index at which the element is found.

not found: If the number is not there in the array, it stores the result as -1. **exit:**

Exits from the program.

Instructions Used:

- Load Word (lw) Store
 Word (sw) Load address
 (la) Add immediate (addi)
 Add (add)
- Shift right logical (srl)
- Shift left logical (sll)
 Branch on equal (beq)
 Branch on less than (blt)
 Branch on greater than (bgt)
 Jump (j)

ASSEMBLY CODE:

```
#binary search
 2
    .data
 3
             arr: .word 11, 20, 34, 45, 56 #sorted array
 4
     .text
 5
 6
    addi $s0,$zero,5
                                    #store size of array in $s0
    addi $s1,$zero,45
                                    #number to be found
 7
    la $t1,arr
                                    #store base address of array
 8
 9
    addi $t7,$zero,0
                                    #lowerbound=0
 10
    addi $t6,$s0,-1
                                    #upperbound=size-1
11
12
13
    loop:
             bgt $t7,$t6,not_found #if lowerbound>upperbound, search is over
14
             add $t5,$t7,$t6
                                    #mid = lb+ub
                                    #mid = lb+ub/2
15
             srl $t5,$t5,1
             sll $t4,$t5,2
                                    #mid*4 for alignment
16
             add $t4,$t4,$t1
                                    #store address of arr[mid]
17
18
             lw $t3, 0($t4)
                                    #$t3 = arr[mid]
19
             beq $t3,$s1, found
                                    #arr[mid] = key
20
             blt $t3,$s1,else
                                    #if arr[mid]<key
                                    #ub= mid-1
21
             addi $t6,$t5,-1
22
             j loop
23
24
    else:
            addi $t7,$t5,1
                                    #lb=mid+1
25
             j loop
26
27
     found: addi $t0,$0,268501024 #t0 = address where the result will be stored
                                    \#M[t0] = t5 = index of element found
28
             sw $t5,0($t0)
29
             j exit
 30
31
    not found:
32
             addi $t5,$zero,-1
                                    #to store -1 if no. not found
33
             addi $t0,$0,268501024 #t0 = address where the result will be stored
34
             sw $t5,0($t0)
                                    \#M[t0] = t5
35
36
    exit:
37
             addi $v0,$0,10
                                    #exit program
38
             syscall
```

TEXT SEGMENT:

pt	Address	Code	Basic				Source	
	4194304	0x20100005	ddi \$16,\$0,5	6:	addi \$s	0,\$zero,5	#store size of array in \$s0	
			ddi \$17,\$0,45			1,\$zero,45	#number to be found	
		0x3c0110017		8:	la \$t1.	err	#store base address of array	
	4194316	0x34290000	ri \$9.\$1.0				**************************************	
			ddi \$15,\$0,0	10:	addi \$t	7,\$zero,0	#lowerbound=0	
			ddi \$14,\$16,-1			3,\$s0,-1	#upperbound=size-1	
			lt \$1,\$14,\$15				#if lowerbound>upperbound, search is over	
1		0x14200011 b				-0-1		
			dd \$13,\$15,\$14	14:		add \$t5,\$t7,\$t6	#mid = lb+ub	
			rl \$13,\$13,1	15:		srl \$t5,\$t5,1	#mid = \b+ub/2	
	4194344	0x000d6080 s	11 \$12,\$13,2	16:		sll \$t4,\$t5,2	#mid*4 for alignment	
			dd \$12,\$12,\$9	17:		add \$t4,\$t4,\$t1	#store address of arr[mid]	
	4194352	0x8d8b00001	w \$11,0(\$12)	18:		lw \$t3, 0(\$t4)	#\$t3 = arr[mid]	
			eq \$11,\$17,6	19:		beg \$t3,\$sl, found	#arr[mid] = key	
			lt \$1.\$11.\$17	20:		blt \$t3,\$sl,else	#if arr[mid] <key< td=""><td></td></key<>	
	4194364	0x14200002 b	ne \$1,\$0,2			,		
	4194368	0x2laeffffa	ddi \$14.\$131	21:		addi \$t6,\$t5,-1	#ub= mid-1	
	4194372	0x08100006	4194328	22:		i loop	300/10003000	
	4194376	0x21af0001	ddi \$15.\$13.1	24:	else:	addi \$t7,\$t5.1	#lb=mid+l	
	4194380	0x08100006	4194328	25:		j loop		
		0x3c011001		27:	found:		#t0 = address where the result will be stored	
	4194388	0x34210020 d	ri \$1.\$1.32			* ***		
	4194392	0x00014020 a	dd \$8,\$0,\$1					
	4194396	0xad0d00000	w \$13,0(\$8)	28:		sw \$t5,0(\$t0)	#M(t0) = t5 = index of element found	
	4194400	0x0810001e	4194424	29:		j exit		
			ddi \$13,\$0,-1	32:		addi \$t5,\$zero,-1	#to store -1 if no. not found	
		0x3c011001		33:			#t0 = address where the result will be stored	
	4194412	0x34210020 d	ri \$1,\$1,32			100 00000		
		0x00014020 a						
	4194420	0xad0d00000	w \$13,0(\$8)	34:		sw \$t5,0(\$t0)	#M[t0] = t5	
			ddi \$2,\$0,10	37:		addi \$v0.\$0.10	#exit program	
		0x0000000c		38:		syscall	Supplemental Desire Monage	

• DATA SEGMENT:

Address	Value (+0)	Value (+4)	Value (+8)	Value (+12)	Value (+16)	Value (+20)	Value (+24)	Value (+28
268500992	11	20	34	45	56	0	0	
268501024	3	0	0	0	9	0	0	
268501056	0	0	0	0	9	0	0	
268501088	0	0	0	0	0	0	0	
268501120	0	0	0	0	0	0	0	
268501152	0	0	0	0	0	0	0	
268501184	0	0	0	0	0	0	0	
268501216	0	0	0	0	0	0	0	
268501248	0	0	0	0	0	0	0	
268501.280	0	0	0	0	0	0	0	
268501312	0	0	0	0	0	0	0	
268501344	0	0	0	0	0	0	0	
268501376	0	Θ	Θ	0	9	0	0	
268501408	0	0	0	0	O	0	0	
268501440	0	0	0	0	9	0	0	
268501472	0	0	0	0	0	0	0	

• MEMORY:

Registers	Coproc 1	Coproc 0				
Nam	е	Number		Value		
\$zero			0	0		
\$at	11		1	268501024		
\$v0			2	10		
\$v1	11		3	O		
\$a0			4	0		
\$al	11		5	0		
\$a2			6	0		
\$a3	11		7	O		
\$t0			8	268501024		
\$t1	11		9	268500992		
\$t2			10	0		
\$t3	11		11	45		
\$t4			12	268501004		
\$t5			13	3		
\$t6			14	4		
\$t7	11		15	3 5		
\$s0			16	5		
\$sl	11		17	45		
\$s2			18	0		
\$s3	11		19	0		
\$s4			20	O		
\$s5	13		21	0		
\$s6			22	O		
\$s7	13		23	0		
\$t8			24	0		
\$t9	13		25	0		
\$k0			26	O		
\$k1			27	0		
\$gp			28	268468224		
\$sp			29	2147479548		
\$fp			30	0		
\$ra	14		31	0		
рс				4194432		

ASSUMPTIONS IN PROCESSOR:

- 1) We have defined the ALUOp of the bne instruction as 11.
- 2) We have defined the ALU control input code of srl as '101' and sll as '110'.

RESULT (FROM PROCESSOR)

FACTORIAL

Teorigia/Pion - Teorigia/Pion / Colombia: Section Colombia: Sect
Register file: [0: 0, 1: 0, 2: 0, 3: 0, 4: 0, 5: 0, 6: 0, 7: 0, 8: 10, 9: 0, 10: 0, 11: 0, 12: 0, 13: 0, 14: 0, 15: 0, 16: 0, 17: 0, 18: 0, 19: 0, 20: 0, 21: 0, 22: 0, 23: 0, 26: 0, 27: 0, 28: 268468224, 29: 2147479548, 30: 0, 31: 0]
Data Remory: (266569992: 0, 265501024: 0)
PC: 4394366
Register file: (0: 0, 1: 265500992, 2: 0, 3: 0, 4: 0, 5: 0, 6: 0, 7: 0, 8: 10, 9: 0, 10: 0, 11: 0, 12: 0, 13: 0, 14: 0, 15: 0, 16: 0, 17: 0, 18: 0, 19: 0, 20: 0, 21: 0, 22: 0, 23: 0, 24: 0, 25: 0, 26: 0, 27: 0, 28: 268468224, 29: 2147479548, 30: 0, 31: 0}
Outa Minory: {268551024: 0}
PC: 4180112
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Data Minory: (2005/01/02): 0, 2005/01/02: 0)
K: 4194316
Register (File: (6) 6, 1: 268669992, 2: 0, 3: 0, 4: 0, 5: 0, 6: 0, 7: 0, 8: 10, 9: 26869992, 10: 0, 11: 0, 12: 0, 13: 0, 14: 0, 15: 0, 16: 0, 17: 0, 18: 0, 19: 0, 20: 0, 21: 0, 22: 0, 23: 0, 20: 0, 27: 0, 28: 26046220, 20: 2107779040, 30: 0, 31: 0)
Data Meaory: { 1266569992: 0, 266501624: 0}
PC: 4194330
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Data Monory: { {cnsserver: 10, 2nsseries+: 0}
K: 1993N
Register File: (0: 0, 1: 268500992, 2: 0, 5: 0, 4: 0, 5: 0, 6: 0, 7: 0, 8: 10, 9: 26850992, 10: 0, 11: 0, 12: 0, 13: 0, 14: 0, 15: 0, 16: 10, 17: 0, 16: 0, 19: 0, 20: 0, 21: 0, 22: 0, 23: 0, 26: 0, 27: 0, 28: 26860224, 29: 2187479540, 30: 0, 31: 0}
Data Memory: {266501624: 0}
K: 439428
Register file: (0: 0, 1: 2685869972, 2: 0, 3: 0, 4: 0, 5: 0, 6: 0, 7: 0, 8: 10, 9: 268586997, 10: 0, 11: 0, 12: 0, 13: 0, 14: 0, 15: 0, 16: 10, 17: 1, 18: 0, 19: 0, 20: 0, 21: 0, 22: 0, 23: 0, 24: 0, 25: 0, 26: 0, 27: 0, 28: 268468724, 29: 2147479548, 30: 0, 31: 0]
Data Resory: {268506992: 18, 268501824: 0}
PC: 4194332
Register file: (6: 6, 1: 288580992, 2: 0, 3: 0, 4: 0, 5: 0, 6: 0, 7: 0, 8: 10, 9: 268580992, 10: 0, 11: 0, 12: 0, 13: 0, 14: 0, 15: 0, 16: 10, 17: 1, 18: 1, 19: 0, 20: 0, 21: 0, 22: 0, 23: 0, 24: 0, 25: 0, 26: 0, 27: 0, 28: 269468224, 29: 2147479948, 30: 0, 31: 0)
Data Memory: {2665899992: 18, 268591024: 0}
pi: 4194336
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Tata Rescry: {208598992: 18, 288501024: 0}
IC: 4194349
Register file: (61:4) 1-12 (14
Data Recorp: [266569992: 18, 266501024: 0]
PC: 4194052
Register file:
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EC: 4194356
(6: e, 1:266569992, 2: e, 3: e, 4: e, 5: e, 6: e, 7: e, 8: 10, 9: 266569992, 10: e, 11: e, 12: e, 13: e, 14: e, 15: e, 16: 16, 17: 2, 18: 1, 19: e, 20: e, 21: e, 22: e, 23: e, 24: e, 25: e, 26: e, 27: e, 28: 268469224, 29: 2187079548, 30: e, 31: e) Data Reservey: (6: e, 1: 265669992, 12: e, 21: e, 22: e, 23: e, 24: e, 25: e, 26: e, 27: e, 28: 26
(266566922: 18, 266501024: 0)

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92, 2: 0, 3: 0, 4: 0, 5: 0, 6: 0, 7: 0, 6: 10, 9: 26896992, 10: 0, 11: 0, 12: 0, 13: 0, 14: 0, 15: 0, 16: 10, 17: 2, 18: 1, 19: 0, 20: 0, 21: 0, 22: 0, 24: 0, 25: 0, 24: 0, 25: 0, 26: 0, 27: 0, 28: 269468224, 29: 21:
   ata Memory:
{268500992: 10, 268501024: 0}
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 Register file: (6: 6; 1: 6: 86000002, 2: 6, 3: 9, 6: 6, 5: 6, 6: 8, 7: 6, 8: 10, 9: 2685000002, 10: 0, 11: 0, 12: 0, 13: 0, 16: 10, 17: 3, 18: 2, 19: 0, 20: 0, 21: 0, 20: 0, 21: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20: 0, 20:
  Data Monory:
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  PC: 4194352
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  PC: 4194356
 Register (1/16):
 Oata Memory:
{268500992: 10, 268501024: 0}
 egistor file:
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  negistor file: [19. 4]: 6, 18. 4; 6, 5; 8, 6; 6, 7; 6, 8; 18, 9; 268560997, 16; 6, 11; 6, 12; 6, 11; 6, 12; 6, 15; 6, 16; 19, 17; 6, 16; 19, 19; 6, 26; 6, 21; 6, 22; 6, 21; 6, 22; 6, 21; 6, 24; 6, 25; 6, 26; 6, 27; 6, 28; 26866224, 29; 21479
  egister file: (0° 6, 1: 6, 4: 0, 5: 0, 4: 0, 5: 0, 6: 0, 7: 0, 8: 10, 9: 268500992, 18: 0, 11: 0, 12: 0, 13: 0, 14: 0, 15: 0, 16: 10, 17: 0, 18: 120, 19: 0, 20: 0, 21: 0, 22: 0, 23: 0, 24: 0, 25: 0, 26: 0, 27: 0, 28: 26
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ata Memory:
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  Data Memory:
{268588992: 10, 268581824: 8}
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BINARY SEARCH

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