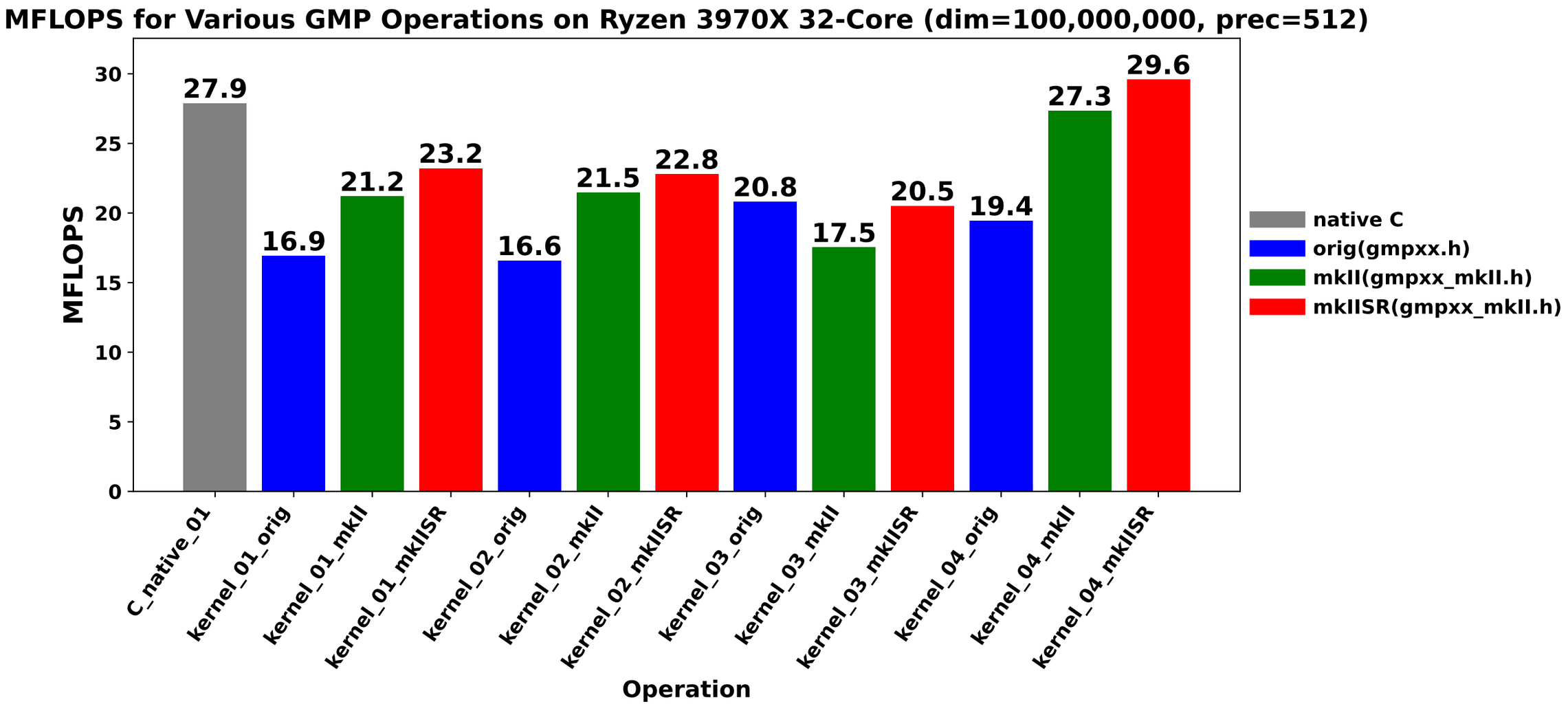
MFLOPS for Various GMP Operations on Ryzen 3970X 32-Core (dim=100,000,000, prec=512)
MFLOPS
Operation
0
5
10
15
20
25
30
27.9
16.9
21.2
23.2
16.6
21.5
22.8
20.8
17.5
20.5
19.4
27.3
29.6
C_native_01
kernel_01_orig
kernel_01_mkII
kernel_01_mkIISR
kernel_02_orig
kernel_02_mkII
kernel_02_mkIISR
kernel_03_orig
kernel_03_mkII
kernel_03_mkIISR
kernel_04_orig
kernel_04_mkII
kernel_04_mkIISR
native C
orig(gmpxx.h)
mkII(gmpxx_mkII.h)
mkIISR(gmpxx_mkII.h)