MFLOPS for Various GMP Operations on Xeon Max 9480 (dim=100,000,000, prec=512)

Legend:
- native C
- orig(gmpxx.h)
- mkII(gmpxx_mkII.h)
- mkIISR(gmpxx_mkII.h)

Operation	MFLOPS
C_native_01	23.2
kernel_01_orig	18.9
kernel_01_mkII	19.4
kernel_01_mkIISR	18.9
kernel_02_orig	18.7
kernel_02_mkII	17.5
kernel_02_mkIISR	19.5
kernel_03_orig	20.0
kernel_03_mkII	18.3
kernel_03_mkIISR	18.5
kernel_04_orig	20.6
kernel_04_mkII	21.2
kernel_04_mkIISR	23.3