MFLOPS for Various GMP Operations on Xeon Max 9480 (dim=100,000,000, prec=512)

Legend:
- native C
- orig(gmpxx.h)
- mkII(gmpxx_mkII.h)
- mkIISR(gmpxx_mkII.h)

Operation	MFLOPS
C_native_01	12.9
kernel_01_orig	17.1
kernel_01_mkII	18.9
kernel_01_mkIISR	19.8
kernel_02_orig	28.1
kernel_02_mkII	11.6
kernel_02_mkIISR	22.1