

# Digital Electronics

## Number representation

1. Binary to convert binary to
2. hence octal number octal make a grouping of 3 bits and for hexa make it with 4 bits.
3. Decimal number
4. hence decimal

## Redix Compliment: →

(i) 9's Compliment = Sub. the given no from 9's  
 (in number 9 should be in same order as that no.)

Ex      in  $\textcircled{100} \rightarrow$  ie 8 bit if should be 999  
 9's compliment for 4 bit = 9999  
 of 0987       $= 9999$   
 $- 0987$   
 $\hline 1012 \rightarrow 9's \text{ comp.}$

## 10's Compliment

Sub. the given no. as in 9's  
 Compliment the only change is ones digit  
 (i.e. last digit ~~from 10's~~) must be sub. from 10.

or simply take 9's complement and then add 1 to the no. you get. Ex  $98 = 02$

### ③ 1's complement

to find 1's complement just convert 1's into zeroes and 0's into ones.

Ex.

$$110101 \rightarrow 001010$$

### (7) 2's complement:→

to find 2's complement, write all zeroes same upto 187 1 (including 1960) <sup>from right side</sup> and then convert 1  $\Rightarrow 0$ . or and 0 to 1's complement

Ex  $1101100 \Rightarrow 0010100$

### Subtraction with Complement

#### i. using 10's complement:→

- Step.1. take complement which is going to subtract
- Step.2. add it with other no.
- Step.3. discard carry if any.
- Step.4. if you do not get any carry then

put a -ve sign before the <sup>10<sup>13</sup></sup> Complement of result you got. ②

Same for 2's Compliment also.

using 1's compliment: →

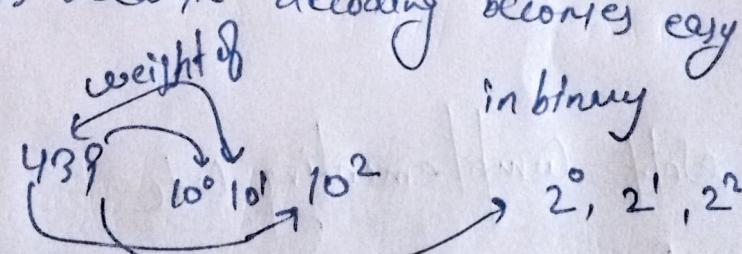
In 1's Compliment all steps are same the only change is, add the carry to the result if you got if no carry then same process you did last time.

Signed binary number: →

0 → for +ve no. ↗ used at leftmost place  
1 → for -ve no. ↗

Binary Code →

we use these Codes because decoding becomes easy than binary system.

1. Weighted Code → Ex 439   
Some weight code 8421, 8427  
5421, 12421  
5411, 7421
2. Non weighted Code
3. Reflected Code
4. Alphanumeric code
5. Error detecting and correcting codes.

NOTE: after 5 we give more high weightage from left side to write any no. in Binary System.

BCD (Binary Code decimal)

$$396 \rightarrow 0011 \quad 1001 \quad 0110$$

Note after 9 we can not rep. a digit in BCD.

BCD addition

if sum > 9 then add 0110

$$\begin{array}{r} \text{Ex} \quad 6 \\ + 7 \\ \hline 1101 \end{array} \rightarrow \begin{array}{l} (13) \times (\text{not full 4}) \\ \text{then.} \end{array} \quad \begin{array}{r} 1101 \\ - 0110 \\ \hline 0011 \end{array}$$

Self Complementing code

Excess-3

$$\begin{array}{r} \text{unweighted code} \quad \text{Ex} \\ + 0011 \quad 0011 \\ \hline 0100 \quad 0100 \end{array} \rightarrow \begin{array}{l} \text{in BCD} \\ \text{in Excess 3 code} \end{array}$$

# Gray Code / Cyclic Code / even distance Code

- Unweighted Code
- not an arithmetic one

$$0 \rightarrow 0000$$

$$1 \rightarrow 0001$$

$$2 \rightarrow 0011$$

$$3 \rightarrow 0010$$

$$4 \rightarrow 0110$$

$$5 \rightarrow 0111$$

$$6 \rightarrow 0101$$

$$7 \rightarrow 0100$$

$$8 \rightarrow 1100$$

$$9 \rightarrow 1101$$

$$10 \rightarrow 1111$$

$$11 \rightarrow 1110$$

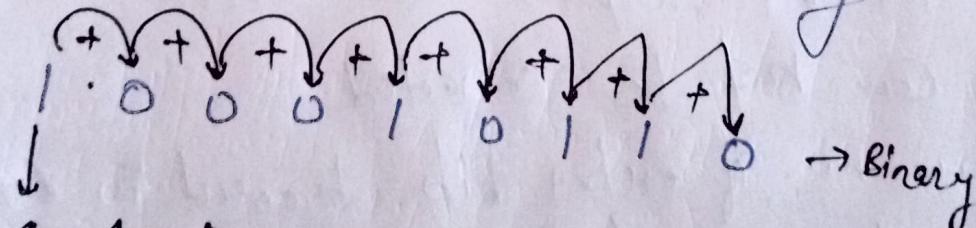
00	00	01	11	10
01	0	1	2	3
11	7	6	5	4
10	8	9	10	11
	15	14	13	12
	12	1010		
	13	1011		
	14	1001		
	15	1000		

## Conversion of Binary to Gray Code

Step.1. write as if it is MSD

Step.2 → then add MSD to next digit (bit) and next bit to next bit and so on record only sum discard the carry.

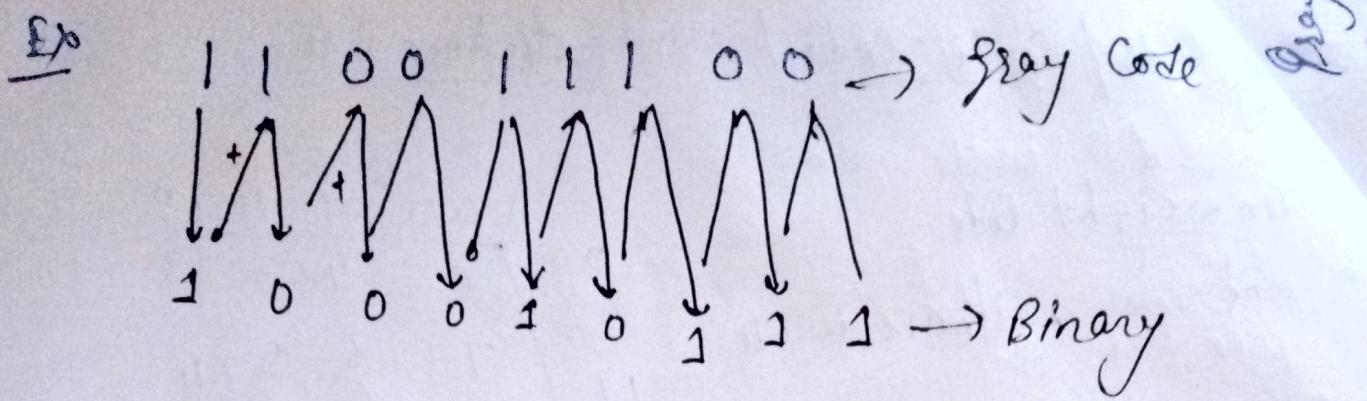
Ex



1 1 0 0 1 1 1 0 → gray code

## Gray Code to Binary →

write MSD alone then add each result with next digit (bit) of gray code (last result by MSD).



### Alphanumeric Codes: →

ASCII → alphanumeric group  $> 36$   
 it include , ten decimal digit , 26 letter , and certain  
 special character such as \$ .  
 it must be coded with a minimum 6 bits.

Ex       $A = 1000001$  ,  $B = 1000010$

### Error detecting code →

- code should be with even or odd 1's.
- one extra bit helps to detect error.
- error can not be corrected.  
 if even no. of entry bits present in code error  
 then it may not give error.

### Application of Code

- BCD used in digital clock, digit thermometers, digit meters and other devices with seven segment display.

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Gray codes only one bit changes in next number  
are used in ~~Stepper~~ position encoders.

- Ex-3 used in old computer and self complementary.

Logic gates: →

- 1) Basic logic gate: → AND, OR, NOT
- 2) universal gate → NAND, NOR
- 3) other gates →

Buffer, XOR (Exclusive OR), Exclusive NOR

$$\begin{array}{ll} \downarrow & \\ x\bar{y} + \bar{x}y & xy + \bar{x}\bar{y} \\ x \oplus y & x \odot y \end{array}$$

Literals: →

if it is variable or the complement of variable.

←: Boolean Algebra: →

- 1) Commutative law  $A+B = B+A$ ,  $AB = BA$
- 2) Associative law  $A+(B+C) = (A+B)+C$ ,  $ABC = (AB)C$
- 3) Distributive law  $A(B+C) = AB+AC$

$$A+0 = A$$

$$1+A = 1$$

$$A \cdot 0 = 0$$

$$A+A = A$$

$$A+A = 1$$

$$A \cdot A = A$$

$$A \cdot \bar{A} = 0$$

$$\bar{A} = A$$

$$A \cdot AB = A$$

$$A+\bar{A}B = A+B$$

$$(A+B)(A+C) = A+BC$$

De morgan's Law  $\rightarrow$

$$\overline{xy} = \overline{x} + \overline{y}, \quad \overline{x+y} = \overline{x} \cdot \overline{y}$$

Min form  $\rightarrow$

write      A for  $I = A$       symbol of Sop =  $\Sigma$   
 $0 = \overline{A}$

we find here Sop (sum of product)

Ex       $x\bar{y}z + x\bar{y}z + \bar{x}\bar{y}z$

Note we write sum of all those term in which we are getting 1 as output.

Max form  $\rightarrow$

write       $I = \overline{A}$       symbol of pos  $\Rightarrow \Pi$   
 $0 = A$

Here we calculate pos (product of sum).

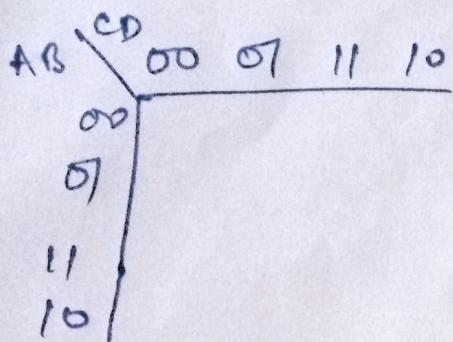
Ex       $(x+y)(\bar{x}+y)$

Note  $\rightarrow$

we write product of sum of all those term in which we are getting 0 as output

Note Sop and pos are complement to each other

## K map /



- for minterms make grouping of 1's.
- In grouping we can include only 2^n terms.
- Try to include more terms.
- Then we write SOP
- \* for Max terms grouping should include zeros.
- \* Here other thing are same.
- \* Here we will write POS.

## Don't Care Condition:

There are some condition of which our output do not depends there we can use 1 or 0 acc. our requirement to above logic and it will not affect the output.