



BCOMP

This documentation is a part of the BCOMP project:
Gate Level Simulation of The Basic Computer
Copyright (C) 2012-2014, Nima Alamatsaz, All rights reserved
Email: nalamat@gmail.com
Web: <http://github.com/nalamat/bcomp>

This documentation describes Open Hardware and is licensed under the CERN OHL v1.2. You may redistribute and modify this documentation under the terms of the CERN OHL v1.2. BCOMP is distributed WITHOUT ANY EXPRESS OR IMPLIED WARRANTY, INCLUDING OF MERCHANTABILITY, SATISFACTORY QUALITY AND FITNESS FOR A PARTICULAR PURPOSE. Please see the CERN OHL v1.2 for applicable conditions. (<http://ohwr.org/CERNOHL>)