

Microprocessor and Computer Architecture Laboratory

UE20CS256

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Section: D

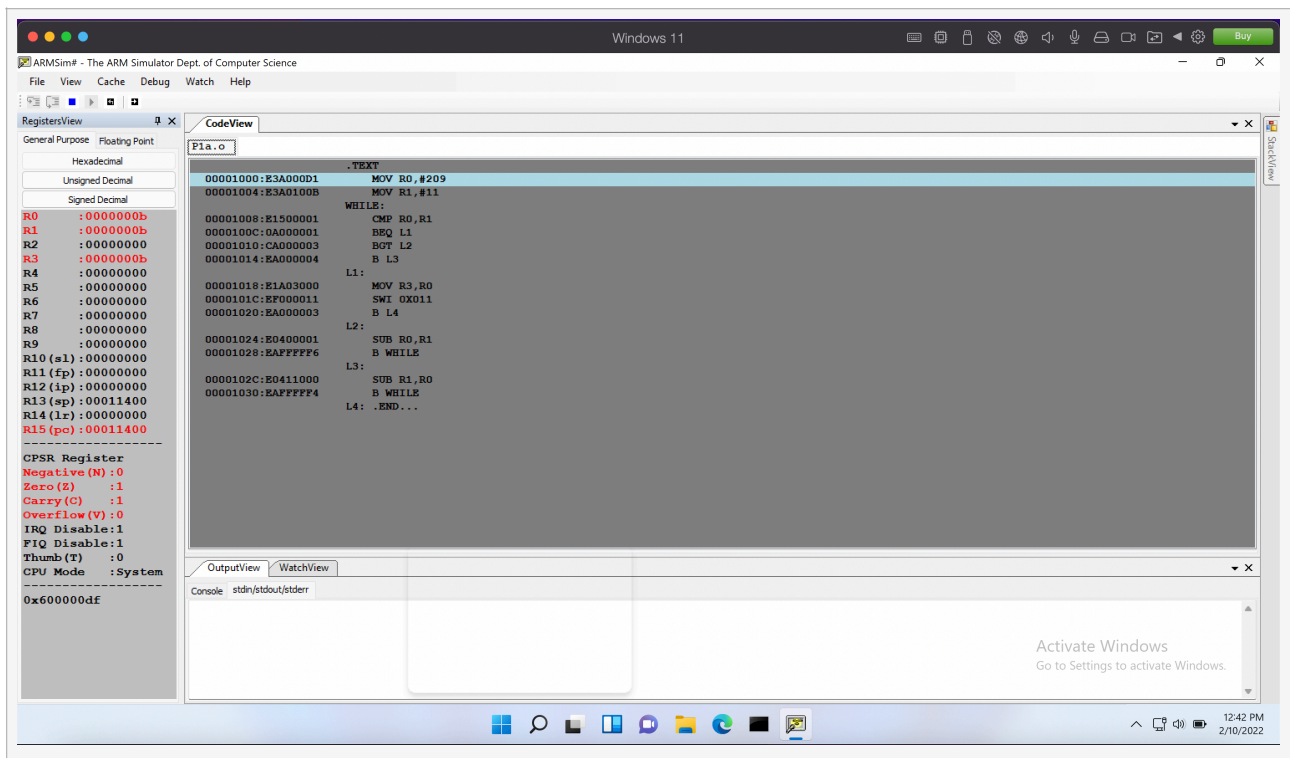
Week 4

Program Number	1 a
Program Qn	Write a program in ARM7TDMI-ISA to find GCD of two numbers. Assume operands to be in the CPU registers

ARM Assembly Code

```
.TEXT
    MOV R0,#209
    MOV R1,#11
WHILE:
    CMP R0,R1
    BEQ L1
    BGT L2
    B L3
L1:
    MOV R3,R0
    SWI 0X011
    B L4
L2:
    SUB R0,R1
    B WHILE
L3:
    SUB R1,R0
    B WHILE
L4: .END
```

Output Screenshot



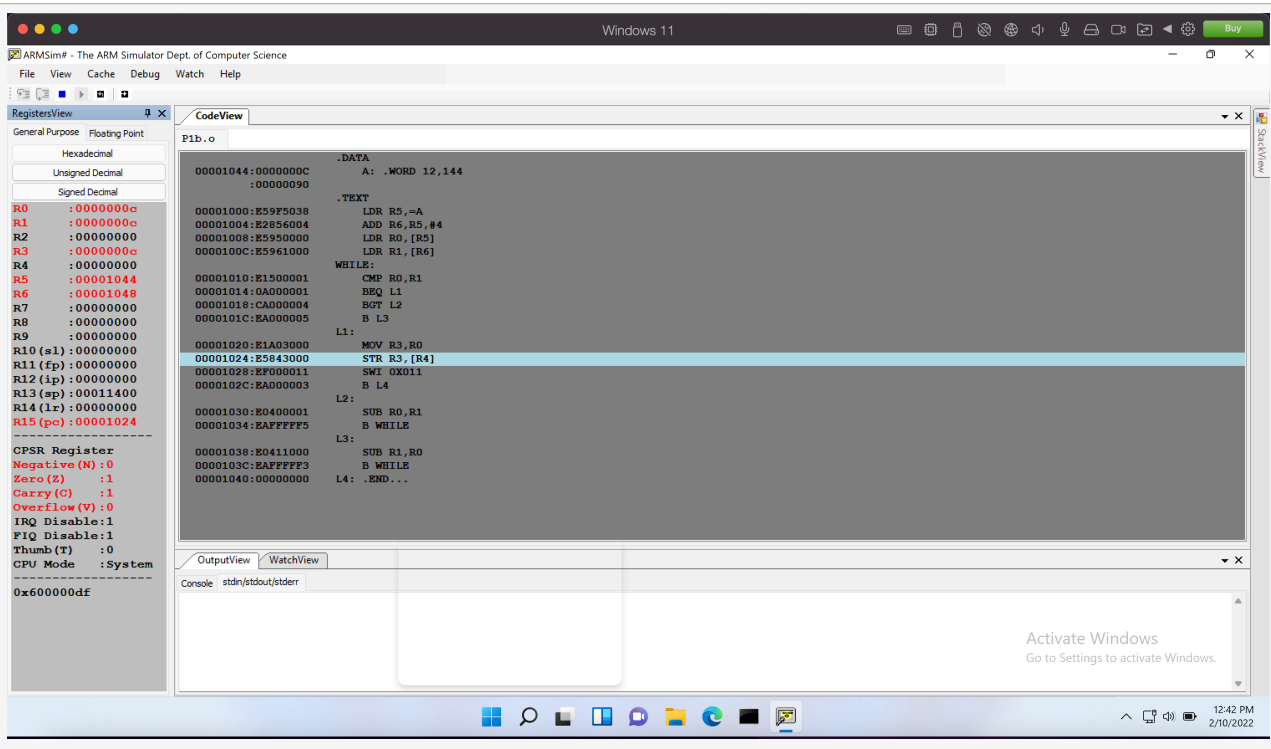
Program Number	1 b
Program Qn	Write a program in ARM7TDMI-ISA to find GCD of two numbers. Assume operands in the memory locations
ARM Assembly Code	

```

.DATA
A: .WORD 12,144
.TEXT
LDR R5,=A
ADD R6,R5,#4
LDR R0,[R5]
LDR R1,[R6]
WHILE:
CMP R0,R1
BEQ L1
BGT L2
B L3
L1:
MOV R3,R0
STR R3,[R4]
SWI 0X011
B L4
L2:
SUB R0,R1
B WHILE
L3:
SUB R1,R0
B WHILE
L4: .END

```

Output Screenshot

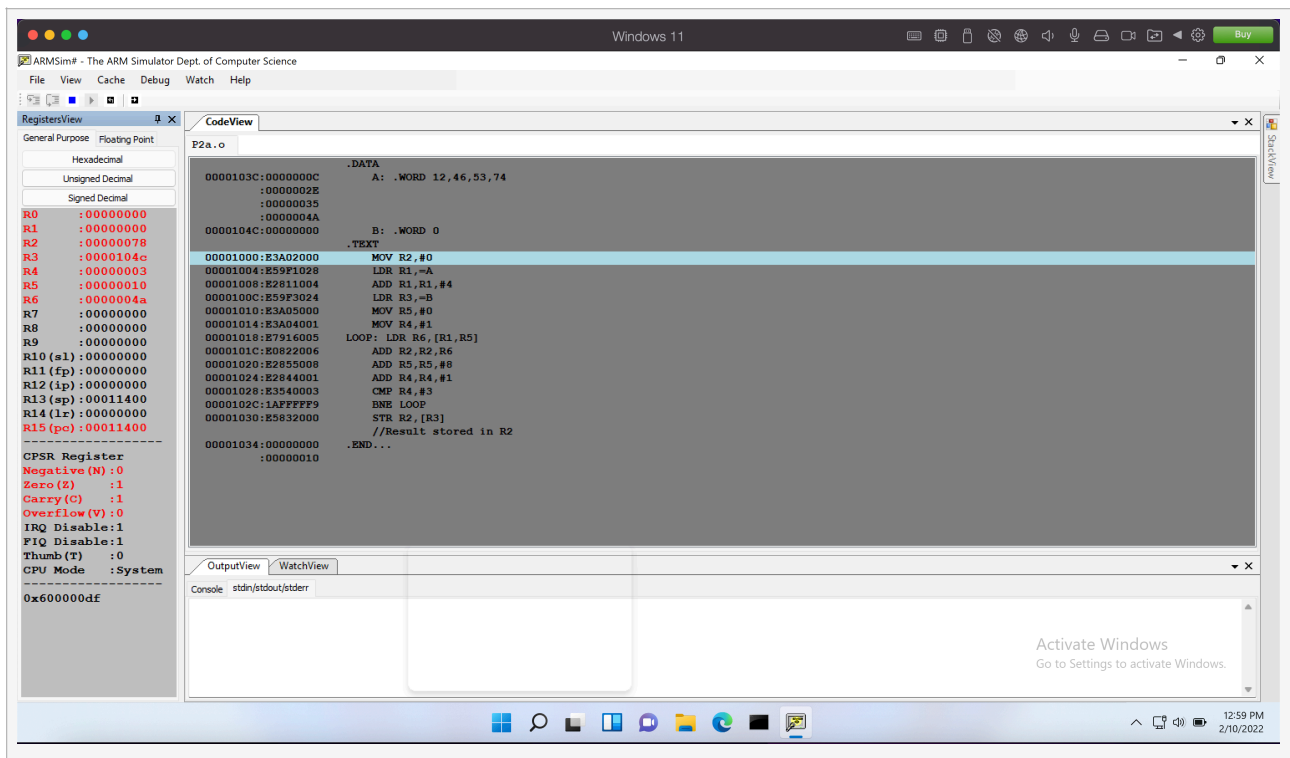


Program Number	2 a
Program Qn	Write a program in ARM7TDMI-ISA to find the sum of N data items at alternate [odd or even positions] locations in the memory. Store the result in the memory location. Use Pre-indexing addressing mode

ARM Assembly Code

```
.DATA
A: .WORD 12,46,53,74
B: .WORD 0
.TEXT
MOV R2,#0
LDR R1,=A
ADD R1,R1,#4
LDR R3,=B
MOV R5,#0
MOV R4,#1
LOOP: LDR R6,[R1,R5]
      ADD R2,R2,R6
      ADD R5,R5,#8
      ADD R4,R4,#1
      CMP R4,#3
      BNE LOOP
      STR R2,[R3]
      //Result stored in R2
.END
```

Output Screenshot



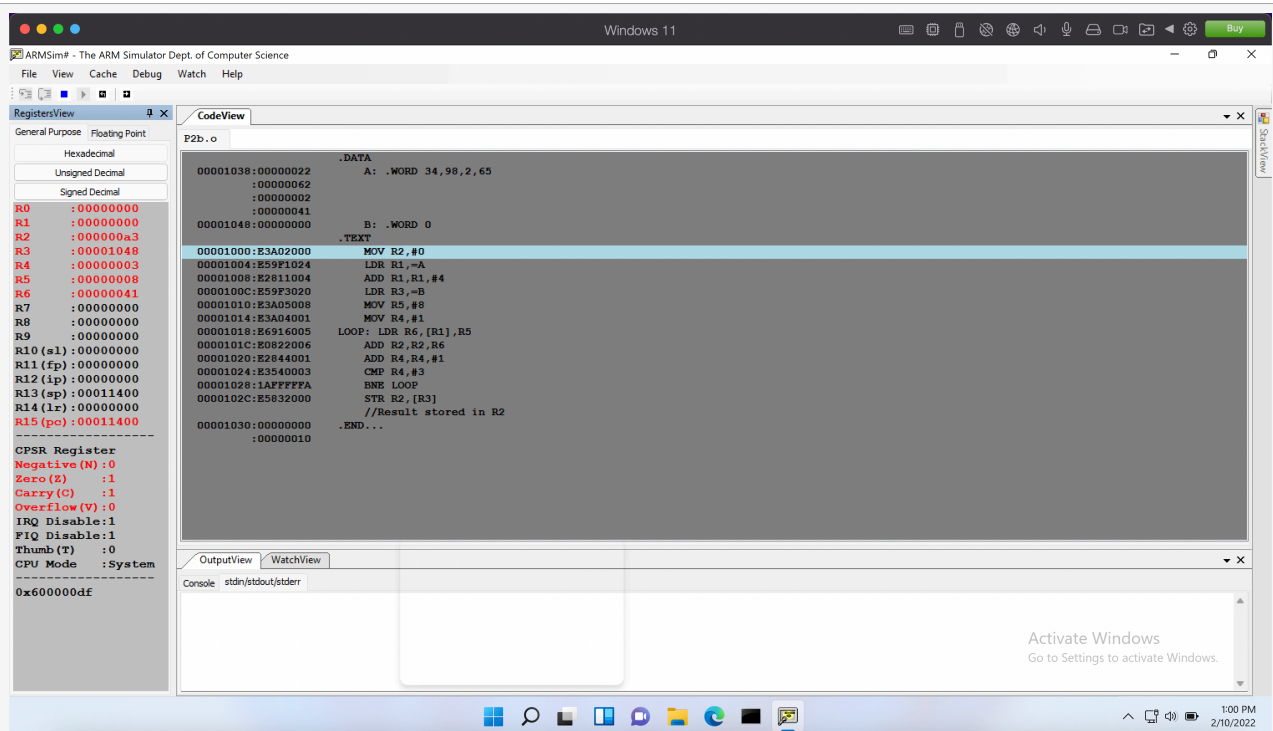
Program Number	2 b
Program Qn	Write a program in ARM7TDMI-ISA to find the sum of N data items at alternate [odd or even positions] locations in the memory. Store the result in the memory location. Use Post- Indexing addressing mode
ARM Assembly Code	

```

.DATA
A: .WORD 34,98,2,65
B: .WORD 0
.TEXT
MOV R2,#0
LDR R1,=A
ADD R1,R1,#4
LDR R3,=B
MOV R5,#8
MOV R4,#1
LOOP: LDR R6,[R1],R5
      ADD R2,R2,R6
      ADD R4,R4,#1
      CMP R4,#3
      BNE LOOP
      STR R2,[R3]
      //Result stored in R2
.END

```

Output Screenshot



Disclaimer:

- The programs and output submitted is duly written, verified and executed by me.
- I have not copied from any of my peers nor from the external resource such as internet.
- If found plagiarized, I will abide with the disciplinary action of the University.

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