**1.** Write a function that, given a number n, returns another number where the k<sup>th</sup> bit from the right is set to to 0.

## Examples:

```
killKthBit(37, 3) = 33 because 37_{10} = 100101_2 \sim 100001_2 = 33_{10} killKthBit(37, 4) = 37 because the 4<sup>th</sup> bit is already 0.

int killKthBit(int n, int k) {

yetvrn (n & ~(1 << (k-1)));
}
```

2. mov vs lea - describe the difference between the following:

- **3.** Invalid mov Instructions Explain why these instructions would not be found in an assembly program.
- a) movl %eax, %rdx

destination operand has incorrect size.

b) movb %di, 8(%rdx)

c) movq (%rsi),8(%rbp)

source and destination cannot both be memory references.

d) movw 0xFF, (%eax)

**4.** What would be the corresponding instruction to move 64 bits of data from register  $\rrangle$  to register  $\rrangle$  ?

**5**. Operand Form Practice (see page 181 in textbook) Assume the following values are stored in the indicated registers/memory addresses.

Address	<u>Value</u>	Register	<u>Value</u>	
0x104	0x34	%rax	0x104	

0x108	0xCC	%rcx	0x5
0x10C	0x19	%rdx	0x3
0x110	0×42	%rbx	0×4

Fill in the table for the indicated operands:

<u>Operand</u>	<u>Value</u>	<u>Operand</u>	<u>Value</u>
\$0x110	0×110	_ 3(%rax, %rcx)	0×19
%rax	0×104	_ 256(, %rbx, 2)	0 × CC
0x110	0×42	(%rax, %rbx, 2)	0×19
(%rax)	0× 34	_	
8(%rax)	0×19	_	
(%rax, %rbx)	0×CC		

**6.** Condition Codes and Jumps - Assume the addresses and registers are in the same state as in Problem 6. Does the following code result in a jump to .12?

- (1) 0×104 + 0×04 = 0×108 → 1/2rdi
- 2) Sets codes according to 0x108 0x100, no codes set.
  - 3) jg is calculated as  $\sim (SF^{\Lambda}OF) g \sim ZF$ , i.e.  $\sim (0^{\Lambda}O) g \sim 0 = |g| = 1$ . Hence jump.