

# A Survey of Error-Correcting Codes for Channels With Symbol Synchronization Errors

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**Abstract**—We present a comprehensive survey of error-correcting codes for channels corrupted by synchronization errors. We discuss potential applications as well as the obstacles that need to be overcome before such codes can be used in practical systems.

**Index Terms**—Error-correcting codes, deletion errors, duplication errors, insertion errors, synchronization errors.

## I. INTRODUCTION

EARLY communication systems designers were preoccupied with symbol and word synchronization as much as they were preoccupied with additive noise. Samuel Morse, when designing his eponymous code in the 1830s, allowed three time units of silence for a letter space and six time units of silence for a word space, a high price to pay to reduce the synchronization errors and facilitate the decoding of messages transmitted over telegraph lines. Today, synchronization problems remain an integral part of technological systems operating in environments affected by uncertainties in timing, or time noise. These include data storage systems like magnetic and optical recording [1], semiconductor devices and integrated circuits [2], and synchronous digital communication networks [3]. Time noise introduces insertions and deletions of symbols for the recipient, and as a result, systems corrupted by timing errors do not know their exact position when processing data.

Synchronization and additive noise are usually treated as different problems and overcome using different techniques. This being said, both have the same effect on communication channels, i.e., reducing their capacity. Unfortunately, although it has early [4] and often been conjectured that error-correcting codes capable of correcting timing errors could improve the overall performance of communication systems, they are quite challenging to design, which partly explains why a large collection of synchronization techniques not based on coding were developed and implemented over the years. Channels corrupted by synchronization errors have memory, hence the techniques developed for memoryless channels and additive noise can seldom be used straightforwardly.

Tools developed for error-correcting codes robust against timing errors could also be of interest for a number of problems that can be tackled using synchronization models, like speech [5] and pattern recognition [6]. Deletion channels with large alphabets can model packet-switched communication networks like the Internet, where packets are either lost or dropped due to buffer overflow at finite-buffer queues [7]. When codes with variable-length symbols are used to transmit information, additive noise can cause synchronization errors by changing the boundaries of the symbols. One such instance is differential pulse-position modulation over wireless optical networks [8]. Insertions and deletions can also occur for a large class of distributed problems involving reconciliation of correlated data [9] such as reconciliation of nucleotide sequences in DNA molecules [10], remote data storage [11], and synchronization of mobile data [12].

In this article, we present a comprehensive survey of synchronization error-correcting codes, their potential applications, as well as the obstacles that need to be overcome before such codes can be used in practical systems. Our motivation for such an overview is threefold. Firstly, most comprehensive books on error-correcting codes do not even mention synchronization-correcting codes, and the very few who do, like [13], only do so in passing. A recent survey by Sloane [14] has frequently been used as a reference source, but its scope is limited to block synchronization-correcting codes able to correct a single deletion error per block. Our opinion that there was a need for a list of pointers to relevant references was somehow reinforced when we realized that several results recently presented seemed unaware of overlapping work done as far as forty years ago. Secondly, although major theoretical advances were made, we are still far from a unified theory of synchronization and many fundamental questions remain unanswered. The lack of mathematical tools makes it difficult to construct good coding schemes and efficient decoding algorithms. Likewise, the lack of understanding of most channels with synchronization errors makes it impossible to assess how good the existing codes are. Finally, most of the early work on synchronization-correcting codes was of little interest to communication systems designers. However, modern communication systems require more and more stringent synchronization constraints. Using increasingly faster and cheaper hardware as well as smaller high-capacity data storage devices, it appears that improved coding schemes might be able to complement existing synchronization techniques.

The outline of the article is as follows. Section II begins with a brief introduction to error-correcting codes. It also describes the types of synchronization errors surveyed in this

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article and the challenges they introduce. The synchronization-correcting codes are surveyed in Section III. The codes use a wide range of ideas, techniques and tools, some adapted from their counterparts for noisy channels without synchronization errors, others specifically developed to deal with synchronization errors. Due to the lack of connecting thread among the codes, we tried to regroup similar code families. We start with the seminal work of Levenshtein on algebraic block codes in Subsection III-A. This is followed by nonbinary and perfect codes in III-B, bursts-correcting codes in III-C, synchronizable codes in III-D, marker codes in III-E, codes for weak synchronization errors in III-F, convolutional codes in III-G, spectral-null codes in III-H, expurgated codes in III-I, and codes over random synchronization channels in III-J. The preceding categories are of course not mutually exclusive, one might say arbitrary, so we used them loosely and tried to present the results as coherently as possible. We conclude by discussing future directions in Section IV.

## II. PRELIMINARIES

In 1948, Shannon [15] showed that information could be encoded and transmitted (or preserved) reliably in the presence of noise at any rate below the capacity of the channel. Unfortunately, although Shannon showed the existence of “good” codes, he gave little insight as how to construct or decode them. Since Shannon’s breakthrough, a lot of research was done to construct good error-correcting codes with efficient encoding and decoding algorithms, and today reliable and efficient codes are used in a wide range of digital systems. An overview of error-correcting codes and their applications can be found in one of several books on the subject, like [16] and references therein.

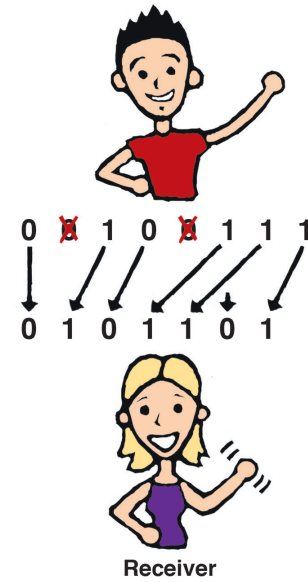
The vast majority of error-correcting codes assume that the transmitter and receiver are synchronized. In particular, the receiver knows where or when the received message starts, the transmitted and received symbols have the same length or duration, and the receiver samples each symbol perfectly. In this setting, the redundancy introduced by the codes is used to correct symbols corrupted by the channel.

In this article, we consider channels that introduce synchronization errors between the transmitted and received versions of a message, and survey the codes designed to correct such errors. As mentioned in the introduction, these codes are rarely discussed in the literature. All the codes surveyed consider discrete symbols as well as discrete synchronization errors. Of course, in some practical scenarios, discrete symbols are ill suited for transmission or storage of information, and the synchronization errors can be small fractions of the length or duration of a symbol. Nevertheless, symbol synchronization errors are easy to model and capture the difficulty of manipulating information on such channels.

### A. Mathematical Preliminaries

Let  $\Sigma_q \triangleq \{0, 1, \dots, q-1\}$  be an alphabet with  $q \geq 2$  symbols, and let  $\Sigma_q^n$  be the set of all strings of length  $n$  over  $\Sigma_q$ . We write  $x \triangleq x_1x_2\dots x_n$  to denote a  $q$ -ary string of length  $n$  in  $\Sigma_q^n$ . A *run* is a substring of identical symbols of maximum length. For example, the string  $u = 01100$  has three runs.

### Transmitter



### Receiver

Fig. 1. A message corrupted by synchronization errors.

### B. Types of Synchronization Errors

A *synchronization error* is either a *deletion error* or an *insertion error* and excludes substitution errors. In Figure 1, the message 00100111 is transmitted over a channel with synchronization errors. The second and fifth transmitted bits are deleted and a bit is inserted after the seventh transmitted bit, resulting in the received message 0101101. A *duplication* or *repetition error* is a special insertion that replaces a bit by two copies of the same bit. Deletions and duplications arise naturally in systems where errors are caused by the varying rate of imperfect sampling devices. This is shown in Figure 2, where the transmitted message 00100111 is sampled with four duplication errors and a deletion error at the receiver, resulting in the received message 00011100011. A *bitshift error*, also called *peak-shift error*, transforms the substring 01 into the substring 10, or vice-versa. A *bitshift error of size  $\alpha$*  transforms the substring  $0^\alpha 1$  into the substring  $10^\alpha$ , or vice-versa, and also corresponds to the deletion of  $\alpha$  zeroes on one side of a 1 followed by the insertion of  $\alpha$  zeroes on the other side. Bitshift errors are of interest because they can be caused by intersymbol interference in magnetic recording systems where information is represented using polarity changes along the data track.

### C. Obstacles Specific to Synchronization Error-Correcting Codes

Besides the usual challenges facing code designers like constructing codes with good distance properties and manageable decoding algorithms, synchronization errors introduce difficulties that do not occur for other classes of errors. One of them is that a single uncorrected synchronization error can have catastrophic consequences by causing a huge burst of substitution errors lasting until the system is resynchronized. Consider the example shown in Figure 3. Using the mapping

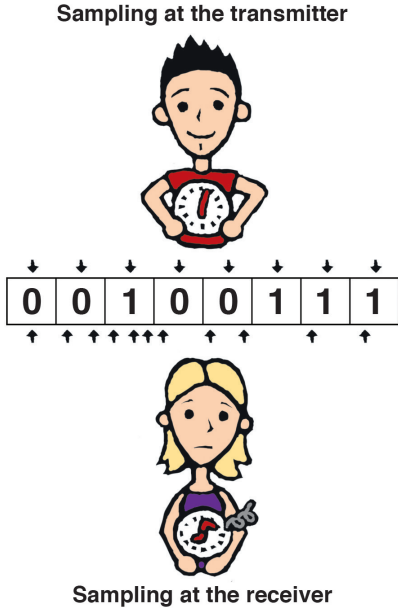


Fig. 2. Deletion and duplication errors caused by the varying rate of sampling devices.

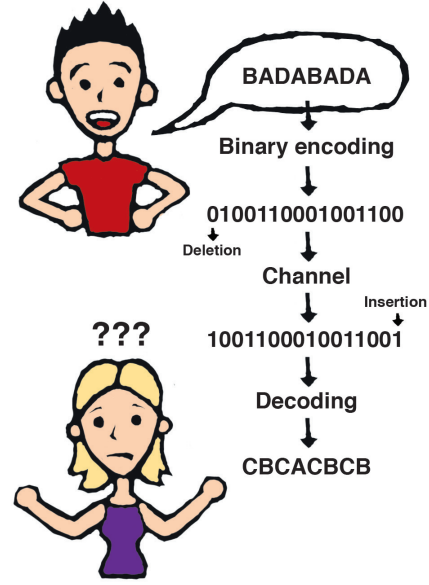


Fig. 3. Huge burst of substitution errors caused by an uncorrected synchronization error.

$\{A \leftrightarrow 00, B \leftrightarrow 01, C \leftrightarrow 10, D \leftrightarrow 11\}$ , the word BADABADA is encoded into the binary string 0100110001001100 and transmitted over a channel with synchronization errors. If the first bit is deleted and left uncorrected, then the received binary message corresponds to the word CBCACBCB.

In most communication systems, long messages are divided into blocks or words, thus another challenge introduced by synchronization errors is that the boundaries of the blocks might be unknown to the receiver. It is therefore required to deal with symbol and word synchronization in order to design coding schemes for such systems. Furthermore, a code that can correct a certain number of synchronization errors in one block does not necessarily keep the same correction capability when used to encode a long message using several blocks, because received sequences cannot always be parsed. Consider, for example, the code consisting of the two codewords 000 and 111. This code can clearly correct two deletion errors if a single block is transmitted, but not two deletions per block if a long message is divided into blocks of length 3. For instance, the received string 000000 could have been produced, among others, by the uncorrupted transmission of 000000 and by the transmission of 000000000000 with two deletion errors per block.

### III. SYNCHRONIZATION-CORRECTING CODES

#### A. Binary Algebraic Block Codes

The Varshamov-Tenengolts codes [17] consist of all the binary vectors of length  $n$  satisfying

$$\sum_{i=1}^n i \cdot x_i \equiv a \pmod{m} \quad (1)$$

for  $m = n + 1$  and a fixed  $a$  such that  $0 \leq a \leq m - 1$ . Binary algebraic synchronization error-correcting codes were first studied by Levenshtein [18], who realized that Varshamov

and Tenengolts' codes, originally constructed to correct one asymmetric error, were also asymptotically optimal single-synchronization-correcting codes. Levenshtein proved that the number of codewords satisfying (1) is asymptotically equal to  $\frac{2^n}{n}$  and provided a simple decoding algorithm to correct one synchronization error. He also gave bounds on the size of codebooks able to correct more than one synchronization error and explained how to maintain synchronization during long transmissions by inserting separators between blocks. Varshamov [19] proved that the number of vectors satisfying (1) is maximized when  $a = 0$ , henceforth we will consider the case  $a = 0$  when referring to Levenshtein single-synchronization codes. Ullman [20] independently presented a slightly different and more redundant family of codes also capable of correcting one synchronization error.

As we show in this survey, Levenshtein single-synchronization-correcting codes were subsequently modified and adapted in numerous coding schemes. In fact, systems of congruences similar to the one introduced in (1) are behind a large fraction of the work on algebraic synchronization-correcting codes. For instance, Levenshtein [18] showed that the codes consisting of the codewords of length  $n$  satisfying (1) with  $m = 2n$  could correct one synchronization error or one substitution error. Saowapa, Kaneko, and Fujiwara [21] showed that single-synchronization-correcting codes in systematic form could be constructed using a subset of Levenshtein codes. The number of codewords of length  $n$  in their codebooks is exactly  $\frac{2^{n-1}}{n}$  when  $n$  is a power of 2, which is asymptotically equal to half the number of codewords in Levenshtein codebooks. Tenengolts [22] constructed codes that can correct a substitution error immediately followed by a deletion error, again using a similar family of congruences.

Unfortunately it does not seem that Varshamov-Tenengolts' construction for a single error can be generalized to correct multiple synchronization errors in an asymptotically optimal

way. For instance, Helberg and Ferreira [23] extended it for up to five insertion and deletion errors, but the loss in rate is significant, they do not provide efficient encoding and decoding algorithms, and their technique does not scale well for large block sizes. Double-synchronization-correcting codebooks found using a greedy algorithm were studied by Swart and Ferreira [24], who improved upon the results from [23].

Sloane [14] observed that interesting questions remain unanswered when a single bit is deleted from a codeword, like the difference between the size of the largest codebooks and the size of the largest linear codebooks able to correct one deletion error. It should also be pointed out that the size of the spheres associated with deletion-correcting-codewords is not constant. Although the number of subsequences that can be obtained by deleting one symbol from a string is equal to the number of runs of the said string, no such simple expressions exist when two or more bits are deleted from a string [25]. This partly explains why it seems very challenging to construct algebraic codes for more than one synchronization error. Schulman and Zuckerman [26] showed the existence of polynomial-time encodable and decodable codes which are asymptotically good for channels allowing synchronization and transposition errors, but their constructions are not described explicitly and seem to be only of theoretical interest. They use a concatenated scheme consisting of a Reed-Solomon outer code and an inner code found by a greedy algorithm.

### B. Nonbinary and Perfect Codes

Nonbinary synchronization-correcting codes were first studied by Calabi and Hartnett [27] and Tanaka and Kasai [28]. Tenengolts [29] generalized Levenshtein single-synchronization-correcting codes for nonbinary alphabets. Levenshtein [30] derived bounds for the maximum size of nonbinary codes capable of correcting  $s$  synchronization errors. He also developed asymptotic bounds for the maximum size of single-deletion-correcting codebooks in  $\Sigma_q^n$  as  $\frac{q}{n}$  approaches infinity [31].

In another seminal paper, Levenshtein [31] defined and studied perfect deletion-correcting-codes. A code  $C \subseteq \Sigma_q^n$  capable of correcting  $s$  deletion errors is perfect if the disjoint spheres associated to its codewords partition  $\Sigma_q^{n-s}$ . Levenshtein proved that all the binary single-synchronization-correcting codes whose codewords satisfy (1) are perfect deletion-correcting codes. Using ordered Steiner systems [32], he also constructed perfect deletion-correcting codes of length three and any alphabet size, as well as perfect codes of length four and even alphabet size. Several authors [33]-[42] followed and studied perfect nonbinary deletion-correcting-codes using combinatorial designs. Besides the Varshamov-Tenengolts codes, all the constructions are derived using ad hoc techniques and without encoding or decoding algorithms.

Hollmann [43] and Konstantinidis [44] studied the relationships between distance functions and the correcting capabilities of codes. Konstantinidis [45], [46] also presented an algorithm that tests whether a code is error-correcting for a large class of channels corrupted by synchronization and substitution errors.

Mitzenmacher [47] developed capacity-approaching low-density parity-check codes for deletion channels with  $q$ -ary alphabets. For sufficiently large  $n$  and  $q$ , he constructed codes of length  $n$  with rate  $(1 - p_d)(1 - \epsilon)$  that can achieve a probability of failure in  $\frac{n^{O(1/\epsilon^2)}}{q}$  on a channel deleting each symbol with probability  $p_d$ . The work is mainly of theoretical interest due to the fact that the computational complexity of the decoding algorithm is in  $n^{O(1/\epsilon^2)} \log q$ . Inspired by Mitzenmacher's approach, Metzner [48] proposed to encode  $k$  information packets of size  $r \gg 2n - k$  into a codeword of  $n$  packets using an  $(n, k)$  linear code with minimum distance  $d$ . The verification-based decoding algorithm requires  $O(n^3)$  bit operations and can correct  $d - 3$  packet deletions, even if the packets are not received in order. Corrupted packets can also be discarded with high probability.

Tonien and Safavi-Naini [49] and McAven and Safavi-Naini [50] showed that generalized Reed-Solomon codes can be used to correct deletions and decoded in polynomial time using the list decoding algorithm of Guruswami and Sudan [51]. For instance, they found a 6619-ary code of length 18 that can correct fifteen symbol deletions. The motivation behind their work was to use the codes to trace fingerprinted media corrupted by colluders cropping their pirate copies [52].

As opposed to nonbinary codes like Reed-Solomon codes, which can be used to correct bursts of binary substitution errors, a single bit deletion or insertion in a codeword from a large alphabet is sufficient to corrupt all the transmitted symbols. Consequently, synchronization-correcting codes with large alphabets can only work over channels that delete or insert entire symbols. Furthermore, when the alphabet size is very large, only a small overhead is required to transform a channel that deletes and transposes symbols into a symbol erasure channel. The obvious example here is the transmission over packet-switched networks, where short preambles allow to sort the packets and to know which ones have been deleted by the channel. Capacity-approaching rateless erasure codes [53] with extremely small encoding and decoding complexities can also be used to deal with corrupted, deleted, duplicated, and unsorted packets. Unless improved coding schemes are discovered, it is thus unlikely that synchronization-correcting codes with very large alphabets will become a viable alternative to erasure codes and other techniques used in packet-switched networks in the foreseeable future.

### C. Bursts of Synchronization Errors

A few codes were specifically designed to correct bursts of synchronization errors. The first such construction is from Levenshtein [54], who, using a family of congruences similar to the one introduced in (1), constructed asymptotically optimal codes capable of correcting two consecutive deletion errors.

Iizuka, Kasahara and Namekawa [55] designed codes that can correct a burst of substitution errors as well as a burst of insertion or deletion errors occurring in the burst of substitution errors. Their codes consist of a coset code from a cyclic code interleaved with a single burst code. The decoding is done by trying all the possible positions of synchronization bursts of increasing size until it is possible to deinterleave a



synchronized codeword and correct the burst of substitution errors. One of their codes has blocks of length 1245, a rate of 0.33 and can correct, in each block, a burst of 8 deletion or insertion errors occurring in a burst of substitution errors of size 83. Iwamura and Imai [56] constructed a code that splits information sequences in  $k$  segments of  $q$  bits, and can correct one synchronization error and a burst of substitution errors as long as all the errors are located in the same segment. The number of required parity bits is  $6q + 7 \log k + O(\log q)$ .

Bours [57] constructed codes that can correct small infrequent bursts of deletion or insertion errors. His construction is a 2-dimensional array code, where the rows of the array are codewords of a comma-free code used to recover synchronization, and the columns of the array are codewords from the (32,16,8) Reed-Muller code used to correct substitution errors as well as erasures caused by the temporary loss of synchronization.

#### D. Synchronizable Codes

Several researchers have designed codes to protect communication systems against loss of synchronization, like comma-free codes [58], [59], prefix-synchronized codes [60], codes with bounded synchronization delay [61], and synchronization with timing codes [62]. These synchronizable codes are designed to recover block boundaries efficiently and sometimes to correct a few substitution errors or to estimate the time indexes of the decoded symbols, but not to correct synchronization errors per se. Blocks corrupted by synchronization errors are usually discarded. There is a large body of literature on synchronizable codes and their applications, which is beyond the scope of the present survey. An overview of the early work on the subject was written by Stiffler [63], and more recent results can be found in [64]-[67] and references therein.

Morita, van Wijngaarden, and Vinck [68] used Levenshtein single-synchronization-correcting codes to construct prefix synchronized codes capable to correct one synchronization error per codeword. Calabi and Hartnett [69] presented a construction very similar to comma-free codes using carefully selected subsets of Levenshtein codes so they can be used in a concatenated fashion. The resulting codes can either correct one synchronization error or  $t - 2$  substitution errors every  $t$  blocks. No decoding algorithm is provided, but explicit codes for various block lengths, as cited in [69], are included in an unpublished report [70]. Calabi and Hartnett's codes were extended by Tanaka and Kasai [28] to correct  $e$  synchronization errors every  $t \geq 2e + 1$  blocks. Liu and Mitzenmacher [71] also used single-deletion-correcting codes in a concatenated fashion so they can correct one deletion error per block. The rate of their codes is lower than the rate of Calabi and Hartnett's codes, although they provided a simple linear time decoding algorithm.

#### E. Marker Codes

Another technique to detect and correct synchronization errors is to insert periodic markers in the codewords. Marker codes were first introduced by Sellers [72], who suggested to insert the substring 001 at regular intervals in a codeword

from a burst-correcting code. The markers can be used to find the interval where a single synchronization error has occurred, after which a bit is deleted or inserted in the middle of the interval and the resulting burst of substitution errors is corrected by the outer decoder. Sellers also discussed use of longer markers to correct bursts of synchronization errors.

The separators used by Levenshtein [18], [73] in two of his constructions play the same role as markers. Ferreira et al. [74] used markers based on Levenshtein single-synchronization-correcting codes that can recover synchronization if there is at most one substitution or synchronization error per block, where a block contains the bits between consecutive markers plus a marker. Their algorithm can correct synchronization and substitution errors if there is at most one of them per two blocks.

Sethakaset and Gulliver [75] used markers to correct insertion and deletion errors for communications over wireless infrared channels, and obtained improved results in [76] using an inner marker code and an outer Reed-Solomon code. It should be noted here that the synchronization errors are not caused by timing uncertainties of the channel. Instead, the authors use differential pulse-position modulation [8], for which the symbols do not have the same duration, thus additive noise results in the insertion and the deletion of nonbinary symbols.

Additional codes using markers and probabilistic synchronization are discussed in Subsection III-J.

#### F. Codes for Weak Synchronization Errors

In this section, we survey codes able to correct weak synchronization errors, weak in the sense that the synchronization between the transmitter and receiver is partly preserved. Weak synchronization errors include duplication errors, bit shifts, and binary channels where only 0s (or only 1s) can be deleted or inserted. For all these errors, the channel can only change the length of the runs of the transmitted messages. Runs cannot be completely deleted, nor can new runs be created. This is clear for duplications errors. For bit shifts and binary channels where only 0s can be deleted or inserted, the 1s act as markers between the zeroes, and the decoding problem is to recover the original number of zeroes between each 1.

The first study of codes robust against weak synchronization errors was again done by Levenshtein [73]. Using congruences similar to the one used in (1), he constructed asymptotically optimal binary codes capable of correcting the insertion or deletion of a single one. His construction can be extended to build codes robust against  $s$  insertions and deletions of ones with asymptotically minimal redundancy. He also showed that a separator of at least  $2s + 1$  bits must be inserted between consecutive blocks, and that the separator  $0^s 1^{s+1}$  could be used. Dolecek and Anantharam [77] presented binary codes able to correct multiple duplication errors, which are equivalent to codes capable of correcting multiple insertions of ones. Their construction is asymptotically better than Levenshtein's by a constant factor, although it does not correct deletions. Dolecek and Anantharam [78] also described a prefixing method for correcting multiple duplication errors in any binary code. The size of the prefixes grow logarithmically with the size of the codewords.

A  $(d, k)$ -constrained runlength-limited code is a code such that there are at least  $d$  and at most  $k$  zeroes between consecutive ones. Constrained codes are commonly used to reduce intersymbol interference in magnetic recording systems by increasing the distance between consecutive 1s. Some classes of constrained runlength-limited codes have been found to be robust against weak synchronization errors. Kuznetsov and Vinck [79] constructed a concatenated constrained code able to correct a single bitshift of size up to  $\frac{k-d}{2}$  or the deletion of  $\frac{k-d}{2}$  zeroes between consecutive ones or the insertion of  $\frac{k-d}{2}$  zeroes between consecutive ones. The outer code consists of a  $(k-d+1)$ -ary block code, and the inner code is simply a mapping of the nonbinary symbols into binary codewords of the type  $0^\alpha 1$ , for  $d \leq \alpha \leq k$ .

Similarly to Levenshtein's work on perfect deletion-correcting codes, Levenshtein and Vinck [80] introduced perfect constrained bitshift-correcting codes. They presented perfect binary codes that can correct a bitshift error of size 1, 2, and  $\frac{(p-1)}{2}$  where  $p$  is a prime number. Kløve [81] proved the existence and provided constructions for perfect constrained codes with various error metrics. For instance, one of his perfect codes can correct one bitshift error per block, and another one can correct a single bitshift error or the insertion of a zero or a deletion of a zero.

Roth and Siegel [82] designed constrained BCH codes based on the Lee metric [83] able to correct bitshift errors as well as insertions and deletions of zeroes. For blocks of length  $n = p^m - 1$ , the redundancy of their code is at most  $1 + (r-1) \log_p(n+1)$ , and it can simultaneously correct  $b$  bitshift errors and  $s$  insertions and deletions of zeroes as long as  $2b+s < r$ . The codes are decoded using Euclid's algorithm. Constrained codes based on the Lee metric with similar error-correction capabilities and higher rates were independently obtained by Bours [84]. His codes are constructed by mapping codewords from a  $q$ -ary code based on the Lee metric into  $(d, k)$  constrained codewords.

### G. Convolutional Codes

Another approach is to adapt convolutional codes for synchronization error-correction. This was first suggested by Gallager [85], whose work is discussed in Subsection III-J. A technique proposed to facilitate block synchronization of convolutional codes is to invert alternate symbols at the output of the encoder to avoid long blocks of zeroes and ones, based on the assumption that such long runs are more frequent than long sequences of alternate bits. Baumert, McEliece, and van Tilborg [86] studied which convolutional encoders can output infinitely many consecutive alternate bits, and for the other codes provided upper bounds on the maximum length of such sequences.

Bouloutas, Hart and Schwartz [87] generalized the Viterbi decoding algorithm to correct synchronization and substitution errors from data generated by finite state machines. They mentioned that their algorithm could be used with convolutional codes, although they did not present any results. Hart and Bouloutas [88] extended the work to correct more general errors modeled using channel rules. Their approach cannot be used efficiently for channels allowing a large number of insertion errors between consecutive message symbols.

Mori and Imai [89] used convolutional encoders and a metric based on the Levenshtein distance for the Viterbi decoding algorithm. Brink, Ferreira, and Clarke [90] used convolutional codes from which they pruned branches to obtain a subset of Levenshtein single-synchronization-correcting codes. They presented a code of rate  $\frac{1}{4}$  that can correct one synchronization error. Further results on pruned convolutional codes for deletion and substitution errors were obtained by Cheng and Ferreira [91]. Swart and Ferreira [92] modified convolutional encoders to correct a small number of insertions or deletions of bits by inverting some of the output bits to eliminate large runs of 0s and 1s. They designed codes of rate  $\frac{1}{4}$  and  $\frac{1}{3}$  that can correct one deletion error every 8 and 12 bits, respectively. Dos Santos et al. [93] used convolutional codes and parallel Viterbi decoders to correct a small number of deletions of bits. One of their codes of rate  $\frac{1}{3}$  has a bit error rate of approximately  $10^{-2}$  on a channel that randomly deletes each transmitted bit with probability  $10^{-3}$ . Cheng, Ferreira and Swart [94] used a metric based on the Levenshtein distance to decode convolutional codes for the deletion channel. One of their codes has a rate of  $\frac{1}{4}$  and a bit error rate between  $10^{-4}$  and  $10^{-5}$  on a channel that randomly deletes 1% of the bits.

### H. Spectral Properties of Single-Synchronization-Correcting Codes

The design of codes with low power near the zero frequency was considered for data storage devices as well as communication over metallic cables [95]. A codeword  $x = x_1 x_2 \dots x_n$  where  $x_i \in \{-1, +1\}$  is of  $K$ -th-order zero-disparity if

$$\sum_{i=1}^n i^k x_i = 0 \text{ for } 0 \leq k \leq K. \quad (2)$$

Immink and Beenker [95] studied codewords of  $K$ -th-order zero-disparity and proved that the first  $2K+1$  derivatives of their power spectral density function are zero at zero frequency. Noting the similarities between (1) and (2), Ferreira et al. [74] pointed out that the codes of Immink and Beenker are subsets of constant-weight Levenshtein codes, thus they can correct one synchronization error. They also studied synchronization-correcting codes with nulls at other frequencies, as well as the inclusion of markers between codewords such that the resulting codes can correct one synchronization error per block while preserving their good spectral properties.

Ouahada et al. [96] showed that a family of permutation codes [97] of block length  $M^2$  were subsets of Levenshtein codes and thus could correct one synchronization error. The codes have spectral nulls at the frequency  $\frac{\alpha}{M}$  for  $\alpha \in \{0, 1, \dots, M\}$ . Helberg, Clarke, and Ferreira [98] constructed a code capable to correct one synchronization error as well as a bitshift. The code has the additional property of having a first order null at the zero frequency, and is again a subset of Levenshtein single-synchronization-correcting code.

### I. Expurgated Codes

It has recently been proposed to correct synchronization errors by expurgating existing codes like Reed-Muller and

array-based LDPC codes. Costing only a small reduction in rate, the expurgation allows to keep the robustness of the original codes against substitution errors and, to a certain extent, to use their decoding algorithms efficiently in the presence of synchronization errors.

Dolecek and Anantharam [99] pruned Reed-Muller  $RM(1, m)$  codes of length  $n$  to obtain codes that can correct one deletion or one duplication error per block while remaining robust against substitution errors. Their decoding algorithm is based on the fast Hadamard transform decoder for Reed-Muller codes, and its complexity is in  $O(n \log n)$ . The expurgation reduces the number of information bits by one. By further pruning  $RM(1, m)$  codes, Dolecek [100] constructed codes that can correct one deletion error and one duplication error, although no decoding algorithm is given. Dolecek and Anantharam [101] expurgated array-based LDPC codes so they can also correct one duplication error per block. The rate loss is asymptotically equal to zero as the code length approaches infinity. It should be mentioned that all the schemes in this subsection assume that the boundaries between the codewords are known.

#### J. Codes for Random Synchronization Channels

All the synchronization-correcting codes presented so far have the drawback of only working under restrictive noise assumptions, like one synchronization error every  $k$  bits, the insertion and deletion of zeroes or the corruption of large symbols. On a channel which, say, randomly deletes 1% of the bits, they will either suffer from rates far below capacity or fail to maintain synchronization, resulting in catastrophic bursts of substitution errors. The codes that follow were designed for more adversarial noise assumptions.

In 1961, Gallager [85] studied a binary synchronization channel where each input bit is either deleted, complemented, or replaced by two random bits. He suggested to add pseudo-random sequences to the output of convolutional encoders and to correct synchronization errors using a sequential decoding algorithm. He also derived a computational cut-off rate above which the expected computation required by the decoding algorithm becomes unbounded. One should note that Gallager's work precedes the Viterbi decoding algorithm [102], and that like his early work on low-density parity-check codes, it has somehow been overlooked for a long time. Bahl and Jelinek [103] studied very general channels corrupting input sequences into variable-length output sequences. They also designed a stack decoding algorithm for these channels.

Chen et al. [104] constructed concatenated codes for deletion channels. Their inner codes, used to recover synchronization, combine Levenshtein single-synchronization-correcting codes and a marker, whereas the outer codes are low-density parity-check codes. One of their codes has a rate of 0.21 and can achieve a bit error rate of  $10^{-4}$  on a channel that randomly deletes 8% of the bits.

Davey and MacKay [105] designed a concatenated scheme using a nonlinear inner code named watermark code and a low-density parity-check outer code over a nonbinary field. The output of the LDPC encoder is mapped into a sparse binary string, which is then added modulo 2 to a pseudo-random watermark vector known to the transmitter and the

receiver. The watermark can be seen as a marker uniformly distributed throughout the codeword and is used to recover synchronization using a probabilistic algorithm. The outer LDPC code is used to correct substitution errors caused by the channel and by the imperfect synchronization provided by the inner encoder. One of the codes they presented has a rate of 0.7 and can correct 30 synchronization errors per block of length 5000. Davey and MacKay's approach can also be used to acquire block boundaries, thus synchronization can be maintained during long transmissions.

Ratzer and MacKay [106] replaced the watermark inner code by fixed and pseudo-random markers of different size also decoded with a probabilistic algorithm. The best markers performed better than random watermarks at very low error rates. Ratzer [107] implemented a complete iterative system for marker codes, where the soft output of the inner decoder is transmitted to the LDPC outer decoder. The output of LDPC decoder can then be used by the inner decoder to improve the synchronization. One of his iterative codes has a rate of 0.5, blocks of 4000 bits, and can achieve a block error rate of  $10^{-3}$  on a channel that randomly deletes 7% of the bits.

#### IV. FUTURE DIRECTIONS

Synchronization error-correcting codes have a long and rich history, and have been studied in various forms both for their intrinsic appeal and their potential applications. This being said, the number of fundamental problems remaining unsolved is quite remarkable.

##### A. Binary Block Codes for Multiple Synchronization Errors

Most of the good binary block codes capable of correcting one synchronization error are related to the original construction of Varshamov and Tenengolts [17], and the gap from one to two synchronization errors seems very hard to bridge. There is not a single known algebraic binary block code with a reasonably good rate and reasonably simple encoding and decoding algorithms that can correct more than one synchronization error per block. The maximum size of binary codes capable of correcting two synchronization errors is also unknown for very small block lengths [108].

##### B. Codes for Synchronization Channels

Estimating the capacity of channels corrupted by synchronization errors is a very challenging problem, and a recent survey on the subject was written by Mitzenmacher [109]. Dobrushin [110] generalized Shannon's channel coding theorem and its weak converse for channels with random synchronization errors, but his work does not lead to useful ways of computing capacity bounds. Tremendous progress has been made for deletion channels [111], [112] and channels with duplication errors [113], for instance, but almost nothing is known about the capacity of channels where random deletion and insertion errors can occur simultaneously or about the capacity of more practical channels with synchronization errors and additive noise. It is not surprising, then, that constructing synchronization-correcting codes achieving channel capacity with vanishing bit error probability remains a somewhat distant objective.



### C. From Theory to Practice

If synchronization-correcting codes are ever to be used in practical communication systems, we believe that they should at the very least have the following three properties. Firstly, global loss of synchronization has catastrophic consequences and must be avoided, even if the correction of some of the synchronization errors results in small bursts of substitution errors. Otherwise, it will not be possible to curtail the role of current techniques used to ensure synchronization of communication systems. Secondly, the codes should be able to correct errors caused by additive noise or intersymbol interference at rates close to capacity. Finally, the additional complexity required to correct synchronization errors should not be prohibitive. Unfortunately, none of the codes surveyed in this article satisfies all three properties. This being said, it seems to us that concatenated coding schemes where an inner code is used to recover synchronization and an outer code is used to correct additive noise and imperfect synchronization from the inner decoder are the most promising avenue. To conclude, although it is not clear where and how exactly synchronization-correcting codes could be used, we believe that once good codes are discovered, systems designers will find ways interesting ways to use them.

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