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Is your model checker on time? On the complexity of model checking for timed modal logics

Luca Aceto^{a,1}, François Laroussinie^{b,*}

^a BRICS, Department of Computer Science, Aalborg University, Fredrik Bajers Vej 7-E,
DK-9220 Aalborg Ø, Denmark²

^b Laboratoire Spécification et Vérification, CNRS UMR 8643, ENS de Cachan, 61 av. du Pr. Wilson,
94235 Cachan, France

Abstract

This paper studies the structural complexity of model checking for several timed modal logics presented in the literature. More precisely, we consider (variations on) the specification formalisms used in the tools CMC and UPPAAL, and fragments of a timed μ -calculus. For each of the logics, we characterize the computational complexity of model checking, as well as its specification and program complexity, using (parallel compositions of) timed automata as our system model. In particular, we show that the complexity of model checking for a timed μ -calculus interpreted over (networks of) timed automata is EXPTIME-complete, no matter whether the complexity is measured with respect to the size of the specification, of the model or of both. All the flavours of model checking for timed versions of Hennessy–Milner logic, and the restricted fragments of the timed μ -calculus studied in the literature on CMC and UPPAAL, are shown to be PSPACE-complete or EXPTIME-complete. Amongst the complexity results offered in the paper is a theorem to the effect that the model checking problem for the sublanguage L_S of the timed μ -calculus, proposed by Larsen, Pettersson and Yi, is PSPACE-complete. This result is accompanied by an array of statements showing that any extension of L_S has an EXPTIME-complete model checking problem. We also argue that the model checking problem for the timed propositional μ -calculus T_μ is EXPTIME-complete, thus improving upon results by Henzinger, Nicollin, Sifakis and Yovine.

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1. Introduction

The model checking approach to the computer-aided verification of finite-state programs, first proposed in [25,62], has gained considerable acceptance in the last few years,

* Corresponding author. Fax: +33-1-47-40-24-64.

E-mail addresses: luca@cs.auc.dk (L. Aceto), fl@lsv.ens-cachan.fr (F. Laroussinie).

¹ Fax: +45-98-15-98-89.

² Basic Research in Computer Science, Centre of the Danish National Research Foundation.

in light of its impressive applications in the design and verification of circuits and distributed protocols (see, e.g., [15,19,28,27] and the references therein). In model checking, we establish the correctness of a system with respect to a given specification by showing that a mathematical model of it satisfies correctness criteria expressed in some formal language. Such a language often takes the form of a temporal logic, like CTL [24], or of a modal logic with fixed points, like the modal μ -calculus [50].

The choice of an appropriate specification language for the application at hand is often the result of a trade-off between the contrasting issues of expressiveness and complexity. On the one hand, the use of a highly expressive logic allows one to describe many interesting behaviours of concurrent systems. On the other, since model checking of realistic systems relies on fully automatic computer support, it is crucial to use a specification language which admits efficient model checking algorithms.

The extension of model checking to the specification and verification of real-time systems has been thoroughly studied in the last few years. This has led to the development of specification logics that extend standard untimed formalisms with the quantitative analysis of timing constraints (see, e.g., [6,8,11,23,40,42,54,65]), and to important theoretical results investigating the limits of decidability for model checking. This theory is now embodied in verification tools like HyTech [70], Kronos [76] and UPPAAL [58], which have been successfully used in the verification of non-trivial systems (see, e.g., [18,45,60]). These applications indicate that automatic verification of real-time, embedded software may be feasible in practice. However, despite many important theoretical results presented in op. cit., the literature is lacking a comprehensive analysis of the structural complexity of model checking for the real-time modal logics, and variations thereof, used as specification formalisms in the verification tools CMC [53] and UPPAAL.

A complexity-theoretic analysis of the model checking problem yields useful guidelines for selecting a specification logic which offers a good compromise between expressiveness and complexity—see, e.g., [72] and the references therein for a comprehensive complexity-theoretic perspective on model checking for linear- and branching-time logics for reactive systems, which has been one of the main sources of inspiration for this work.

In the remainder of this section, we introduce the different ways of measuring the complexity of model checking problems, outline our main results, and compare them with those for the untimed case. We end the introduction with a discussion of related work.

The complexity of model checking for real-time logics

In the untimed case, model checking algorithms with a polynomial time complexity, and often small space requirements, have been developed for several branching time temporal logics [12,14,25,29]. In the timed case, most of the model checking problems considered in literature are PSPACE-hard [5,6,31,42]. Clearly the quantitative analysis of timing constraints increases the complexity of model checking, but it is interesting to analyze precisely in which cases this complexity blow-up occurs. In the untimed case, several papers (see, e.g., [32,37,67]) study in detail the effect of the temporal operators, the number of atomic propositions or the depth of operators' nesting in the complexity of model checking, giving a better understanding of the complexity issue. Here, among other things, we address the same kind of problem for the timed case: what happens if time is inserted either only in the model or only in the formula? And what happens if we use less expressive logics with restricted operators?

We consider several timed modal property languages: L_v has been introduced in [54], and is the specification language used in the tool CMC [53]; L_s is a fragment of L_v which has been proposed in [57] in order to improve the efficiency of model checking in practice; SBLL [2] (for Safety and Bounded Liveness Logic) and L_{VS} [1] have been introduced for their properties with respect to the testing timed automaton method that is currently used in verification tools like UPPAAL to check for properties other than plain reachability ones.

For each of these property languages, we study the computational complexity of model checking, using (networks of) timed automata [7] as our system model. As argued by Lichtenstein and Pnueli [59], the complexity of the model checking problem can be measured in three different ways. First, one can fix the specification and measure the complexity as a function of the size of the program being verified (the *program complexity* measure). Secondly, one can fix the program and measure the complexity as a function of the size of the specification (the *specification complexity* measure). Finally, the combined complexity of the model checking problem is measured as a function of the size of both the program and the specification. (The ‘Lichtenstein–Pnueli Thesis’ is that, since models of systems are usually large, whereas specifications are usually small, it is the program complexity of model checking that is the most significant measure of its feasibility.) In this paper we offer complexity results for these three different views of the model checking problem for the property languages we consider. In so doing, we give an a posteriori justification, couched in complexity-theoretic arguments, for some of the folk beliefs in the area of model checking for real-time systems, and for some of the choices made by developers of real-time verification tools.

Outline of the main results

We begin our study by analyzing the complexity of model checking for a timed μ -calculus, denoted by $L_{\mu,v}^+$, and for its alternation-free fragment $L_{\mu,v}$ (AFMC) (Section 3.2). In the untimed setting, such a fragment of the modal μ -calculus plays an important role as a specification formalism because it is fairly expressive and its restricted syntax makes the symbolic evaluation of expressions linear both in the size of the model and the specification. In the real-time setting, we show that the complexity of model checking for the timed AFMC, and for its sublogic L_v , is EXPTIME-complete, as are both the program complexity and the specification complexity (Theorem 9). We show, furthermore, that, perhaps surprisingly, the model checking problem for L_v —and a fortiori for the timed AFMC—is already EXPTIME-hard even if we fix the model to be the inactive process without clocks, *nil* (see Lemma 13). We feel that this result is more than a theoretical curiosity. Indeed, one of the most successful approaches to model checking for variations on the modal μ -calculus is partial model checking—as presented in, e.g., [13,52]—upon which the verification engine of a tool like CMC is based. In this approach, model checking problems for (networks of timed) automata are reduced to checking properties, expressed in the logic L_v , of the process *nil*. Our results show that, in the worst case, such a problem is just as hard as the original one. Similar results apply to the full timed μ -calculus $L_{\mu,v}^+$ we study in this paper. We also prove that the model checking problem for the fragment of L_v without greatest fixed points—essentially, a timed version of Hennessy–Milner logic [41]—is PSPACE-complete (Theorem 11).

We then proceed to develop a thorough analysis of the complexity of model checking for all the other timed modal property languages that we have found in the literature on

Table 1

Overview of the results (C stands for complete)

	Model checking	Prog. compl.	Spec. compl.
$L_{\mu,v}^+, L_{\mu,v}, L_v$	EXPTIME-C	EXPTIME-C	EXPTIME-C
$L_s, SBLL, L_{\forall S}$	PSPACE-C	PSPACE-C	PSPACE-C
L_v^-	PSPACE-C	P	PSPACE-C
L_s^-	coNP-C	P	coNP-C
$SBLL^-, L_{\forall S}^-$	PSPACE-C	PSPACE-C	coNP-C

the tools CMC and UPPAAL. In each case, we offer results pinpointing the program, the specification as well as the combined complexity of model checking for the property languages with and without greatest fixed points. Here we just wish to point out that the model checking problem for the property language L_s is PSPACE-complete, no matter whether the complexity is measured with respect to the size of the program, of the specification or of both (see Theorem 16). In light of the aforementioned results, and assuming that PSPACE is different from EXPTIME, the model checking problem for L_s has a lower computational complexity than that for L_v . Our results thus offer a complexity-theoretic justification for the claims in [57] to the effect that model checking is easier for L_s than for L_v . The source of the lower complexity derives from the observation that the model checking problem for L_s , unlike that for L_v , can be reduced in polynomial time to reachability checking in timed automata—a problem whose PSPACE-completeness was shown in [7].

Since the syntax of the property language L_s contains several seemingly ad hoc restrictions, it is informative to study the structural complexity of model checking for the extensions of this language obtained by removing each of the constraints on the syntax of L_v imposed in [57]. We prove an array of statements to the effect that the resulting extensions of L_s have an EXPTIME-complete model checking problem (Section 3.4). Such results offer some further justification for the choices made in op. cit.

An overview of the main results we have obtained is presented in Table 1, where L^- denotes the fixed point free fragment of a property language L . As argued in Appendix D, the results presented in this paper are robust with respect to changes in the type of guards allowed in timed automata.

Model checking concurrent programs

The aforementioned results on the complexity of model checking are based on the use of timed automata as our model for real-time systems. However, in practice, most real-time systems contain several communicating components, and may be modelled as parallel compositions of timed automata. We denote by *model checking for concurrent programs* the problem of deciding if a modal logic formula holds for a concurrent timed system (viz. a parallel composition of timed automata). Already in the untimed setting, model checking suffers from the so-called state explosion problem in the presence of concurrency. Hence a characterization of the complexity of the model checking problem, for the property languages we study, in the presence of concurrency would yield a more realistic assessment of the hardness of the task of model checking real-time systems. Here we prove that, for all the property languages we consider, unlike in the untimed setting, the concurrency feature does not increase the structural complexity of the model checking problems (Section 4).

Table 2

Timed vs untimed formalisms (C stands for complete)

	$A \models \varphi$		$(A_1 \dots A_n) \models \varphi$ (Un)timed
	Untimed	Timed	
Reachability	NLOGSPACE-C	PSPACE-C	PSPACE-C
(T)CTL	P-C	PSPACE-C	PSPACE-C
AFMC	P-C	EXPTIME-C	EXPTIME-C
full μ -calculus	$UP \cap co-UP$	EXPTIME-C	EXPTIME-C

An explanation of this phenomenon may be gleaned by comparing our results with those for untimed formalisms.

Comparison with results for untimed formalisms

It is instructive to compare the results we present here with similar ones for the untimed (alternation-free) μ -calculus. As previously mentioned, for such a program logic, we have algorithms for model checking that run in time linear both in the size of the program and of the specification. Moreover, both the program and the specification complexities are P-complete [36,51]. The model checking problem for the full μ -calculus is in $UP \cap co-UP$ [47]. Note, however, that the (program) complexity of the (alternation-free) μ -calculus for *concurrent* programs is EXPTIME-complete [51], and this matches exactly the complexity results we offer for $L_{\mu,v}$ and $L_{\mu,v}^+$ model checking. It is also interesting to note that the (program) complexity of CTL model checking and of reachability for concurrent programs is PSPACE-complete [49,51], matching the complexity of model checking for TCTL [6] and of reachability in timed automata, respectively. These results seem to provide a mathematical grounding to the folk belief that “clocks act like concurrent programs”, and that increasing the number of clocks corresponds to adding parallel components.

An overview of the above comparison of the complexity of model checking for untimed formalisms and their timed counterparts is presented in Table 2.

Related work

There is an extensive literature on the complexity of model checking for linear- and branching-time logics for (concurrent) reactive systems (see, e.g., [32,51,55,64,67,72,73] and the references therein). In particular, [51] offers an automata-theoretic approach to the model checking problem for the branching-time logics CTL, CTL* and the μ -calculus, and gives improved space-complexity bounds for this problem. It will be clear to the readers of this study that we have been greatly inspired by the developments in op. cit.—especially in the technical developments related to establishing lower bounds on the complexity of the model checking problems we consider, and in the study of the model checking problems for concurrent programs. The (program) complexity of LTL model checking for concurrent programs was shown to be PSPACE-complete in the classic study [73], also by means of automata-theoretic techniques.

The literature on the complexity of verification for untimed reactive systems is also rich in results on the complexity of *implementation verification*. In implementation verification, one models both implementations and specifications by means of transition systems. The

verification task then consists in checking that the behaviour of the model of an implementation correlates with the behaviour of the model of a specification. The complexity of implementation verification for (concurrent) programs is studied in, e.g., [39,56,63], where both trace- and tree-based approaches are investigated, with and without fairness constraints.

Related literature dealing with the complexity of model checking for real-time logics, and the expressiveness of such languages, has been extensively mentioned throughout this introductory section. Surveys of results in this area of research may be found in, e.g., [8–10,74]. Recent interesting work by Hirshfeld and Rabinovich [43,44] has striven to develop real-time logics which are firmly grounded in the Mathematical Logic tradition, and in the framework of Monadic Logic of Order in particular. The work presented *ibidem* mostly focuses on expressiveness and decidability issues. For example, [44] introduces Quantitative Temporal Logic (QTL), shows that the satisfiability problem for this logic is PSPACE-complete using techniques from Mathematical Logic, and argues that QTL is as expressive as any of its rivals in the literature. Behavioural relations between timed automata and the relationship with timed logics (via characteristic formulae) are studied in [3].

This paper is an expanded version of [4]. Apart from providing proofs of results that were announced *ibidem*, the current paper offers several new developments. In particular, we point out that, amongst other things:

- we deal with the full timed μ -calculus $L_{\mu,v}^+$,
- we study the structural complexity of model checking over networks of timed automata for the real-time logics we consider,
- we analyze the complexity of model checking for extensions of the property language L_s —thus mapping the territory between this property language and L_v , and
- we study the robustness of our results with respect to changes in the model of timed automata under consideration.

2. Preliminaries

We begin by briefly reviewing a variation on the timed automaton model proposed by Alur and Dill [7] (Section 2.1), and the property languages that will be used in this study (Sections 2.2 and 2.3). The algorithms we shall employ in order to establish upper bounds on the complexity of the model checking problems we consider rely on the region graph construction [7]. For the sake of clarity, this construction and its main properties are outlined in Section 2.4. Finally, we recall the basic problems in complexity theory that will find application in our lower bound arguments (Section 2.6).

2.1. Timed automata

Let Act be a finite set of *actions* (ranged over by a, b), and let \mathbb{N} and $\mathbb{R}_{\geq 0}$ denote the sets of natural and non-negative real numbers, respectively. We write \mathcal{D} for the set of *delay actions* $\{\epsilon(d) \mid d \in \mathbb{R}_{\geq 0}\}$.

Let C be a set of clocks. We use $\mathcal{B}(C)$ to denote the set of boolean expressions over atomic formulae of the form $x \sim p$ and $x - y \sim p$, with $x, y \in C$, $p \in \mathbb{N}$, and $\sim \in \{<, >, =\}$. Moreover we write $\mathcal{B}_k(C)$ for the restriction of $\mathcal{B}(C)$ to expressions whose integer constants belong to $\{0, \dots, k\}$. Expressions in $\mathcal{B}(C)$ are interpreted over the

collection of time assignments. A *time assignment*, or *valuation*, for C is a function from C to $\mathbb{R}_{\geq 0}$. We write $\mathbb{R}_{\geq 0}^C$ (with typical elements u, v) for the collection of valuations for C . Given $g \in \mathcal{B}(C)$ and a time assignment v , the boolean value $g(v)$ describes whether g is satisfied by v or not. For every time assignment v and $d \in \mathbb{R}_{\geq 0}$, we use $v + d$ to denote the time assignment which maps each clock $x \in C$ to the value $v(x) + d$. For every set of clocks C' included in C , we write $[C' \rightarrow 0]v$ for the assignment for C which maps each clock in C' to the value 0 and agrees with v over $C \setminus C'$.

Definition 1. A *timed automaton* (TA) is a quintuple $A = \langle \text{Act}, N, n_0, C, E \rangle$ where

- N is a finite set of *nodes*,
- $n_0 \in N$ is the *initial node*,
- C is a finite set of *clocks*, and
- $E \subseteq N \times \mathcal{B}(C) \times \text{Act} \times 2^C \times N$ is a finite set of *edges*. The quintuple $\langle n, g, a, r, n' \rangle \in E$ stands for an edge from node n to node n' with action a , where r denotes the set of clocks to be reset to 0 and g is the enabling condition (or *guard*). In lieu of $\langle n, g, a, r, n' \rangle$, we shall sometimes use the more suggestive $n \xrightarrow{g, a, r} n'$, with g (resp. r) possibly omitted if $g = \top$ (resp. $r = \emptyset$).

We use $\text{MCst}(A)$ to denote the largest integer constant occurring in the guards of A .

In the remainder of this study, we shall write *nil* for the one node timed automaton $\langle \text{Act}, \{n_0\}, n_0, \emptyset, \emptyset \rangle$.

A *state* (or *configuration*) of a timed automaton A is a pair (n, v) , where n is a node of A and v is a time assignment for C . The initial state of A is $(n_0, [C \rightarrow 0])$ where n_0 is the initial node of A , and $[C \rightarrow 0]$ is the time assignment mapping all clocks in C to 0. The operational semantics of a timed automaton A is given by the Timed Labelled Transition System (TLTS) $\mathcal{T}_A = \langle \mathcal{S}_A, \text{Act} \cup \mathcal{D}, s^0, \longrightarrow \rangle$, where \mathcal{S}_A is the set of states of A , s^0 is the initial state of A , and \longrightarrow is the transition relation defined as follows:

$$\begin{aligned} (n, v) &\xrightarrow{a} (n', v') \text{ iff } \exists \langle n, g, a, r, n' \rangle \in E : g(v) = \top \wedge v' = [r \rightarrow 0]v, \\ (n, v) &\xrightarrow{\epsilon(d)} (n', v') \text{ iff } n = n' \text{ and } v' = v + d. \end{aligned}$$

The reader will readily realize that the TLTS determined by the timed automaton *nil* consists of one state s^0 of the form $(n_0, -)$ and uncountably many delay transitions of the form $s^0 \xrightarrow{\epsilon(d)} s^0$ with $d \in \mathbb{R}_{\geq 0}$.

Definition 2. Let $A = \langle \text{Act}, N, n_0, C, E \rangle$ be a timed automaton. We say that a node $n \in N$ is *reachable* in A iff there is a sequence of transitions in \mathcal{T}_A leading from the initial state s^0 to a state of the form (n, v) .

Remark 3. Note that we could consider *extended TAs*, where we assign an *invariant* (i.e. a downward closed clock constraint) to each node to avoid excessive time delays. All the results presented in this paper will still hold for extended TAs. Indeed, given a complexity class \mathcal{C} , having a \mathcal{C} -hardness result for (simple) TAs implies the same for extended TAs, while having a \mathcal{C} membership result for extended TAs implies the same for TAs. Every \mathcal{C} membership result presented here could be easily adapted to extended TAs. This is because such results are based on the size of the region graph (cf. Section 2.4), that is not bigger for extended TAs than for the TAs introduced in Definition 1 (since adding invariants can only remove some states and delay transitions).

2.2. A timed modal μ -calculus

We now define $L_{\mu,v}^+$, a timed modal μ -calculus inspired by the logic L_v [54].

Definition 4. Let K be a finite, non-empty set of clocks (disjoint from C), and Id be a countably infinite set of identifiers (ranged over by X, Y). The set $L_{\mu,v}^+$ of formulae over K and Id is generated by the following grammar:

$$\begin{aligned} L_{\mu,v}^+ \ni \psi, \varphi := & g \mid \varphi \wedge \psi \mid \varphi \vee \psi \mid \langle a \rangle \varphi \mid [a] \varphi \mid \exists \varphi \mid \forall \varphi \\ & \mid K' \underline{\text{in}} \varphi \mid \max(X, \varphi) \mid \min(X, \varphi) \mid X, \end{aligned}$$

where $a \in \text{Act}$, $g \in \mathcal{B}(K)$, K' is a subset of K and $X \in \text{Id}$. Moreover, each occurrence of an identifier X in a formula has to be bound by a $\min(X, \varphi)$ or a $\max(X, \varphi)$ operator.

We use $\text{MCst}(\varphi)$ to denote the largest integer constant occurring in the clock constraints in φ .

New operators like tt , ff , $g \Rightarrow \psi$ (read “ g implies ψ ”) can be easily defined. For example, tt and ff are shorthands for $x \geq 0$ and $x < 0$, respectively, for some $x \in K$.

Given a timed automaton A , we interpret formulae in $L_{\mu,v}^+$ with respect to *extended configurations* (n, v, u) , where (n, v) is a configuration of A and u is a time assignment for K . Whereas the classic modal operators $\langle a \rangle$ and $[a]$ deal with action transitions [41], the operator \exists (resp. \forall) denotes existential (resp. universal) quantification over delay transitions. The clocks in K are so-called *formula clocks*; they increase synchronously with the automata clocks, and are used as stopwatches for measuring the time elapsing between states of the system. The formula $(K' \underline{\text{in}} \varphi)$ initializes the formula clocks in K' to 0 in φ . The constraints g are used to compare the values of formula clocks in the current extended configuration with integer values. Finally, an extended configuration satisfies $\max(Y, \varphi)$ (resp. $\min(Y, \varphi)$) if it belongs to the largest (resp. least) solution of the equation $Y = \varphi$ over the complete lattice of sets of extended configurations. The existence of these solutions is guaranteed by standard fixed point theory [71].

To define the formal semantics of $L_{\mu,v}^+$, it is convenient to consider open formulae—i.e., formulae which may contain occurrences of identifiers which are not bound by least or greatest fixed point operators. Given a timed automaton A , an *environment* is a mapping ρ from identifiers in Id to sets of extended configurations of A . For an environment ρ , identifier X and subset of extended states S , we write $\rho[X \mapsto S]$ for the environment mapping X to S , and acting like ρ on all the other identifiers. The formal semantics of $L_{\mu,v}^+$ (presented in Table 3) associates with every formula $\varphi \in L_{\mu,v}^+$ and environment ρ the set of extended states $\llbracket \varphi \rrbracket \rho$ that satisfy φ , under the assumption that each identifier X is satisfied by the extended states in $\rho(X)$. The interested reader will find more details on this definition in, e.g., [50]. If φ is a closed formula, then the collection of extended states satisfying it is independent of the environment ρ , and will be written $\llbracket \varphi \rrbracket$. In the sequel, for every extended state (n, v, u) and closed formula φ , we shall write $(n, v, u) \models \varphi$ (read “ (n, v, u) satisfies φ ”) in lieu of $(n, v, u) \in \llbracket \varphi \rrbracket$. For a timed automaton A and closed formula φ , the suggestive shorthand $A \models \varphi$ will be used in lieu of $(n_0, [C \rightarrow 0], [K \rightarrow 0]) \models \varphi$.

As an example of a property that can be expressed in $L_{\mu,v}^+$ using fixed points and clock constraints, consider the formula

$$\max(X, [b](\{x\} \underline{\text{in}} \exists(\langle c \rangle \text{tt} \wedge x \leq 3)) \wedge [a]X \wedge \forall X).$$

Table 3

Semantics of $L_{\mu,v}^+$

$\llbracket g \rrbracket \rho$	$\stackrel{\text{def}}{=} \{(n, v, u) \mid g(u) = \text{tt}\}$
$\llbracket \varphi_1 \vee \varphi_2 \rrbracket \rho$	$\stackrel{\text{def}}{=} \llbracket \varphi_1 \rrbracket \rho \cup \llbracket \varphi_2 \rrbracket \rho$
$\llbracket \varphi_1 \wedge \varphi_2 \rrbracket \rho$	$\stackrel{\text{def}}{=} \llbracket \varphi_1 \rrbracket \rho \cap \llbracket \varphi_2 \rrbracket \rho$
$\llbracket \langle a \rangle \varphi \rrbracket \rho$	$\stackrel{\text{def}}{=} \{(n, v, u) \mid \exists (n', v'). (n, v) \xrightarrow{a} (n', v') \text{ and } (n', v', u) \in \llbracket \varphi \rrbracket \rho\}$
$\llbracket [a] \varphi \rrbracket \rho$	$\stackrel{\text{def}}{=} \{(n, v, u) \mid \forall (n', v'). (n, v) \xrightarrow{a} (n', v') \Rightarrow (n', v', u) \in \llbracket \varphi \rrbracket \rho\}$
$\llbracket \exists \varphi \rrbracket \rho$	$\stackrel{\text{def}}{=} \{(n, v, u) \mid \exists d \in \mathbb{R}_{\geq 0}. (n, v + d, u + d) \in \llbracket \varphi \rrbracket \rho\}$
$\llbracket \forall \varphi \rrbracket \rho$	$\stackrel{\text{def}}{=} \{(n, v, u) \mid \forall d \in \mathbb{R}_{\geq 0}. (n, v + d, u + d) \in \llbracket \varphi \rrbracket \rho\}$
$\llbracket K' \dot{\leq} \varphi \rrbracket \rho$	$\stackrel{\text{def}}{=} \{(n, v, u) \mid (n, v, [K' \rightarrow 0]u) \in \llbracket \varphi \rrbracket \rho\}$
$\llbracket X \rrbracket \rho$	$\stackrel{\text{def}}{=} \rho(X)$
$\llbracket \min(X, \varphi) \rrbracket \rho$	$\stackrel{\text{def}}{=} \bigcap \{S \mid \llbracket \varphi \rrbracket \rho[X \mapsto S] \subseteq S\}$
$\llbracket \max(X, \varphi) \rrbracket \rho$	$\stackrel{\text{def}}{=} \bigcup \{S \mid S \subseteq \llbracket \varphi \rrbracket \rho[X \mapsto S]\}$

This formula expresses the fact that, in every state that is reachable by performing a -actions and delays, every occurrence of a b -action can be followed by a c -action within 3 time units. The interested reader is referred to, e.g., [53] for further examples of property specifications in a fragment of $L_{\mu,v}^+$.

2.3. Fragments of $L_{\mu,v}^+$

The following fragments of the logic $L_{\mu,v}^+$ will be of interest in the remainder of this study:

- The alternation-free fragment of $L_{\mu,v}^+$ (denoted by $L_{\mu,v}$) consists of all the formulae where each occurrence of an identifier X , which is bound by a $\min(X, \varphi)$ (resp. $\max(X, \varphi)$) operator, cannot occur in a subformula of φ of the form $\max(Y, \psi)$ (resp. $\min(Y, \psi)$).
- The logic L_v [54] is the fragment of $L_{\mu,v}$ in which only the use of greatest fixed points is allowed.
- The property language L_s [57] is the fragment of L_v without the existential modalities $\langle a \rangle$ and \exists , and where only a restricted disjunction of the form $g \vee \varphi$ (with $g \in \mathcal{B}(K)$) is allowed.
- The property languages $SBLL$ and $L_{\forall S}$ extend L_s , and are interpreted over a slightly different kind of timed automata where
 - (1) \mathcal{U} is a subset of Act such that any edge labeled with $a \in \mathcal{U}$ has the guard tt , and
 - (2) Act contains the label τ used to denote an internal action of automata.

Moreover, the semantics of these property languages is based on a different notion of satisfaction relation (denoted by \vdash in what follows) compared with $L_{\mu,v}^+$, L_v or L_s .

A formula φ holds for an extended configuration (n, v, u) only if φ holds for every (n', v', u) with (n', v') reachable from (n, v) in zero or more τ -transitions. For example,

- $(n, v, u) \vdash [a]\varphi$ iff for every (n', v') such that $(n, v) \xrightarrow{\tau}^* (n', v')$, we have that $(n', v') \xrightarrow{a} (n'', v'')$ implies $(n'', v'', u) \vdash \varphi$; and
- $(n, v, u) \vdash \forall \varphi$ iff for every (n', v') reached from (n, v) by using τ - and delay transitions (of total duration d), we have $(n', v', u + d) \vdash \varphi$.

$SBLL$ extends L_s by allowing the use of $\langle a \rangle \text{tt}$ subformulae with $a \in \mathcal{U}$. $L_{\forall S}$ extends $SBLL$ with new operators \mathbb{W}_S with $S \subseteq \mathcal{U}$. A formula $\mathbb{W}_S \varphi$ holds for (n, v, u) iff φ

holds for any $(n', v', u + d)$ s.t. (n', v') is reachable from (n, v) by using only τ - and delay transitions (with total duration d), but delay transitions with a positive duration occur only in states in which none of the actions in S are enabled.

These two languages can be translated into L_v in the following sense: for any $\varphi \in L_{VS}$, there exists an L_v formula $\bar{\varphi}$ s.t. $A \vdash \varphi$ iff $A \models \bar{\varphi}$. For example, we have that $[a]\psi = \max(X, [a]\psi \wedge [\tau]X)$ (see Appendix A for a complete translation). An important property [1,2] of SBL and L_{VS} is that their model checking problem can be reduced to a reachability problem as follows: for any formula φ of these languages, we can build a *testing automaton* T_φ s.t. $A \vdash \varphi$ iff a reject node is not reachable in the synchronized parallel composition $(A|T_\varphi)$. Moreover it has been shown that L_{VS} is expressive enough to encode any reachability property over the kind of timed automata used in the tool UPPAAL [1]. The interested reader is referred to op. cit. for more information on SBL and L_{VS} .

For each property language L , we shall use L^- to denote its fixed point free fragment.

Remark 5. In [42], a timed μ -calculus T_μ has been proposed, and the model checking problem for T_μ was shown to be PSPACE-hard. The logic T_μ uses a powerful binary operator \triangleright (instead of our modalities $\langle a \rangle$ and Ξ). Our subsequent EXPTIME-completeness result for the model checking problem for the logic $L_{\mu,v}^+$ (see Theorem 9) can be easily adapted to T_μ , yielding an improved lower bound on the complexity of T_μ model checking (see Remark 10).

2.4. The region graph construction

For the sake of clarity, we now briefly review some of the basic techniques and results from the algorithmics of timed automata that will be needed in the remainder of this study. Our presentation will be necessarily sketchy, and we heartily refer the reader to the references for full details.

Regions of time assignments

Automatic verification of timed systems modelled as (networks of) timed automata is possible despite the uncountably infinite number of configurations associated with a timed automaton. The decision procedure for the problem $A \models \varphi$ is based on the well-known *region technique* [6]. Let C^+ denote the set of clocks $C \cup K$. Given A and φ , it is possible to partition the uncountably infinite set of time assignments over C^+ into a finite number of *regions*, in such a way that two extended configurations (n, u) and (n, v) , where $u, v \in \mathbb{R}_{\geq 0}^{C^+}$ are in the same region, satisfy the same subformulae of φ . Formally, the regions can be defined as the equivalence classes induced by the equivalence relation over valuations defined thus: two valuations u and v are in the same region iff they satisfy the same clock constraints in $\mathcal{B}_M(C^+)$, where $M = \max(\text{MCst}(A), \text{MCst}(\varphi))$.

We write $[u]$ for the region which contains the time assignment u , and use \mathcal{R}_k^{Cl} (ranged over by γ) to denote the (finite) set of all regions for a set Cl of clocks and an integer constant k . Given a region $[u]$ in \mathcal{R}_k^{Cl} and $C' \subseteq Cl$, we define the reset operator thus: $[C' \rightarrow 0][u] = [[C' \rightarrow 0]u]$. Moreover, given a region γ , its (unique) successor region in \mathcal{R}_k^{Cl} , denoted by $\text{succ}(\gamma)$, is the region γ' such that:

- $\gamma' = \gamma$ if $\gamma(x > k) = \text{tt}$ for every $x \in Cl$,
- otherwise $\gamma' \neq \gamma$, and for every $u \in \gamma$ there exists $d \in \mathbb{R}_{\geq 0}$ with
 - $[u + d] = \gamma'$, and
 - $[u + d'] \in \{\gamma, \gamma'\}$ for every $d' < d$.

We write $\gamma' \in \text{succ}^*(\gamma)$ if $\gamma' = \text{succ}^i(\gamma)$ for some non-negative integer i .

The region graph

Given a timed automaton $A = \langle \text{Act}, N, n_0, C, E \rangle$, a set K of formula clocks (disjoint from C) and an integer constant M , with $M \geq \text{MCst}(A)$, we can define a symbolic semantics [54] for A over the finite transition system (S, \rightarrow) , called the *region graph*, defined thus:

- the set of symbolic states S is $N \times \mathcal{R}_M^{C^+}$, and
- $\rightarrow = (\bigcup_{a \in \text{Act}} \xrightarrow{a}) \cup \xrightarrow{\text{succ}}$, where, for every symbolic state (n, γ) ,

$$(n, \gamma) \xrightarrow{a} (n', \gamma') \text{ iff } \exists \langle n, g, a, r, n' \rangle \in E : g(\gamma) = \text{tt} \text{ and } \gamma' = [r \rightarrow 0]\gamma,$$

$$(n, \gamma) \xrightarrow{\text{succ}} (n', \gamma') \text{ iff } n = n' \text{ and } \gamma' = \text{succ}(\gamma).$$

Formulae in $L_{\mu, v}^+$ can be readily interpreted over the states of the region graph by evaluating the delay modalities with respect to *succ*-transitions, and guards with respect to regions instead of single time valuations. (Cf., e.g., [54] for details. Note that the developments in op. cit. only deal explicitly with L_v ; however, they apply equally well to the whole of $L_{\mu, v}^+$.) The resulting symbolic semantics is closely related to the standard one: for every $L_{\mu, v}^+$ formula whose clock constraints do not use constants greater than M , region $\gamma \in \mathcal{R}_M^{C^+}$ and valuation $u \in \gamma$, we have that $(n, \gamma) \models \varphi$ iff $(n, u) \models \varphi$. Therefore to decide if $A \models \varphi$ holds, it is sufficient to apply a standard model checking algorithm over the (finite) region graph built from A and φ . (For the sake of clarity, we remark that the approach described above yields a possibly different instance of an untimed model checking problem for every timed automaton A and formula φ .)

In our complexity analyses to follow, we shall need to estimate the size of the region graph. Note that the size of $\mathcal{R}_M^{C^+}$ is in $O(|C^+|! \cdot M^{|C^+|})$. We can estimate the sizes of the set of states S and of the transition relation \rightarrow of the region graph thus:

- $|S|$ is in $O(|N| \cdot |C^+|! \cdot M^{|C^+|})$ and
- $|\rightarrow| = |\bigcup_{a \in \text{Act}} \xrightarrow{a}| + |\xrightarrow{\text{succ}}|$ is in $O((|E| + |N|) \cdot |C^+|! \cdot M^{|C^+|})$.

Moreover, for every region γ ,

$$|\{\gamma' \mid \gamma' = \text{succ}^i(\gamma) \text{ for some } i \in \mathbb{N}\}| \leq 2 \cdot |C^+| \cdot (M + 1).$$

The interested reader is invited to consult, e.g., [6] for details.

2.5. Algorithms over regions

In order to define algorithms operating on region graphs, we shall need some basic operations over regions. We can represent a region γ over a set of clocks C by means of a Difference Bounded Matrix [17,33,75] M_γ , which is a $(|C| + 1) \times (|C| + 1)$ integer matrix s.t. $M_\gamma(i, j) = k$ iff k is the smallest integer s.t. $x_i - y_j \leq k$ (with the convention that $x_0 = 0$). (For simplicity we just mention non-strict comparisons. Strict comparisons can also be handled in a similar way.) Usually, DBMs encode convex sets of regions. These data

structures are employed in many verification tools for timed systems, and many algorithms over DBMs have been defined. Here we are interested in the following problems:

- building a DBM M_γ from the representation of γ as a set of clock constraints (see, e.g., [7]),
- deciding whether a DBM M corresponds to a region,
- deciding whether a given clock constraint g is satisfied by a region γ ,
- deciding (by using their DBM representations) whether a region γ' is reachable from γ by using only *succ*-transitions, and
- computing the DBM $M_{[C' \rightarrow 0]\gamma}$ from M_γ .

All these operations can be performed in polynomial time by using standard algorithms [33]. By way of example, we limit ourselves to presenting an algorithm for deciding if $\gamma' \in \text{succ}^*(\gamma)$ holds for two regions γ and γ' . Given M_γ , we first compute $M_{\vec{\gamma}}$, where $\vec{\gamma}$ denotes the (convex) set of regions reachable from γ by using *succ*-transitions. To this end, it is sufficient to remove the upper bounds on the values of the clocks in the constraints for γ (this is done by using a large enough integer value m_∞ for $M_{\vec{\gamma}}(i, 0)$). Finally it remains to check if each constraint of γ' implies the corresponding one in $\vec{\gamma}$. Therefore deciding whether $(n, \gamma) \xrightarrow{\text{succ}^*} (n', \gamma')$ can be done in polynomial time by verifying that:

- (1) $n' = n$,
- (2) γ' is a region, and
- (3) $\gamma' \in \text{succ}^*(\gamma)$.

Deciding whether $(n, \gamma) \xrightarrow{a} (n', \gamma')$ holds can also be done in polynomial time: we need to verify if (1) there is an edge $n \xrightarrow{g, a, r} n'$ whose guard g holds for γ , and (2) γ' is the region $[r \rightarrow 0]\gamma$.

2.6. Basic problems for complexity analysis

To determine lower bounds on the complexity of model checking problems, we shall reduce several classic problems, whose complexity is well known, to instances of model checking problems. In the remainder of this study, we shall consider reachability in Linear Bounded Turing Machines and validity of Quantified Boolean Formulae for PSPACE-hardness results, reachability in Linear Bounded Alternating Turing Machines for EXPTIME-hardness results, and validity of propositional formulae for coNP-hardness arguments. These we now proceed to sketch briefly, for the sake of completeness. The interested reader is warmly referred to [61] for an encyclopaedic treatment.

Linear Bounded and Alternating Turing Machines

A non-deterministic Turing machine is a quintuple $\mathcal{M} = \langle Q, \Sigma, q_0, q_F, T \rangle$ where

- Q is a finite set of states,
- Σ is a finite alphabet,
- $q_0 \in Q$ is the initial state,
- $q_F \in Q$ is the final state, and
- $T \subseteq Q \times \Sigma \times \Sigma \times \{L, R\} \times Q$ is the set of transitions.

A configuration of \mathcal{M} is a triple $(q, w, i) \in Q \times \Sigma^* \times \mathbb{N}$ where q denotes the current control state, w represents the contents of the tape (with the understanding that cell j contains the symbol $w(j)$, viz. the j th symbol in w , if $0 < j \leq |w|$, and the blank symbol otherwise) and i denotes the current position of the tape head. Let (q, α, δ, q') be

a transition in T . We say that such a transition is enabled in the configuration (q, w, i) iff $w(i) = \alpha$. In that case, performing it leads to the successor configuration (q', w', i') with $w'(j) = w(j)$ for every $j \neq i$, $w'(i) = \alpha'$, and $i' = i + 1$ (resp. $i' = i - 1$) if $\delta = R$ (resp. $\delta = L$ and $i > 1$). An input word w is accepted by \mathcal{M} iff there is an execution starting from $(q_0, w, 1)$ (the initial configuration of \mathcal{M} on input w) which leads to a configuration whose control state is q_F .

A *Linear Bounded Turing Machine* (LBTM) \mathcal{M} is a non-deterministic Turing Machine which can only use $|w|$ cells of the tape to decide whether an input w is accepted by \mathcal{M} or not. Deciding whether a word is accepted by a LBTM is PSPACE-complete [48].

An *Alternating Turing Machine* (ATM) \mathcal{M} is a non-deterministic Turing machine whose set of states is partitioned into two sets Q_{and} and Q_{or} . We assume without loss of generality that $q_0, q_F \in Q_{\text{or}}$. Let (q, w, i) be a configuration of \mathcal{M} . A configuration (q, w, i) with $q \in Q_{\text{or}}$ is said to be *accepting* if either q is the accepting state q_F or there exists a successor configuration (q', w', i') such that (q', w', i') is an accepting configuration. A configuration (q, w, i) with $q \in Q_{\text{and}}$ is said to be *accepting* if it has at least one successor configuration, and each of its successor configurations (q', w', i') is an accepting configuration. A word w is said to be *accepted* by \mathcal{M} iff $(q_0, w, 1)$ is an accepting configuration.

A *Linear Bounded Alternating Turing Machine* (LBATM) is an ATM whose tape head cannot go beyond the end of the input markers. Deciding whether a word is accepted by a LBATM is EXPTIME-complete [22].

Problems from logic

We shall also use the validity problem for Quantified Boolean Formulae (QBF), which is known to be PSPACE-complete [68,69]. An instance of *QBF* is a formula of the form $\Phi = Q_1 p_1 \cdots Q_n p_n \cdot \varphi$, where φ is a boolean formula (using \wedge and \vee) over the set of atomic propositions $P = \{p_1, \dots, p_n\}$ and their negations $\{\bar{p}_1, \dots, \bar{p}_n\}$. The quantifier Q_i belongs to $\{\exists, \forall\}$ for every $i \in \{1, \dots, n\}$. We shall have some use for the following lemma from [32]:

Lemma 6. *Let $\Phi = Q_1 p_1 \cdots Q_n p_n \cdot \varphi$ be an instance of QBF. Then Φ is valid iff there exists a non-empty set $\mathcal{V} \subseteq \{\text{tt}, \text{ff}\}^P$ of boolean valuations such that:*

- **correctness:** $v \models \varphi$ for every $v \in \mathcal{V}$, and
- **closure:** for all $v \in \mathcal{V}$, for all i such that $Q_i = \forall$, there is a valuation $v' \in \mathcal{V}$ such that $v'(p_i) = \neg v(p_i)$ and $v'(p_j) = v(p_j)$ for every $j < i$.

Finally we shall use the *validity* problem for propositional formulae (viz. “Does a given propositional formula hold for every interpretation of its atomic variables?”) which is a well-known coNP-complete problem.

3. Complexity results for model checking

In order to formulate and prove the complexity results to follow, we need to define what is the size of a timed automaton $A = \langle \text{Act}, N, n_0, C, E \rangle$ and of a formula $\varphi \in L_{\mu, v}^+$. The size $|\varphi|$ of a formula is its length in symbols. (Note that, since each $g \in \mathcal{B}(C)$ is a formula, this also defines the size of a guard g .) We define the size $|A|$ of a timed automaton

as $|N| + |C| + \sum_{e \in E} |g_e|$. Considering constants represented in unary or binary does not change our results except when it is explicitly mentioned.

3.1. Reachability in timed automata

Before embarking in our analysis of the complexity of model checking for $L_{\mu,v}^+$ and its sublanguages, we recall a well-known, and important, result [5,31] on the complexity of reachability in timed automata. The proof of such a statement is based upon an encoding of the workings of a Linear Bounded Turing Machine on a given input string by means of a timed automaton which will be used repeatedly in the technical developments to follow.

Lemma 7. *Reachability in timed automata is PSPACE-complete.*

Proof. We establish membership in PSPACE and hardness for PSPACE separately.

PSPACE membership. This will follow from the PSPACE membership of the reachability problem for networks of timed automata (see Theorem 31).

PSPACE-hardness. We show that the acceptance problem for LBTMs can be reduced, in polynomial time, to the reachability problem for timed automata. Let $\mathcal{M} = \langle Q, \Sigma, q_0, q_F, T \rangle$ be a non-deterministic Linear Bounded Turing Machine. We assume, without loss of generality, that $\Sigma = \{a, b\}$. Let w_0 be an input word over Σ^* . From \mathcal{M} and w_0 we are going to build, in polynomial time, a timed automaton $A_{\mathcal{M}, w_0}$ such that w_0 is accepted by \mathcal{M} iff a distinguished *end* node is reachable in $A_{\mathcal{M}, w_0}$.

Let $n = |w_0|$. The timed automaton $A_{\mathcal{M}, w_0}$ is constructed as follows. The set of nodes of $A_{\mathcal{M}, w_0}$ is $\{(q, i) \mid q \in Q \text{ and } 1 \leq i \leq n\} \cup \{\text{init}, \text{end}\}$. Intuitively, a node (q, i) denotes the current state q of \mathcal{M} and the current position i of the tape head. To encode the configurations of \mathcal{M} , it is necessary to represent the tape contents. This is done by means of appropriate clock valuations. For each tape cell C_j ($1 \leq j \leq n$), we have two clocks x_j and y_j whose values encode the content of C_j as follows: if C_j contains an a (resp. b), we have $x_j = y_j$ (resp. $x_j < y_j$). Writing an a (resp. a b) on cell j will consist in resetting x_j and y_j at the same time (resp. delaying a positive amount of time to ensure that the value of y_j is positive, and thereafter resetting x_j). Note that this encoding of the tape contents is preserved by delay transitions.

The action set of $A_{\mathcal{M}, w_0}$ is $\{s_0, s, \text{accept}\}$. For every transition $(q, \alpha, \alpha', \delta, q')$ of machine \mathcal{M} , we add, for every possible position of the tape head $i \in \{1, \dots, n\}$, transitions $(q, i) \xrightarrow{g, s, r} (q', i')$ such that

- (1) g is $x_i = y_i$ (resp. $x_i < y_i$) if $\alpha = a$ (resp. $\alpha = b$),
- (2) $r = \{x_i, y_i\}$ (resp. $r = \{x_i\}$) if $\alpha' = a$ (resp. $\alpha' = b$), and
- (3) $i' = i + 1$ (resp. $i' = i - 1$) if δ is R and $i < n$ (resp. δ is L and $i > 1$).

Finally, we need to add a constraint to ensure time elapsing because, to be sure that resetting x_j will encode the writing of a b in cell C_j , the value of the clock y_j has to be greater than 0. To this end, we use an extra clock t which is reset by every transition, and we add the constraint $t = 1$ to the guard g of the previously described transitions.

The initialization of the tape with the input word w_0 can be encoded by adding a transition $\text{init} \xrightarrow{t=1, s_0, r_{w_0}} (q_0, 1)$, where $r_{w_0} = \{x_i \mid w_0(i) = b\} \cup \{t\}$. To detect the accepting run, we add transitions $(q_F, i) \xrightarrow{\text{accept}} \text{end}$ for every i . Clearly the node *end* is reachable in $A_{\mathcal{M}, w_0}$ iff \mathcal{M} accepts w_0 .

Since $A_{\mathcal{M}, w_0}$ can obviously be built in polynomial time from \mathcal{M} and w_0 , we have thus proven that the acceptance problem for LBTMs can be reduced in polynomial time to the reachability problem in timed automata, which was to be shown. \square

Remark 8. The encoding used in the above proof employs comparisons between the values of the clocks x_j, y_j to represent the content of cell C_j . The encoding of an LBTM on input w by means of a timed automaton would be possible by using only comparisons between clocks and integer values in the guards. However, in this case, one step of the LBTM would be simulated by several transitions in the timed automaton encoding it. Nevertheless all the complexity results to follow would still hold. We refer the interested reader to Appendix D for the details of such an alternative encoding.

We now consider the complexity of model checking for the property languages introduced previously.

3.2. The logics L_v , $L_{\mu, v}$ and $L_{\mu, v}^+$

We begin by studying the complexity of model checking for the logics L_v , $L_{\mu, v}$ and $L_{\mu, v}^+$.

Theorem 9. *The model checking problem for $L_{\mu, v}^+$, $L_{\mu, v}$ and L_v is EXPTIME-complete. Moreover, we have that the specification and program complexities of $L_{\mu, v}^+$, $L_{\mu, v}$ and L_v model checking are also EXPTIME-complete.*

Proof. EXPTIME membership. This is a consequence of the EXPTIME membership of $L_{\mu, v}^+$ model checking for networks of timed automata (see Theorem 32 to follow).

EXPTIME-hardness. We offer a polynomial time reduction from the acceptance of an input w_0 by a LBATM \mathcal{M} to a model checking problem $A_{\mathcal{M}, w_0} \models \Phi$, where $A_{\mathcal{M}, w_0}$ is the timed automaton used in the proof of Lemma 7, and Φ is an L_v formula used to encode the behaviour of \mathcal{M} on input w_0 . We assume, without loss of generality, that we have a strict alternation of “or” and “and” states in \mathcal{M} ; moreover we remind the reader that we assume that the initial and final states of \mathcal{M} are “or” states.

By following the same approach proposed in [51] for untimed concurrent systems, the alternating behaviour of \mathcal{M} can be handled by an L_v formula of the form

$$\Phi = \mathbb{W}[s_0]_{\max}(X, [\text{accept}]_{\text{ff}} \wedge \mathbb{W}[s] \exists \langle s \rangle X). \quad (1)$$

Intuitively Φ is satisfied by $A_{\mathcal{M}, w_0}$ if, after the initialization step, the current “or” state is not an accepting state (as required by the subformula $[\text{accept}]_{\text{ff}}$), and after any step (leading to an “and” state), there exists a transition leading to a non-accepting “or” state and so on. We have that $A_{\mathcal{M}, w_0} \models \Phi$ iff the LBATM \mathcal{M} does not accept w_0 . Indeed Φ is the negation of a formula that uses least fixed points to express that an accepting state is reachable after some sequence of transitions, namely

$$\exists \langle s_0 \rangle_{\min}(X, \langle \text{accept} \rangle_{\text{tt}} \vee \exists \langle s \rangle \mathbb{W}[s] X).$$

Its negation Φ uses only a greatest fixed point, and can be expressed with L_v . The use of quantifiers and modalities in the formula allows us to express the alternating form of \mathcal{M} 's

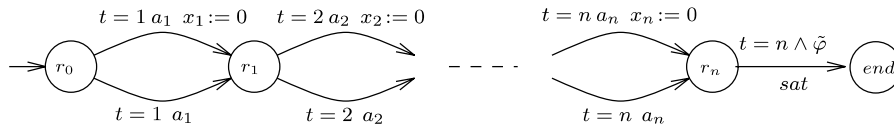


Fig. 1. The automaton $A_{(\varphi)}$ with $\tilde{\varphi} = \varphi[p_i \leftarrow x_i = n - i; \bar{p}_i \leftarrow x_i = n]_{i=1}^n$.

acceptance conditions. This yields the EXPTIME-hardness of the model checking problem for L_v , $L_{\mu,v}$ and $L_{\mu,v}^+$.

Program complexity. The claim follows from the proof of EXPTIME-hardness for L_v model checking, where the formula Φ we have constructed is fixed, and does not depend on the LBATM \mathcal{M} .

Specification complexity. The claim is a consequence of Lemma 13 to follow, which states that the model checking problem for the *nil* process is EXPTIME-complete. \square

Remark 10. The proof of Theorem 9 can be adapted³ to the propositional timed μ -calculus T_μ proposed in [42], thus yielding an improved lower bound on the complexity of T_μ model checking. Moreover, the proof used to show EXPTIME membership of $L_{\mu,v}^+$ model checking over networks of timed automata (see Theorem 32 to follow) can be used *mutatis mutandis* for T_μ . Therefore model checking for T_μ over (networks of) timed automata is EXPTIME-complete. To the best of our knowledge, this is the first precise characterization of the structural complexity of model checking for this logic.

We now proceed to study the computational complexity of model checking for the logic L_v^- —that is, the fixed point free fragment of L_v (and thus of $L_{\mu,v}$ and $L_{\mu,v}^+$). We have:

Theorem 11.

- (1) *The model checking problem for L_v^- is PSPACE-complete.*
- (2) *The specification complexity of L_v^- model checking is PSPACE-complete.*
- (3) *The program complexity of L_v^- model checking is in P, if the integer constants in the timed automata are represented in unary.*

Proof. We prove each statement in turn.

- (1) We separately establish membership in, and hardness for, PSPACE.

PSPACE membership. This will follow from the more general argument in Theorem 33.

PSPACE-hardness. Let $\Phi = Q_1 p_1 \cdots Q_n p_n \cdot \varphi$ be an instance of the QBF problem. We offer a polynomial time reduction from the validity of Φ to a model checking problem for L_v^- . Consider the timed automaton $A_{(\varphi)}$ and the formula $\tilde{\varphi}$ in Fig. 1, and the following L_v^- formula:

$$\tilde{\Phi} = \exists (\langle a_1 \rangle \text{tt} \wedge O_1 (\exists (\langle a_2 \rangle \text{tt} \wedge O_2 \cdots \exists (\langle a_n \rangle \text{tt} \wedge O_n \langle \text{sat} \rangle \text{tt}))))),$$

where O_i is $\langle a_i \rangle$ (resp. $[a_i]$) if Q_i is \exists (resp. \forall).

³ For example by considering the formula $\mu X. \text{accept} \vee [\text{tt} \triangleright (p_o \wedge \neg(\text{tt} \triangleright (p_e \wedge \neg X)))]$, where p_e (resp. p_o) marks even (resp. odd) states.

Consider a path

$$\begin{aligned} \rho &= (r_0, u_0^\rho) \xrightarrow{\epsilon(1)} (r_0, u_0^\rho + 1) \xrightarrow{a_1} (r_1, u_1^\rho) \cdots \\ &\quad (r_{n-1}, u_{n-1}^\rho) \xrightarrow{\epsilon(1)} (r_{n-1}, u_{n-1}^\rho + 1) \xrightarrow{a_n} (r_n, u_n^\rho) \end{aligned} \quad (2)$$

in the TLTS $\mathcal{T}_{A(\varphi)}$, where u_0^ρ is the valuation setting all clocks to 0. Along such a path,

- (1) each action a_i is performed at time i ,
- (2) each clock x_i is either reset at time i or is never reset, and
- (3) the clock t is never reset.

Such a path describes a valuation v_ρ for the atomic propositions p_1, \dots, p_n according to the following convention:

- if x_i has been reset at time i (i.e., if $u_n^\rho(x_i) = n - i$), then $v_\rho(p_i) = \text{tt}$,
- otherwise (i.e., if $u_n^\rho(x_i) = n$) $v_\rho(p_i) = \text{ff}$.

This entails that at time n (i.e., when the value of t is n), the *sat*-labelled transition is enabled from the configuration (r_n, u_n^ρ) (i.e., $u_n^\rho \models \tilde{\varphi}$) iff $v_\rho \models \varphi$. We claim that:

$$\Phi \text{ is valid} \Leftrightarrow A(\varphi) \models \tilde{\Phi}. \quad (3)$$

To prove that this does hold, define

$$\tilde{\Phi}_i \stackrel{\text{def}}{=} \exists (a_{i+1}) \text{tt} \wedge O_{i+1} \widetilde{\Phi_{i+1}} \quad (i = 0, \dots, n-1)$$

and let $\tilde{\Phi}_n$ be $\langle \text{sat} \rangle \text{tt}$. Note that $\tilde{\Phi} = \tilde{\Phi}_0$. We establish the two implications of statement (3) separately.

- (\Rightarrow) Assume that Φ is valid. This implies that there exists a set of boolean valuations \mathcal{V} satisfying the requirements in Lemma 6. To establish that $A(\varphi) \models \tilde{\Phi}$ holds, it is sufficient to use the following auxiliary lemma, whose proof is an easy induction over $n - i$.

Lemma 12. *Let Φ be valid and let v be a boolean valuation in a set of boolean valuations \mathcal{V} that satisfies the requirements of Lemma 6. Let u_i^v (with $i \leq n$) be a timed valuation for the set of clocks $\{x_1, \dots, x_n, t\}$ such that:*

- $u_i^v(t) = i$,
- $u_i^v(x_j) = i$ if $j > i$, and
- $u_i^v(x_j) = i$ (resp. $u_i^v(x_j) = i - j$) if $j \leq i$ and $v(p_j) = \text{ff}$ (resp. $v(p_j) = \text{tt}$).

Then we have that $(r_i, u_i^v) \models \tilde{\Phi}_i$.

- (\Leftarrow) Assume $A(\varphi) \models \tilde{\Phi}$. We shall show that Φ is valid. To this end, it is sufficient to exhibit a collection \mathcal{V} of boolean valuations satisfying the conditions of Lemma 6. Let ρ be a path in $\mathcal{T}_{A(\varphi)}$ of the form (2). We say that ρ is $\tilde{\Phi}$ -consistent if $(r_i, u_i^\rho) \models \tilde{\Phi}_i$, for every $i \in \{0, \dots, n\}$. In particular, if ρ is $\tilde{\Phi}$ -consistent, then $u_n^\rho \models \tilde{\varphi}$, and thus $v_\rho \models \varphi$. Note, furthermore, that, if ρ is $\tilde{\Phi}$ -consistent and $Q_{i+1} = \forall$ for some $0 \leq i < n$, then the path ρ' defined thus

$$\begin{aligned} &(r_0, u_0^\rho) \xrightarrow{\epsilon(1)} (r_0, u_0^\rho + 1) \xrightarrow{a_1} (r_1, u_1^\rho) \cdots (r_i, u_i^\rho) \xrightarrow{\epsilon(1)} (r_i, u_i^\rho + 1) \xrightarrow{a_{i+1}} \\ &\quad (r_{i+1}, u_{i+1}^{\rho'}) \cdots (r_{n-1}, u_{n-1}^{\rho'}) \xrightarrow{\epsilon(1)} (r_{n-1}, u_{n-1}^{\rho'} + 1) \xrightarrow{a_n} (r_n, u_n^{\rho'}), \end{aligned}$$

is also $\tilde{\Phi}$ -consistent, where, for every $j > i$ and clock y , the time valuation $u_j^{\rho'}$ is such that

- $u_j^{\rho'}(y) = j - (i + 1)$, if $y = x_{i+1}$ and $u_{i+1}^\rho(x_{i+1}) = i + 1$ (i.e., if clock x_{i+1} was not reset by the $(i + 1)$ th action transition in ρ),

- $u_j^{\rho'}(y) = j$, if $y = x_{i+1}$ and $u_{i+1}^\rho(x_{i+1}) = 0$ (i.e., if clock x_{i+1} was reset by the $(i + 1)$ th action transition in ρ), and
- $u_j^{\rho'}(y) = u_j^\rho(y)$, otherwise.

Since ρ' is $\tilde{\Phi}$ -consistent, it is easy to see that $v_{\rho'} \models \varphi$. It follows that the collection of boolean valuations associated with the set of $\tilde{\Phi}$ -consistent paths of $\mathcal{T}_{A(\varphi)}$ is non-empty (since $A(\varphi) \models \tilde{\Phi}$ and $\tilde{\Phi}$ requires the existence of at least one path of the form (2)), and satisfies the correctness and closure requirements from Lemma 6. We may therefore conclude that Φ is valid.

This completes the proof of (3).

(2) **Specification complexity.** Any QBF instance can be encoded as a model checking problem of the form $nil \models \Phi$, with $\Phi \in L_v^-$, by using formula clocks (see Lemma 13 to follow). This entails the PSPACE-hardness of the specification complexity of model checking for L_v^- .

(3) **Program complexity.** Let φ be a given L_v^- formula. We can define an algorithm, whose worst-case complexity is polynomial in $|A|$, by building the pertinent part of the region graph in an “on the fly” manner. The key points are that

- to decide if φ holds for a timed automaton A we only need to consider sequences with at most $|\varphi|$ action transitions, and
- between two action transitions the number of possible delay transitions in the region graph is bounded from above by

$$2(|C_A| + |K|)(\max(\text{MCst}(A), \text{MCst}(\varphi)) + 1),$$

which is polynomial in $|A|$ if $\text{MCst}(A)$ is given in unary.

The algorithm to decide if φ holds for a symbolic state (r, γ) proceeds by structural induction over the formula φ :

- To decide if $(r, \gamma) \models \varphi$ holds, with φ of the form $\psi_1 \wedge \psi_2$ or $\psi_1 \vee \psi_2$, we need (in the worst case) to decide whether $(r, \gamma) \models \psi_i$ holds with $i = 1, 2$.
- To decide if $(r, \gamma) \models \varphi$ holds, with φ of the form $\langle a \rangle \psi$ or $[a] \psi$, we need (in the worst case) to decide whether $(r_i, \gamma_i) \models \psi$ holds for every (r_i, γ_i) such that $(r, \gamma) \xrightarrow{a} (r_i, \gamma_i)$. The number of such (r_i, γ_i) is bounded from above by $|A|$.
- To decide if $(r, \gamma) \models \varphi$ holds, with φ of the form $\exists \psi$ or $\forall \psi$, we need (in the worst case) to decide whether $(r, \text{succ}^i(\gamma)) \models \psi$ holds for every i no larger than $2(|C| + |K_\varphi|)(\max(\text{MCst}(A), \text{MCst}(\varphi)) + 1)$. Since we assume that $\text{MCst}(A)$ is represented in unary, it follows that the number of sub-problems of the form $(r, \text{succ}^i(\gamma)) \models \psi$ to be considered is in $O(|A|^2)$.
- To decide if $(r, \gamma) \models \varphi$ holds, with φ of the form $K' \dot{\leq} \psi$, we need to decide whether $(r, [K' \rightarrow 0]\gamma) \models \psi$.
- To decide if $(r, \gamma) \models g$, we need to verify if the constraint g holds for the region γ . This can be done in constant time in the size of A because g only contains clocks in K , that is disjoint from the set of clocks C in the automaton A .

The depth of the recursive calls in this algorithm is at most $|\varphi|$, and each recursive call generates at most $O(|A|^2)$ subcalls. The computation of each recursive call takes time polynomial in $|\varphi|$, and thus constant in $|A|$. It follows that the time complexity of the aforementioned algorithm is in $O(|A|^{2|\varphi|})$ and, as φ is fixed, the program complexity is in P. \square

Note that the aforementioned proofs of lower bounds for the specification complexity of model checking for L_v and its fixed point free fragment are based upon the realization that the model checking problems of the form $nil \models \varphi$, where φ is a formula in any of the logics considered so far and nil is the (untimed) automaton with no transition, are just as hard as the model checking problems for arbitrary timed automata. We shall refer to this restricted model checking problem as “ nil model checking”. Indeed we have:

Lemma 13.

- The nil model checking problem for L_v , $L_{\mu,v}$ and $L_{\mu,v}^+$ is EXPTIME-complete.
- The nil model checking problem for L_v^- is PSPACE-complete.

Proof. That the nil model checking problem for $L_{\mu,v}^+$ (and, a fortiori, for L_v and $L_{\mu,v}$) and L_v^- are in EXPTIME and PSPACE, respectively, follows from Theorems 9 and 11. The proofs of the relevant lower bounds may be found in Appendix B. \square

In light of the above results, the worst-case complexity of model checking for the real-time logics we have considered in this section may be seen as deriving solely from the use of clocks in formulae. This pattern will remain true for all the property languages we study in what follows, except $SBLL^-$ and $L_{\forall S}^-$.

Remark 14. All the property languages we study in this paper, apart from $L_s + \langle a \rangle$, $L_s^- + \langle a \rangle$, $SBLL^-$ and $L_{\forall S}^-$, enjoy the following property:

Given a timed automaton A and a formula φ , it is possible to build a formula φ/A s.t.
 $A \models \varphi$ iff $nil \models \varphi/A$.

Thus the behaviour of a timed automaton A can be encoded, in some sense, in the formula φ/A . More generally, these logics allow for *compositional model checking* [57,1,52,53]. Nevertheless this expressiveness result for these property languages cannot be used to deduce the computational hardness of the nil model checking problem from the complexity of the general model checking problem $A \models \varphi$, because the size of φ/A can be exponential in $|\varphi|$. For example, in the proof of the previous lemma, the PSPACE-hardness of the nil model checking problem for L_v^- cannot be established by using the formula $\tilde{\Phi}/A_{(\varphi)}$, where $A_{(\varphi)} \models \tilde{\Phi}$ is the L_v^- model checking instance used in the proof of Theorem 11, because $|\tilde{\Phi}/A_{(\varphi)}|$ is in $O(2^{|\tilde{\Phi}|})$.

Remark 15. Note that constructs of the form $K' \text{ in } \varphi$, where K' is a set of formula clocks, were only employed in the lower bound proofs for the specification complexity of model checking of the logics considered in this section.

3.3. The property language L_s

The property language L_s (see Section 2.3) has been introduced in [57] as a sub-language of L_v that allows for more efficient model checking algorithms. To the best of our knowledge, however, such an intention has not been supported yet by precise complexity theoretic considerations. These we now proceed to present.

Theorem 16. *The complexity of L_s model checking is PSPACE-complete. Moreover, the specification and program complexities of L_s model checking are also PSPACE-complete.*

Proof. PSPACE membership. A model checking problem of the form $A \models \varphi$, with $\varphi \in L_s$, can be reduced in polynomial time to a reachability problem for a parallel composition $(A|T_\varphi)$. See the more general Theorem 34 to follow.

PSPACE-hardness. We offer a linear time reduction from the reachability problem for timed automata, which is PSPACE-complete by Lemma 7, to L_s model checking.

Let A be a timed automaton and n one of its nodes. Build the automaton \tilde{A} by relabelling all edges of A with a , and adding a new edge $\langle n, \text{tt}, \text{in}_n, \emptyset, n \rangle$. Then n is reachable in A iff $\tilde{A} \not\models \max(X, [\text{in}_n]\text{ff} \wedge [a]X \wedge \forall X)$.

Specification complexity. It is possible to reduce, in polynomial time, any instance $\langle \mathcal{M}, w_0 \rangle$ of the acceptance problem for linear bounded non-deterministic Turing machines to a model checking problem of the form $\text{nil} \models \Phi_{\mathcal{M}, w_0}$ (with $\Phi_{\mathcal{M}, w_0} \in L_s$) by means of the same kind of encoding used for L_v (see Lemma 18 to follow).

Program complexity. It is PSPACE-complete because the formula expressing the reachability problem in the PSPACE-hardness argument above does not depend on the input automaton. \square

In light of the above result, and assuming that PSPACE is different from EXPTIME, the model checking problem for L_s has a lower computational complexity than that for L_v . The source of the lower complexity is the observation that the model checking problem for L_s , unlike that for L_v , can be reduced in polynomial time to reachability checking in timed automata.

Theorem 17. *The model checking problem for L_s^- is coNP-complete, as is the specification complexity of model checking. The program complexity of L_s^- is in P, if the constants in the input automata are represented in unary.*

Proof. coNP membership. See the more general Theorem 35 to follow.

coNP-hardness. The *validity problem* for propositional formulae can be reduced, in polynomial time, to L_s^- model checking. Let ψ be a propositional formula over the set of atomic propositions $\{p_1, \dots, p_n\}$. Consider the timed automaton $A_{(\neg\psi)}$ built from the negation of ψ as in Fig. 1. We claim that

$$\psi \text{ is valid iff } A_{(\neg\psi)} \models \forall[a_1]\forall[a_2] \dots \forall[a_n][\text{sat}]\text{ff}.$$

To see that this does hold, consider an arbitrary path ρ in the TLTS $\mathcal{T}_{A_{(\neg\psi)}}$ of the form (2) on page 21. We argue that, if ψ is valid, its last configuration (r_n, u_n^ρ) satisfies the formula $[\text{sat}]\text{ff}$. To this end, note that the configuration (r_n, u_n^ρ) is always reached at time n , and there are 2^n possible valuations u_n^ρ . They are of the form: $u_n^\rho(t) = n$ and $u_n^\rho(x_i) \in \{n - i, n\}$, for every $i \in \{1, \dots, n\}$, depending on which clocks have been reset along the path. Since ψ is valid iff $\neg\psi$ is false for every interpretation of the p_i 's, we deduce that ψ is valid iff $\neg\psi$ (viz. the constraint on the values of the clocks x_i guarding the edge from node r_n to node end in $A_{(\neg\psi)}$) does not hold for any reachable (r_n, u_n^ρ) . Thus $(r_n, u_n^\rho) \not\rightarrow^{\text{sat}}$, which was to be shown.

In similar fashion, it is easy to argue that, if ψ is not valid, then there is a path in $\mathcal{T}_{A(\neg\psi)}$ of the form (2) whose last configuration (r_n, u_n^ρ) can perform a *sat*-labelled transition. Hence,

$$A(\neg\psi) \not\models \mathbb{W}[a_1]\mathbb{W}[a_2] \cdots \mathbb{W}[a_n][sat] \text{ff}$$

if ψ is not valid.

Specification complexity. Every instance of the validity problem for propositional formulae can be reduced, in polynomial time, to a model checking problem of the form $nil \models \Phi$ with $\Phi \in L_s^-$ (see Lemma 18 to follow).

Program complexity. This follows immediately from the fact that the program complexity for L_v^- is in P (Theorem 11(3)). \square

Lemma 18.

- The *nil* model checking problem for L_s is PSPACE-complete.
- The *nil* model checking problem for L_s^- is coNP-complete.

Proof. That the *nil* model checking problems for L_s and L_s^- are in PSPACE and coNP, respectively, follows from Theorems 16 and 17. The proofs of the relevant lower bounds may be found in Appendix B. \square

3.4. Mapping the territory between L_s and L_v

In Sections 3.2 and 3.3, we have shown, amongst other things, that the model checking problems for the property languages L_v and L_s are EXPTIME-complete and PSPACE-complete, respectively. Since L_s is a restricted fragment of L_v which has been proposed in [57] in order to improve the efficiency of model checking in practice, it is natural to wonder whether all the, seemingly ad hoc, syntactic restrictions imposed on L_s formulae are necessary in order to obtain a sublanguage of L_v whose model checking problem is in PSPACE. To answer this natural question, we shall now consider several possible extensions of L_s . These extensions of L_s are obtained by adding one of the following constructs to that language:

- diamond modalities, viz. allowing for formulae like $\langle a \rangle \phi$,
- unrestricted disjunction, viz. allowing for formulae like $\psi \vee \phi$, and
- existential quantification over delay transitions, viz. allowing for formulae like $\exists \phi$.

We consider these possible extensions in turn, and show that each of them leads to an EXP-TIME-hard model checking problem. This implies that none of the restrictions imposed by the syntax for L_s can be dropped without increasing the structural complexity of the model checking problem.

3.4.1. Adding diamond modalities to L_s

Let $L_s + \langle a \rangle$ denote the property language obtained by adding the construct $\langle a \rangle \phi$ to the defining clauses for L_s . We shall now argue that:

Proposition 19. *The complexity of the model checking problem for $L_s + \langle a \rangle$ is EXPTIME-complete, and so are its program complexity and its specification complexity.*

Proof. We consider the combined, program, and specification complexities of model checking in turn.

Complexity of model checking. Since $L_s + \langle a \rangle$ is a sublanguage of L_v , it is sufficient to argue that the complexity of the model checking problem for $L_s + \langle a \rangle$ is EXPTIME-hard. To this end, we offer a polynomial time reduction from the acceptance of an input w_0 by a LBATM \mathcal{M} to a model checking problem $A_{\mathcal{M}, w_0} \models \Phi'$ where $A_{\mathcal{M}, w_0}$ is the timed automaton used in the proof of Lemma 7, and Φ' is an $L_s + \langle a \rangle$ formula used to encode the behaviour of \mathcal{M} on input w_0 . As in the proof of Theorem 9, we assume, without loss of generality, that we have a strict alternation of “or” and “and” states in \mathcal{M} ; moreover we remind the reader that we assume that the initial and final states of \mathcal{M} are “or” states.

The formula Φ (cf. Eq. (1) on page 19) used to handle the alternating behaviour of the LBATM \mathcal{M} in the proof of Theorem 9 can be replaced by the following one, which contains neither existential quantifications over delay transitions nor general disjunction:

$$\Phi' = \mathbb{W}[s_0] \max \left(X, [\text{accept}] \text{ff} \wedge \mathbb{W}[s] (z \text{ in } \mathbb{W}(z = 1 \Rightarrow \langle s \rangle X)) \right).$$

Note that the existential quantification over delay transitions used in (1) (cf. page 19) may be replaced by the subformula

$$z \text{ in } \mathbb{W}(z = 1 \Rightarrow \dots)$$

because we know that exactly one time unit separates any two consecutive s -labelled transitions in the simulation of the workings of an LBATM on input w_0 by the timed automaton offered in the proof of Lemma 7. Again \mathcal{M} accepts w_0 iff $A_{\mathcal{M}, w_0} \models \Phi'$. This yields the EXPTIME-hardness of $L_s + \langle a \rangle$ model checking.

Program complexity. Since the formula Φ' we have constructed is fixed, and does not depend on the LBATM \mathcal{M} and on w_0 , the EXPTIME-hardness of the program complexity of the model checking problem also follows.

Specification complexity. See Appendix C. \square

Remark 20. Unlike the general model checking problem, the *nil* model checking problem for $L_s + \langle a \rangle$ is still PSPACE-complete. This follows because the satisfaction of formulae involving the action modalities can be trivially checked for the *nil* process. Indeed, a model checking problem of the form $nil \models \phi$, with $\phi \in L_s + \langle a \rangle$, can be reduced in time linear in the size of ϕ to a model checking problem of the form $nil \models \tilde{\phi}$, where $\tilde{\phi}$ is the L_s -formula obtained from ϕ by replacing any subformula of the form $\langle a \rangle \varphi$ by ff .

Remark 21. The extension of L_s with restricted diamond formulae of the form $\langle a \rangle \text{tt}$ does not increase the complexity of the model checking problem, which remains PSPACE-complete as for L_s . Indeed, given a model checking problem $A \models \varphi$, where φ is a formula in L_s that can additionally use subformulae of the form $\langle a \rangle \text{tt}$ ($a \in \text{Act}$), it is possible to build in polynomial time a timed automaton \tilde{A} and a formula $\tilde{\varphi} \in L_s$ such that $A \models \varphi$ iff $\tilde{A} \models \tilde{\varphi}$. The timed automaton \tilde{A} is built by adding to A transitions of the form $(n, g_n, \text{no_a}, \emptyset, n)$ for every node n , where no_a is a fresh action symbol and

$$g_n = \neg \left(\bigvee_{(n, g', a, r, n') \in A} g' \right).$$

It is easy to see that $(n, u) \models \langle a \rangle \text{tt}$ (in A) iff $(n, u) \models [\text{no_a}] \text{ff}$ (in \tilde{A}), for every node n and clock valuation u . The formula $\tilde{\varphi}$ is built from φ by replacing every occurrence of $\langle a \rangle \text{tt}$ ($a \in \text{Act}$) with $[\text{no_a}] \text{ff}$.

For the fragment of $L_s + \langle a \rangle$ without greatest fixed points, we have the following complexity results:

Proposition 22.

- (1) *The model checking problem for $L_s^- + \langle a \rangle$ is PSPACE-complete.*
- (2) *The specification complexity of $L_s^- + \langle a \rangle$ model checking is PSPACE-complete.*
- (3) *The nil model checking problem for $L_s^- + \langle a \rangle$ is coNP-complete.*
- (4) *The program complexity of $L_s^- + \langle a \rangle$ model checking is in P, if the constants in the input automata are represented in unary.*

Proof. We prove the four statements in turn.

- (1) Since $L_s^- + \langle a \rangle$ is a sublanguage of L_v^- , the model checking problem for $L_s^- + \langle a \rangle$ is in PSPACE by Theorem 11(1). To establish hardness for PSPACE, we argue that any instance of QBF can be reduced in polynomial time to a model checking problem for $L_s^- + \langle a \rangle$.

Let $\Phi = Q_1 p_1 \cdots Q_n p_n \cdot \varphi$ be an instance of the QBF problem. Consider the timed automaton $A_{(\varphi)}$ in Fig. 1. In the proof of Theorem 11(1), we argued that Φ is valid iff the timed automaton $A_{(\varphi)}$ satisfies the L_v^- formula

$$\exists(\langle a_1 \rangle \text{tt} \wedge O_1(\exists(\langle a_2 \rangle \text{tt} \wedge O_2 \cdots \exists(\langle a_n \rangle \text{tt} \wedge O_n \langle \text{sat} \rangle \text{tt}))),$$

where O_i is $\langle a_i \rangle$ (resp. $[a_i]$) if Q_i is \exists (resp. \forall). This formula is not in $L_s^- + \langle a \rangle$ because it uses the existential delay operator \exists . Note, however, that consecutive action transitions in $A_{(\varphi)}$ are separated by exactly one time unit. In lieu of the above formula, we can thus consider the following one, where t' is a fresh clock:

$$\tilde{\Phi} \stackrel{\text{def}}{=} t' \text{ in } \mathbb{W} \left(t' = 1 \Rightarrow O_1 \mathbb{W}(t' = 2 \Rightarrow \dots O_{n-1} \mathbb{W}(t' = n \Rightarrow O_n \langle \text{sat} \rangle \text{tt})) \right),$$

where O_i is $\langle a_i \rangle$ (resp. $[a_i]$) if Q_i is \exists (resp. \forall). We have that Φ is valid iff $A_{(\varphi)} \models \tilde{\Phi}$.

- (2) See Appendix C for the proof of PSPACE-hardness of the specification complexity.
- (3) This follows from Lemma 18 and Remark 20.
- (4) This statement is an immediate corollary of Theorem 11 about L_v^- . \square

3.4.2. Adding disjunction to L_s

Let $L_s + \vee$ denote the property language obtained by adding the construct $\phi \vee \psi$ to the defining clauses for L_s . We shall now argue that:

Proposition 23. *The complexity of the (nil) model checking problem for $L_s + \vee$ is EXPTIME-complete, and so are its specification complexity and its program complexity.*

Proof. See Appendix C. \square

For the fragment of $L_s + \vee$ without greatest fixed points, we have the following complexity results:

Proposition 24. *The model checking problem for $L_s^- + \vee$ is coNP-complete, and so are its specification complexity and the nil model checking problem. The program complexity is in P, if the constants in the input automata are represented in unary.*

Proof. The algorithm used in the proof of coNP membership for L_s^- model checking for concurrent programs (Theorem 35 to follow) can be easily adapted to $L_s^- + \vee$. Indeed such a non-deterministic polynomial algorithm can be used to decide if the negation of an L_s^- formula holds for a symbolic state (\bar{r}, γ) . To extend it to cover the whole of $L_s^- + \vee$, it is sufficient to add a clause for $(\bar{r}, \gamma) \models \psi_1 \wedge \psi_2$. In that case, the procedure simply checks whether $(\bar{r}, \gamma) \models \psi_1$ and $(\bar{r}, \gamma) \models \psi_2$ both hold. The resulting non-deterministic algorithm remains polynomial.

The coNP-hardness of (nil) model checking for $L_s^- + \vee$ is an immediate consequence of the coNP-hardness of nil model checking for L_s^- (Lemma 18). Since model checking for the nil process is coNP-complete, so is the specification complexity of model checking. The characterization of the program complexity comes from Theorem 11 about L_v^- . \square

3.4.3. Adding existential quantification over delay transitions to L_s

Let $L_s + \exists$ denote the property language obtained by adding the construct $\exists\phi$ to the defining clauses for L_s . We shall now argue that:

Proposition 25. *The complexity of the (nil) model checking problem for $L_s + \exists$ is EXPTIME-complete, and so are its specification complexity and its program complexity.*

The proof is in Appendix C. For the fragment of $L_s + \exists$ without greatest fixed points, we have the following complexity results:

Proposition 26. *The model checking problem for $L_s^- + \exists$ is PSPACE-complete, and so are the nil model checking problem and the specification complexity. The program complexity is in P, if the constants in the input automata are represented in unary.*

Proof. The formula used in Appendix B to show that the nil-model checking problem for L_v^- is PSPACE-hard belongs to $L_s^- + \exists$. The program complexity comes from Theorem 11 about L_v^- . \square

Our main results mapping the territory between L_v and L_s are summarized, for ease of reference, in Tables 4 and 5. It is interesting to note that adding $\langle a \rangle$ or \exists increases the

Table 4
Complexity results for languages with fixed points

	L_s	$L_s + \langle a \rangle$	$L_s + \exists$	$L_s + \vee$	L_v
MC					
Spec. com.	PSPACE-C	EXPTIME-C	EXPTIME-C	EXPTIME-C	EXPTIME-C
Prog. com.	Theorem 16	Proposition 19	Proposition 25	Proposition 23	Theorem 9
nil – MC	PSPACE-C Lemma 18	PSPACE-C Remark 20	EXPTIME-C Proposition 25	EXPTIME-C Proposition 23	EXPTIME-C Lemma 13

Table 5
Complexity results for languages with fixed points

	L_s^-	$L_s^- + \langle a \rangle$	$L_s^- + \boxplus$	$L_s^- + \vee$	L_v^-
MC	coNP-C	PSPACE-C	PSPACE-C	coNP-C	PSPACE-C
Spec. compl.	coNP-C	PSPACE-C	PSPACE-C	coNP-C	PSPACE-C
Prog. compl.	P	P	P	P	P
	Theorem 17	Proposition 22	Proposition 26	Proposition 24	Theorem 11
$nil - MC$	coNP-C	coNP-C	PSPACE-C	coNP-C	PSPACE-C
	Lemma 18	Lemma 18	Proposition 26	Proposition 24	Lemma 13

complexity of model checking for L_s^- , whereas adding \vee does not. Thus the addition of general disjunction to L_s only increases the complexity of model checking in the presence of fixed points.

3.5. The property languages $SBLL$ and L_{VS}

The model checking problems for property languages $SBLL$ and L_{VS} have the same complexity, as stated in the following theorem, whose proof is similar to that of Theorem 16 and is therefore omitted:

Theorem 27. *The complexity of $SBLL$ and L_{VS} model checking is PSPACE-complete. Moreover we have that the specification and program complexities of $SBLL$ and L_{VS} model checking are also PSPACE-complete.*

For the property languages $SBLL^-$ and L_{VS}^- , we obtain the following result:

Theorem 28. *The model checking problem for $SBLL^-$ and L_{VS}^- is PSPACE-complete. The specification complexity of model checking for $SBLL^-$ and L_{VS}^- is coNP-complete. Finally, the program complexity of model checking for the property languages $SBLL^-$ and L_{VS}^- is PSPACE-complete.*

Proof. PSPACE membership. This follows from the more general statement in Theorem 36.

PSPACE-hardness. The reachability problem can be reduced in linear time to a model checking problem for $SBLL^-$ (and then for L_{VS}^-). Given a timed automaton A , let r be the node we want to reach in A from the initial configuration. We build a timed automaton \tilde{A} from A by replacing every transition label by τ , and by adding an action transition $r \xrightarrow{\text{in}_r} r$. The control node r is reachable iff $\tilde{A} \not\models \mathbb{W}[\text{in}_r]\text{ff}$.

Specification complexity. Fix a timed automaton A . We can define a non-deterministic algorithm that, given a formula φ , decides in time polynomial in $|\varphi|$ whether $A \not\models \varphi$. Note that we can compute in constant time (w.r.t. to $|\varphi|$) the τ -derivatives and a -derivatives of any symbolic state (r, γ) of A , because this computation does not depend on the clocks in the formula φ . The algorithm uses the same ideas employed in the proof of Theorem 35 to follow. The hardness result is a consequence of Lemma 29 to follow, showing that the

model checking for both $SBLL^-$ and $L_{\forall S}^-$ is coNP-hard if we fix the timed automaton to be the *nil* process.

Program complexity. As previously argued, checking whether a timed automaton satisfies the fixed formula $\mathbb{W}[\text{in_r}]\text{ff}$ is PSPACE-hard. \square

The attentive reader might have noticed that the complexity of model checking for the fixed point-free versions of the languages $SBLL$ and $L_{\forall S}$ coincides with that for the full languages. This is because there is an implicit recursion (over τ and delay transitions) which is hidden in the semantics of the $SBLL^-$ operator \mathbb{W} . This recursion is sufficient to make the model checking problems for $SBLL^-$ and $L_{\forall S}^-$ PSPACE-hard, and is exploited in the aforementioned proof of the hardness result.

The *nil* model checking problems for $SBLL$ and $L_{\forall S}$ (resp. $SBLL^-$ and $L_{\forall S}^-$) are equivalent to the *nil* model checking problem for L_s (resp. L_s^-) because no τ -transition occurs in the *nil* process. For τ -free timed automata, the weak interpretation is equivalent to the strong interpretation. Moreover subformulae of the form $\langle a \rangle \text{tt}$ can be replaced by ff because no action transition occurs in the *nil* process, and \mathbb{W}_S is equivalent to \mathbb{W} . It thus follows from Lemma 18 that:

Lemma 29.

- The *nil* model checking problem for $SBLL$ and $L_{\forall S}$ is PSPACE-complete.
- The *nil* model checking problem for $SBLL^-$ and $L_{\forall S}^-$ is coNP-complete.

4. The complexity of model checking for concurrent timed programs

In practice real-time systems contain several components and are often modelled as *parallel compositions of timed automata*. We denote by *model checking for concurrent programs* the problem of deciding if a property holds for a concurrent timed system (viz. a parallel composition of timed automata). Since, already in the untimed setting, model checking suffers from the so-called state explosion problem in the presence of concurrency, a characterization of the complexity of the model checking problem for concurrent programs would yield a more realistic assessment of the hardness of the task of model checking real-time systems. The aim of this section is to present such an analysis.

Real-time concurrent programs. Let $A_1 \dots, A_n$ be timed automata, with

$$A_i = \langle \text{Act}, N_i, q_{i,0}, C_i, E_i \rangle.$$

(We assume that the sets of clocks C_1, \dots, C_n are pairwise disjoint.) We model a real-time concurrent program by means of the parallel composition of A_1, \dots, A_n . Following [46], we use a parallel composition operator parameterized with a synchronization function. (Such an operator generalizes a large range of existing notions of parallel composition.) A *synchronization function* f is a partial function with signature $(\text{Act} \cup \{\bullet\})^n \hookrightarrow \text{Act}$, where \bullet denotes a distinguished no-action symbol. In fact, f is an n -ary synchronization function with renaming. We denote by $(A_1 | \dots | A_n)_f$ the parallel composition of A_1, \dots, A_n with respect to the synchronization function f . A network configuration is a pair (\bar{q}, v) where $\bar{q} = (q_1, \dots, q_n)$ is a vector of nodes in $N_1 \times \dots \times N_n$ and v is a valuation for $C = \bigcup_i C_i$, i.e., the set of clocks of the network. Given a network configuration (\bar{q}, v) , we write q_i for the i th node in the vector \bar{q} , and v_i for the restriction of v to the set of clocks C_i .

The semantics of $(A_1 | \dots | A_n)_f$ can be defined in terms of a timed labelled transition system, whose states are the configurations of the network and whose transitions are given by the two following rules:

- $(\bar{q}, v) \xrightarrow{b} (\bar{q}', v')$ iff there is a $(a_1, \dots, a_n) \in (\text{Act} \cup \{\bullet\})^n$ such that:
 - for all $i \in \{1, \dots, n\}$ with $a_i = \bullet$, we have that $q'_i = q_i$ and $v'_i = v_i$,
 - for all $i \in \{1, \dots, n\}$ with $a_i \in \text{Act}$, we have that $(q_i, v_i) \xrightarrow{a_i} (q'_i, v'_i)$, and
 - $f(a_1, \dots, a_n) = b$;
- $(\bar{q}, v) \xrightarrow{\epsilon(d)} (\bar{q}', v')$ iff $\bar{q}' = \bar{q}$ and $v' = v + d$.

Note that the second rule stipulates that all clocks increase synchronously.

When the synchronization function f is understood from the context, we simply write \bar{A} in lieu of $(A_1 | \dots | A_n)_f$. The size of \bar{A} is the sum of the sizes of the timed automata A_1, \dots, A_n and the size of the function f (viz. the number of synchronization vectors). Symbolically, $|\bar{A}| = |A_1| + \dots + |A_n| + |f|$. Clearly \bar{A} can be stored in space $O(|\bar{A}|)$.

The possibility of forming parallel compositions of automata does not add expressive power to the model of timed automata. Indeed, from $(A_1 | \dots | A_n)_f$, it is possible to build a (product) timed automaton A which is strongly bisimilar (in the sense of, e.g., [54]) to $(A_1 | \dots | A_n)$. Such an automaton is $A = \langle \text{Act}, N, \bar{q}_0, C, E \rangle$ with

- $N = N_1 \times \dots \times N_n$,
- $\bar{q}_0 = (q_{1,0}, \dots, q_{n,0})$,
- $C = C_1 \cup \dots \cup C_n$, and
- E contains an edge $\bar{q} \xrightarrow{g, a, r} \bar{q}'$ iff there are $a_1, \dots, a_n \in \text{Act} \cup \{\bullet\}$, guards g_1, \dots, g_n and reset sets r_1, \dots, r_n such that:
 - $f(a_1, \dots, a_n) = a$, $g = \bigwedge_i g_i$ and $r = \bigcup_i r_i$, and
 - for every $i \in \{1, \dots, n\}$,
 - $a_i = \bullet$, $g_i = \text{tt}$, $r_i = \emptyset$ and $q'_i = q_i$, or
 - there is an edge $q_i \xrightarrow{g_i, a_i, r_i} q'_i$ in A_i .

It is well known (see, e.g., [35,39]) that the size of the automaton A so constructed is in $2^{O(|\bar{A}|)}$. Moreover, as in the untimed case, it is not hard to prove that:

Lemma 30. \bar{A} and A satisfy exactly the same formulae in $L_{\mu, v}^+$.

Therefore, to decide whether $(A_1 | \dots | A_n)_f \models \varphi$ holds for a formula $\varphi \in L_{\mu, v}^+$, it is sufficient to consider the region graph (S, \longrightarrow) corresponding to φ and the product timed automaton A associated with the parallel composition $(A_1 | \dots | A_n)_f$. Using the analysis in Section 2.4, we have that:

$$S = N_1 \times \dots \times N_n \times \mathcal{R}_M^{C^+},$$

where

- $M = \max(\text{MCst}(A), \text{MCst}(\varphi))$, and
- $C^+ = \bigcup_i C_i \cup K$.

(Recall that K denotes the set of formula clocks.) Moreover the size of the transition relation in the region graph, viz. $|\longrightarrow|$, is in

$$O\left((|f| \cdot \prod_i |E_i| + \prod_i |N_i|) \cdot |C^+|! \cdot M^{|C^+|}\right).$$

Note, furthermore, that the size of the region graph associated with the product automaton A and the formula φ (viz. $|S| + |\longrightarrow|$) is exponential in $(|\varphi| + |\bar{A}|)^2$.

For the sake of clarity, we remark that the regions used in algorithms for model checking networks of timed automata deal with the set of clocks $\bigcup_i C_i \cup K$. Moreover, we recall that the basic operations on regions mentioned in Section 2.4 can be performed in polynomial time.

We are now in a position to study the complexity of model checking for the property languages considered in the previous section over concurrent timed systems. We shall see that, in all cases, the concurrency feature does not increase the structural complexity of the model checking problems.

4.1. Reachability in concurrent timed systems

We begin by studying the complexity of the reachability problem in networks of timed automata. It is well known that reachability in communicating finite state machines is PSPACE-complete [49]. For parallel compositions of timed automata, we obtain the following result.

Theorem 31. *The node reachability problem over concurrent timed systems is PSPACE-complete.*

Proof. PSPACE membership. Let $\bar{A} = (A_1 | \dots | A_n)_f$ be a parallel composition of timed automata. Let r be a node of A_i ($i \in \{1, \dots, n\}$). Deciding whether r is reachable from the initial state (\bar{q}_0, γ_0) of the region graph associated to the product automaton A determined by \bar{A} (which is equivalent to the reachability of r in the timed labelled transition system associated with \bar{A}) can be done by using the following non-deterministic algorithm which builds, step by step, a path in the region graph leading to a configuration with r as one of its control nodes, if one such configuration and path exist:

If the current state (\bar{q}, γ) contains r then the answer is “yes”, otherwise the algorithm guesses (\bar{q}', γ') , that is the next configuration on a path leading to r , where $\bar{q}' \in N_1 \times \dots \times N_n$ and γ is a region over $C_1 \cup \dots \cup C_n$, and it verifies (in polynomial time) that $(\bar{q}, \gamma) \xrightarrow{\text{succ}^*} (\bar{q}', \gamma')$ or $(\bar{q}, \gamma) \xrightarrow{a} (\bar{q}', \gamma')$, for some action a .

At any step we only need to store at most two configurations. By Savitch’s theorem [66], the use of non-determinism in the above algorithm is inessential.

PSPACE-hardness. The reachability problem in a (single) timed automaton is PSPACE-hard (see Lemma 7). \square

4.2. Model checking for sublanguages of $L_{\mu,v}^+$ for concurrent timed systems

The (program) complexity of model checking for the (alternation-free) μ -calculus for untimed concurrent programs is EXPTIME-complete [51]. For parallel compositions of timed automata and fragments of $L_{\mu,v}^+$, we obtain the following results.

Theorem 32. *The model checking problem for $L_{\mu,v}^+$, $L_{\mu,v}$ and L_v over concurrent timed systems is EXPTIME-complete.*

Proof. EXPTIME membership. We use an approach similar to the one used in [51] to prove the EXPTIME membership of the (untimed) μ -calculus model checking problem for concurrent programs. We have previously seen in Lemma 30 that, for every formula $\varphi \in L_{\mu,v}^+$,

$$(A_1 | \cdots | A_n)_f \models \varphi \text{ iff } A \models \varphi,$$

where A is the timed automaton corresponding to the product of $(A_1 | \cdots | A_n)_f$ with respect to the specified synchronization function. Moreover we know that $A \models \varphi$ iff $\tilde{A} \models \varphi$ where \tilde{A} is an untimed automaton (the *region graph*) associated with A and φ whose size is exponential in $(|\varphi| + \sum_i |A_i| + |f|)^2$, and over which φ is interpreted as an untimed formula. If we modify slightly \tilde{A} by adding the transitive closure of the transition relation \xrightarrow{succ} , the size of the resulting automaton is still exponential in $(|\varphi| + \sum_i |A_i| + |f|)^2$, and \exists and \forall become “one step” modalities. Then φ may be viewed as an untimed μ -calculus formula for which time complexity of model checking is in $O((|\varphi| \cdot |\tilde{A}|)^{alt+1})$ where alt is the alternation depth of φ [38]. Clearly we have $alt < |\varphi|$, and then we obtain an algorithm which is exponential in $|\varphi| \cdot (|\varphi| + \sum_i |A_i|)^2$. This gives the EXPTIME membership for $L_{\mu,v}^+$, $L_{\mu,v}$ and L_v .

EXPTIME-hardness. The model checking problem for L_v is EXPTIME-hard for one timed automaton (Theorem 9). \square

Theorem 33. *The model checking problem for L_v^- over concurrent timed systems is PSPACE-complete.*

Proof. PSPACE membership. A non-deterministic model checking algorithm in PSPACE can be easily defined by considering the parts of the region graph associated to $(A_1 | \cdots | A_n)_f \models \varphi$ only when they are required. To this end, simply adapt the algorithm given in the proof of Theorem 11(3). Again, by Savitch’s theorem [66], the use of non-determinism in the above algorithm is inessential.

PSPACE-hardness. Immediate by Theorem 11. \square

Theorem 34. *The model checking problem for L_s , SBL and $L_{\forall s}$ over concurrent timed systems is PSPACE-complete.*

Proof. PSPACE membership. For any of these property languages, an instance of the model checking problem $(A_1 | \cdots | A_n)_f \models \varphi$ can be reduced (in polynomial time) to a reachability problem [2,1] in $((A_1 | \cdots | A_n)_f | T_\varphi)$ where T_φ is a timed automaton s.t. $|T_\varphi| \in O(|\varphi|)$. Hence Theorem 31 gives the PSPACE upper bound.

PSPACE-hardness. This is a consequence of Theorem 16. \square

Theorem 35. *The model checking problem for L_s^- over concurrent timed systems is coNP-complete.*

Proof. coNP membership. Let $\bar{A} = (A_1 | \cdots | A_n)_f$ be a concurrent timed system and ψ be an L_s^- formula. We can define a non-deterministic and “on the fly” polynomial algorithm over the region graph associated with A (viz. the product automaton induced by \bar{A}) to

decide if the negation of ψ (containing only existential modalities) holds for (\bar{n}_0, γ_0) , the initial symbolic configuration of A . We give the main cases:

- CASE: $(\bar{r}, \gamma) \models \varphi_1 \vee \varphi_2$. The procedure first guesses non-deterministically the φ_i which has to hold for (\bar{r}, γ) , and then proceeds by checking whether $(\bar{r}, \gamma) \models \varphi_i$.
- CASE: $(\bar{r}, \gamma) \models g \wedge \varphi$. The procedure first verifies that g holds for region γ , and then proceeds by checking whether $(\bar{r}, \gamma) \models \varphi$.
- CASE: $(\bar{r}, \gamma) \models \exists \varphi$. First the procedure guesses a region γ' such that γ' is reachable from γ with a sequence of delay transitions. Then it verifies, in polynomial time, that $\gamma \xrightarrow{\text{succ}^*} \gamma'$ holds, and finally it checks whether $(\bar{r}, \gamma') \models \varphi$.
- CASE: $(\bar{r}, \gamma) \models \langle a \rangle \varphi$. The procedure guesses a configuration (\bar{r}', γ') such that $(\bar{r}, \gamma) \xrightarrow{a} (\bar{r}', \gamma')$ holds (this can be verified in polynomial time), and then proceeds by checking whether $(\bar{r}', \gamma') \models \varphi$.
- CASE: $(\bar{r}, \gamma) \models K' \text{ in } \varphi$. We simply proceed by checking whether it holds that $(\bar{r}, [K' \rightarrow 0]\gamma) \models \varphi$.

Note that the absence of fixed points entails that deciding whether \bar{A} satisfies an L_s^- formula needs to consider only executions with at most $|\varphi|$ action transitions. For a formula φ , the algorithm sketched above takes at most $|\varphi|$ steps, and each step has a complexity polynomial in $(|\varphi| + |A|)$.

coNP-hardness. This is an immediate consequence of Theorem 17. \square

The following result is a straightforward corollary of the PSPACE membership of reachability in networks of timed automata (Theorem 31) and of the PSPACE-hardness of the model checking problem for $SBLL^-$ and $L_{\forall S}^-$ (Theorem 28).

Theorem 36. *The model checking problem for $SBLL^-$ and $L_{\forall S}^-$ over concurrent timed systems is PSPACE-complete.*

These results show that, unlike in the untimed setting [51], the concurrency feature does not increase the structural complexity of model checking for timed systems. A natural question to ask is whether the same holds true for the specification and program complexities.

We have the following results:

- The specification complexity does not change because, since the program being considered is fixed, its structure does not matter (from a parallel composition of timed automata \bar{A} , it is possible to build the corresponding product timed automaton A , in time constant in the size of the specification).
- The program complexity for model checking with respect to networks of timed automata cannot change for $L_{\mu, v}^+$, $L_{\mu, v}$, L_v , L_s , $SBLL$, $SBLL^-$, $L_{\forall S}$ and $L_{\forall S}^-$ since the program complexity for these property languages is already as hard for one timed automaton as the general model checking problem for concurrent timed systems.

The program complexity for model checking L_v^- over concurrent timed systems is NP-hard and coNP-hard (and belongs to PSPACE) since this is already the case for the untimed Hennessy–Milner logic: it is not hard to see that the satisfiability (resp. validity) problem for boolean expressions can be encoded as a model checking problem for the formula $\langle a \rangle [b] \text{ ff}$ (resp. $[a] [b] \text{ ff}$) over a parallel composition of automata.

The program complexity for model checking L_s^- over concurrent timed systems is coNP-complete. Indeed coNP-hardness comes from the above remark, and coNP-membership can be shown by using similar arguments to those used in the proof of

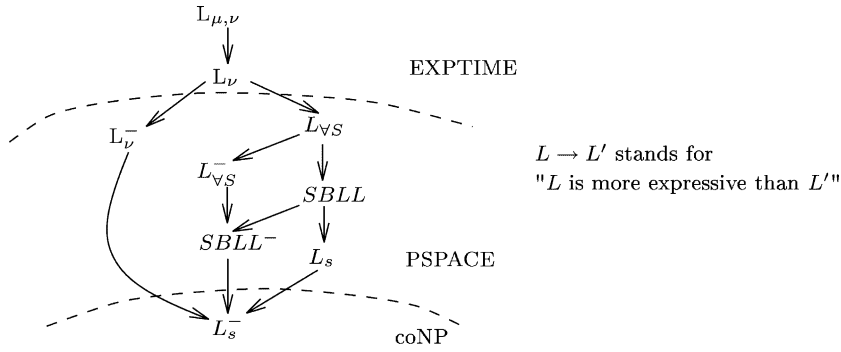


Fig. 2 Expressiveness vs complexity of model checking.

Theorem 11(3): if the formula Φ does not hold for a system S , then there exists a short path ρ (the number of action transitions is at most $|\Phi|$) in S satisfying $\neg\Phi$ and verifying $\rho \models \neg\Phi$ can be done in polynomial time whenever ρ is given.

5. Conclusion

The relationships between the relative expressive power of the property languages that we have considered in this paper, and the complexity of their model checking problems, are summarized in Fig. 2. (There $L \rightarrow L'$ means that any model checking problem $A \models \varphi$ with $\varphi \in L'$ can be reduced in linear time to an equivalent model checking problem $\tilde{A} \models \tilde{\varphi}$ with $\tilde{\varphi} \in L$.)

In the discussion to follow, we shall use the abbreviations $\mathcal{A}_{+t} \stackrel{?}{\models} \Psi_{+t}$, $\mathcal{A} \stackrel{?}{\models} \Psi_{+t}$ and $\mathcal{A}_{+t} \stackrel{?}{\models} \Psi$ to denote, respectively, the model checking problems where clocks are allowed both in automata and specifications, where clocks are allowed only in specifications and where clocks are allowed only in automata.

Note that, for each of the specification languages that we have studied, the proof of \mathcal{C} -hardness of the corresponding model checking problem uses formulae without clocks (cf. the proofs of the hardness results in Theorems 9–28). This implies that the problems $\mathcal{A}_{+t} \stackrel{?}{\models} \Psi$ and $\mathcal{A}_{+t} \stackrel{?}{\models} \Psi_{+t}$ have the same complexity. The results on the complexity of model checking problems of the form $nil \models \varphi$ show that the problems $\mathcal{A} \stackrel{?}{\models} \Psi_{+t}$ and $\mathcal{A}_{+t} \stackrel{?}{\models} \Psi_{+t}$ also have the same complexity, with the exception of $L_s + \langle a \rangle$ and $L_s^- + \langle a \rangle$. Therefore the complexity of model checking does not depend on whether time is added to the model, to the specification or to both. Time seems to be a highly expressive feature in itself, as witnessed, e.g., by the encodings offered in Appendix B.

Our results on the structural complexity of model checking for real-time concurrent programs match those for untimed concurrent programs offered in, e.g., [51]. However, we think that these kind of results must be taken with a grain of salt, and cannot be used by themselves to claim that model checking real-time programs is just as efficient (or inefficient) as model checking untimed ones in practice. First of all, the worst case time complexity of model checking algorithms for an untimed specification logic like the μ -calculus interpreted w.r.t. concurrent programs is in $O(2^n)$, where n is the size of the input. On the other hand, the theoretical algorithms we have offered in this paper for model checking

the logic $L_{\mu,v}^+$ w.r.t. networks of timed automata run in time $O(2^{n^2})$. In practice, there is a big difference between the two bounds. Secondly, and more importantly, the complexity of model checking real-time programs is due to at least two concomitant factors. Indeed, apart from the well-known state explosion problem brought about by the concurrency feature, one has to deal with the complexity deriving from the use of timing information (e.g., in the form of clocks and comparisons amongst clocks). The research community on verification technology has developed data structures (e.g., Reduced Ordered Binary Decision Diagrams [21]) and a variety of techniques (see, e.g., [13,20,26]) to alleviate the explosion in the number of control states deriving from cooperative concurrency. Similarly, efficient data structures have been developed to handle timing information (e.g., DBMs [33]). However, the development of efficient data structures and techniques to handle at the same time both timing information *and* the state explosion problem has so far proved elusive. We expect, however, exciting developments on this line of research—see, e.g., [16].

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Appendix

A. The property languages *SBLL* and L_{VS}

Let $ALW_\tau(\psi)$ be the formula $\max(X, \psi \wedge [\tau]X)$ (where X is a new identifier). Intuitively $ALW_\tau(\psi)$ holds for an extended state (n, v, u) of a timed automaton A iff ψ holds for any (n', v', u) whenever (n', v') is reachable from (n, v) in A by using only τ -transitions.

Let $ALW_{\tau,\delta}(\psi)$ be the formula $\max(X, \psi \wedge [\tau]X \wedge \mathbb{W}X)$ (where X is a new identifier). Intuitively, $ALW_{\tau,\delta}(\psi)$ holds for an extended state (n, v, u) of A iff ψ holds for any $(n', v', u + d)$ whenever (n', v') is reachable from (n, v) in A by using only τ -transitions and delay transitions of total duration d .

The rules in Table 6 give a way of building the equivalent L_v formula $\bar{\varphi}$ from an *SBLL* formula φ (for example with a top-down algorithm), and we have:

Table 6
Building $\bar{\varphi}$ from an *SBLL* formula φ

$\bar{\neg\varphi}$	$\stackrel{\text{def}}{=} \neg\bar{\varphi}$	$\overline{\psi_1 \wedge \psi_2}$	$\stackrel{\text{def}}{=} ALW_\tau(\overline{\psi_1} \wedge \overline{\psi_2})$
$\overline{x \text{ in } \psi}$	$\stackrel{\text{def}}{=} x \text{ in } ALW_\tau(\overline{\psi})$	$\overline{g \vee \psi}$	$\stackrel{\text{def}}{=} g \vee ALW_\tau(\overline{\psi})$
$\overline{[a]\psi}$	$\stackrel{\text{def}}{=} ALW_\tau([a]\overline{\psi})$	$\overline{\langle a \rangle \psi}$	$\stackrel{\text{def}}{=} ALW_\tau(\langle a \rangle \overline{\psi})$
$\overline{\mathbb{W}\psi}$	$\stackrel{\text{def}}{=} ALW_{\tau,\delta}(\overline{\psi})$	$\overline{\max(Y, \psi)}$	$\stackrel{\text{def}}{=} ALW_\tau(\max(Y, \overline{\psi}))$

Lemma A.1. *Let A be a timed automaton. Suppose that φ is an SBLL formula, and let $\bar{\varphi}$ be the formula defined as above. Then we have that $A \models \varphi$ iff $A \models \bar{\varphi}$.*

The size of $\bar{\varphi}$ is linear in the size of φ . Note that it would be possible to translate every SBLL formula into an extension of the property language L_S with formulae of the form $\langle a \rangle \text{tt}$.

As for SBLL, it is possible to translate any L_{VS} formula φ into a L_V formula $\bar{\varphi}$ such that for any timed automaton A , we have $A \models \varphi \Leftrightarrow A \models \bar{\varphi}$. The only difference with SBLL concerns the \mathbb{W}_S operator:

$$\overline{\mathbb{W}_S \varphi} \stackrel{\text{def}}{=} \max \left(X, \bar{\varphi} \wedge [\tau]X \wedge \left(\bigvee_{a \in S} \langle a \rangle \text{tt} \vee \mathbb{W}X \right) \right).$$

B. Complexity of *nil* model checking

This appendix is devoted to the proofs of the lower bounds for the complexity of the *nil* model checking problems for the logics studied in Section 3.2 of the main body of the paper.

Proof (of Lemma 13).

• **EXPTIME-hardness of *nil*-MC for $L_{\mu,v}^+$, $L_{\mu,v}$ and L_v .** We show how we can encode the behaviour of an LBATM \mathcal{M} over an input w_0 by means of an L_v formula. The encoding we present uses formula clocks to represent the configurations of \mathcal{M} . Clock constraints and the in operator are employed to simulate the transitions of \mathcal{M} on input w_0 . Again we assume that we have strict alternations of “and” and “or” states and that $\Sigma = \{a, b\}$. Our aim is to construct an L_v formula Φ from the description of \mathcal{M} and the input w_0 such that \mathcal{M} accepts w_0 iff $\text{nil} \not\models \Phi$.

In the formula Φ , we use $3n + k + 1$ clocks where $n = |w_0|$ and k is the number of states in \mathcal{M} . Furthermore, we assume that the states are numbered q_0, \dots, q_{k-1} with $q_F = q_{k-1}$. The set of clocks is

$$\{x_i, y_i, c_i \mid i = 1, \dots, n\} \cup \{z_j \mid j = 0, \dots, k-1\} \cup \{t\}.$$

The clocks x_i and y_i encode the contents of the i th tape cell thus: the i th tape cell contains the symbol a (resp. b) if $x_i = y_i$ (resp. $x_i < y_i$). The clocks c_i encode the position of the tape head: the head is at position i iff $c_i < 1$. The clocks z_j encode the current control state (when a transition has to be performed) as follows: \mathcal{M} is in state q_j iff $z_j < 1$. The clock t is used to ensure time elapsing, and, indirectly, that after each transition there are exactly one clock c_i and one clock z_j whose values are strictly smaller than 1.

Writing the initial configuration on the tape consists in waiting for some positive delay greater than 1, then resetting each $x_i \in r_{w_0}$, with

$$r_{w_0} = \{x_i \mid w_0(i) = b\},$$

(we write w_0 on the tape) and waiting for some positive delay, and thereafter resetting z_0 (\mathcal{M} starts computing from its initial state q_0) and c_1 (the tape head is reading the first tape cell). We have:

$$\Phi \stackrel{\text{def}}{=} \mathbb{W} \left(t > 1 \Rightarrow \left(r_{w_0} \cup \{t\} \text{ in } \mathbb{W}(t > 0 \Rightarrow (\{z_0, c_1\} \text{ in } \max(X, \Psi))) \right) \right). \quad (\text{B.1})$$

The formula $\max(X, \Psi)$ has to express that from an “or” state q , whatever is the chosen enabled transition in \mathcal{M} which leads from q to an “and” state q' , it is then possible to find a successor configuration that is not accepting (that is, which has $z_{k-1} \geq 1$) and so on. For every transition $\theta = (q_j, \alpha, \alpha', \delta, q_{j'})$ of \mathcal{M} with q_j an *or*-state, let φ_θ^i be the following formula which simulates the transition θ when the tape head is reading cell i :

$$\begin{aligned} \varphi_\theta^i \stackrel{\text{def}}{=} (z_j < 1 \wedge c_i < 1 \wedge g_\alpha^i) \Rightarrow \\ r_{\alpha'}^i \cup \{t\} \underline{\text{in}} \mathbb{W}(t > 1 \Rightarrow (\{z_{j'}, c_{i'}\} \underline{\text{in}} \bigvee_{\theta' \in T(q_{j'})} \psi_{\theta'}^{i'})), \end{aligned} \quad (\text{B.2})$$

where $T(q_{j'})$ denotes the collection of transitions whose source is state $q_{j'}$. The guard g_α^i is $x_i = y_i$ (resp. $x_i < y_i$) if α is a (resp. b). The reset set $r_{\alpha'}^i$ is $\{x_i, y_i\}$ (resp. $\{x_i\}$) if α' is a (resp. b). Finally $i' = i + 1$ if $\delta = R$ and $i < n$, and $i' = i - 1$ if $\delta = L$ and $i > 1$. The formula φ_θ^i can be read as follows: “if the current state is q_j , the tape head is at i and the i th symbol of the tape is α , then after the transition θ , there exists a transition θ' starting from the new configuration (control state $q_{j'}$, symbol α' at i and i' as new position for the head) such that $\psi_{\theta'}^{i'}$ holds”. For every *and*-state q_j , the formula ψ_θ^i states that transition $\theta = (q_j, \alpha, \alpha', \delta, q_{j'})$ leads from a configuration whose state is q_j and with tape head at i to a configuration in X (that is a non-accepting *or*-configuration) thus:

$$\psi_\theta^i \stackrel{\text{def}}{=} g_\alpha^i \wedge (r_{\alpha'}^i \cup \{t\} \underline{\text{in}} \mathbb{W}(t > 1 \Rightarrow \{z_{j'}, c_{i'}\} \underline{\text{in}} X)). \quad (\text{B.3})$$

Note that $z_{j'}$ and $c_{i'}$ are reset after the delay making $t > 1$ in order to ensure that their value will be 0 (and thus less than 1) for the next step of \mathcal{M} . Finally the formula Ψ in (B.1) is the following one:

$$\Psi \stackrel{\text{def}}{=} (z_{k-1} \geq 1) \wedge \bigwedge_{i=1}^n \bigwedge_{\theta \in T_{\text{or}}} \varphi_\theta^i, \quad (\text{B.4})$$

where T_{or} is the set of transitions starting from an *or*-state. The LBATM \mathcal{M} accepts w_0 iff $\text{nil} \not\models \Phi$. The reduction we have just presented is polynomial, and gives the EXPTIME-hardness of the *nil* model checking problem for L_v (and, *a fortiori*, for $L_{\mu,v}^+$ and $L_{\mu,v}$).

- **PSPACE-hardness of *nil*-MC for L_v^- .** Let $\Phi = Q_1 p_1 \cdots Q_n p_n \cdot \varphi$ be an instance of the QBF problem. The following formula $\bar{\Phi}$ encodes it as a model checking problem for *nil*:

$$\begin{aligned} \{t, x_1, \dots, x_n\} \underline{\text{in}} \exists \dots \\ \exists \left(t = i \wedge \left| \begin{array}{l} \exists (t < i + 1 \wedge \dots \wedge x_i \underline{\text{in}} \exists (t = i + 1 \wedge \dots \wedge \exists (t = n + 1 \wedge \bar{\varphi}))) \\ \mathbb{W}(t < i + 1 \Rightarrow \dots) \end{array} \right| \right) \end{aligned}$$

The first (respectively, second) line corresponds to the case $Q_i = \exists$ (resp. $Q_i = \forall$). Moreover,

$$\bar{\varphi} \stackrel{\text{def}}{=} \varphi[p_i \leftarrow x_i = n + 1 - i, \bar{p}_i \leftarrow x_i < n + 1 - i]_{i=1}^n.$$

Note that each clock x_i is reset to 0 at a time in $[i; i + 1[$. The interpretation of the above construction is as follows: if x_i is reset at i , then p_i is tt , otherwise p_i is ff . We have that $\text{nil} \models \bar{\Phi}$ iff Φ is valid. \square

Proof (of Lemma 18).

- **PSPACE-hardness of nil-MC for L_s .** We show how we can encode the behaviour of a linear bounded non-deterministic Turing machine \mathcal{M} over an input w_0 of length n as an L_s formula Φ which holds for nil iff w_0 is not accepted by \mathcal{M} . To this end, we use the same idea⁴ underlying the proof of EXPTIME-hardness of nil-MC for L_v (see the proof of Lemma 13). Consider the formula:

$$\Phi \stackrel{\text{def}}{=} \mathbb{W}(t > 1 \Rightarrow (r_{w_0} \cup \{t\} \text{ in } \mathbb{W}(t > 0 \Rightarrow (\{z_0, c_1\} \text{ in } \max(X, \Psi))))).$$

We remark that the formula Ψ used for proving Lemma 13 encodes an alternating behavior of \mathcal{M} and does not belong to L_s .

Here \mathcal{M} is not an alternating machine and the formula $\max(X, \Psi)$ has just to express that whatever the chosen transition in \mathcal{M} , it is not possible to reach an accepting configuration (that is, $z_{k-1} \geq 1$ always holds). For every transition $\theta = (q_j, \alpha, \alpha', \delta, q_{j'})$ of \mathcal{M} , let φ_θ^i be the following formula which simulates the transition θ when the tape head is reading cell i :

$$\varphi_\theta^i \stackrel{\text{def}}{=} (z_j < 1 \wedge c_i < 1 \wedge g_\alpha^i) \Rightarrow r_{\alpha'}^i \cup \{t\} \text{ in } \mathbb{W}(t > 1 \Rightarrow (\{z_{j'}, c_{i'}\} \text{ in } X)),$$

where g_α^i is $x_i = y_i$ (resp. $x_i < y_i$) if α is a (resp. b). The reset set $r_{\alpha'}^i$ is $\{x_i, y_i\}$ (resp. $\{x_i\}$) if α' is a (resp. b). Finally $i' = i + 1$ if $\delta = R$ and $i < n$, and $i' = i - 1$ if $\delta = L$ and $i > 1$. The formula Ψ is the following one:

$$\Psi \stackrel{\text{def}}{=} (z_{k-1} \geq 1) \wedge \bigwedge_{\theta, i} \varphi_\theta^i.$$

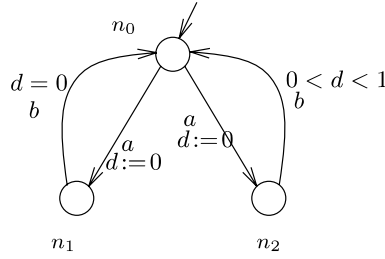
The formula Φ belongs to L_s and its size is in $O(|\mathcal{M}| \cdot |w_0|)$. The construction is done in polynomial time and we have that $\text{nil} \models \Phi$ iff q_{k-1} is not reachable in \mathcal{M} on input w_0 .

- **coNP-hardness of nil-MC for L_s^- .** In the proof of the second statement of Lemma 13, we encoded an instance of the problem QBF as an L_v^- formula which has to hold for the nil process. We can transform this formula into an L_s^- formula encoding a validity problem. Note that a validity problem for φ over the atomic propositions $\{p_1, \dots, p_n\}$ can be seen as the following QBF problem: $\forall p_1. \forall p_2 \dots \forall p_n. \varphi$. We can reuse the formula Φ used for proving Lemma 13 with small changes in order to obtain an L_s^- formula. To this end, we replace the subformulae $\exists(t = i \wedge \dots)$ by $\mathbb{W}(t = i \Rightarrow \dots)$. With this modification, we obtain $\Phi = \{t, x_1, \dots, x_n\} \text{ in } \Psi$ with:

$$\Psi = \left(\mathbb{W} \dots \mathbb{W}(t = i \Rightarrow \mathbb{W}(t < i + 1 \Rightarrow x_i \text{ in } \mathbb{W}(t = i + 1 \Rightarrow \dots)) \dots \mathbb{W}(t = n + 1 \Rightarrow \bar{\varphi})) \right),$$

where $\bar{\varphi} = \varphi[p_i \leftarrow x_i = n + 1 - i, \bar{p}_i \leftarrow x_i < n + 1 - i]_{i=1}^n$. \square

⁴ The same encoding is used, but, since \mathcal{M} is not an alternating machine, every control state is an “or” state.

Fig. 3. The timed automaton \mathcal{A} .

C. The extensions of L_s

This appendix is devoted to proofs that were omitted from Section 3.4 in the main body of the paper.

Proof (of Proposition 19). We show the EXPTIME-hardness of specification complexity for $L_s + \langle a \rangle$ model checking. We reduce the acceptance problem of an input word w_0 by a LBATM \mathcal{M} to a model checking instance $A \models \varphi$ where A is a timed automaton which does not depend on \mathcal{M} or w_0 , and $\varphi \in L_s + \langle a \rangle$.

We assume, without loss of generality, that for each control state q , there are two transitions $(\theta_{a,1}^q$ and $\theta_{a,2}^q)$ of the form (q, a, \dots) and two transitions $(\theta_{b,1}^q$ and $\theta_{b,2}^q)$ of the form (q, b, \dots) . Therefore for any configuration of \mathcal{M} , there are exactly *two* enabled transitions.

We reuse the encoding done to prove EXPTIME-hardness of *nil*-model checking for L_v (Lemma 13, Appendix B): we use $2n$ clocks ($|w_0| = n$) x_i and y_i ($1 \leq i \leq n$) to represent the tape content, n clocks c_i ($1 \leq i \leq n$) to store the current position of the tape head, k clocks z_j (k is the number of control states of \mathcal{M}). Moreover we use two additional clocks t and t' . Let \mathcal{A} be the timed automaton described in Fig. 3. From the node n_0 , there are two a -transitions: the first one has to be followed by a b -transition with a zero delay while the second one requires a delay in $]0; 1[$ before performing the b -transition. We use this fact to encode the existential choice. \mathcal{M} accepts w_0 iff $\mathcal{A} \models \Phi$ with

$$\Phi \stackrel{\text{def}}{=} \mathbb{W}(t > 1 \Rightarrow (r_{w_0} \cup \{t\} \text{ in } \mathbb{W}(t > 0 \Rightarrow \{z_0, c_1\} \text{ in } \max(X, \Psi))))),$$

$$\Psi \stackrel{\text{def}}{=} (z_{k-1} \geq 1) \wedge \bigwedge_{i=1}^n \bigwedge_{\theta \in T_{\text{or}}} \varphi_{\theta}^i.$$

Let θ be a transition $(q_j, \alpha, \alpha', \delta, q_{j'})$. We have

$$\varphi_{\theta}^i \stackrel{\text{def}}{=} (z_j < 1) \wedge (c_i < 1) \wedge g_{\alpha}^i \Rightarrow \left[(r_{\alpha'}^i \cup \{t\}) \text{ in } \mathbb{W}(t > 1 \Rightarrow \{t'\} \text{ in } \right.$$

$$\langle a \rangle \mathbb{W}[b] \left[t' = 0 \Rightarrow \{z_{j'}, c_{i'}\} \text{ in } \left((x_{i'} = y_{i'} \Rightarrow \psi_{\theta_{a,1}^{q_{j'}}}^{i'}) \wedge (x_{i'} < y_{i'} \Rightarrow \psi_{\theta_{b,1}^{q_{j'}}}^{i'}) \right) \wedge \right.$$

$$\left. \left. t' > 0 \Rightarrow \{z_{j'}, c_{i'}\} \text{ in } \left((x_{i'} = y_{i'} \Rightarrow \psi_{\theta_{a,2}^{q_{j'}}}^{i'}) \wedge (x_{i'} < y_{i'} \Rightarrow \psi_{\theta_{b,2}^{q_{j'}}}^{i'}) \right) \right] \right]$$

with

$$\psi_{\theta}^i \stackrel{\text{def}}{=} g_{\alpha}^i \wedge r_{\alpha'}^i \cup \{t\} \text{ in } \mathbb{W}(t > 1 \Rightarrow \{z_{j'}, c_{i'}\} \text{ in } X).$$

After any transition from a non-accepting “or” configuration of \mathcal{M} (and leading to an “and” configuration), there exists a transition (among the two possible ones) which leads to a non-accepting “or” configuration. Choosing one a -transition in \mathcal{M} is used to choose which transition (among the two enabled ones) in \mathcal{M} has to be performed to follow the non-accepting computation. \square

Proof (of Proposition 22). To prove that the specification complexity of $L_s^- + \langle a \rangle$ model checking is PSPACE-hard, we adapt the proof used to show the PSPACE-hardness of L_v^- *nil*-model checking (Lemma 13, Appendix B) using the same idea employed in the previous proof. A QBF instance

$$\Phi = Q_1 p_1 \cdots Q_n p_n \cdot \varphi$$

is reduced to a model checking problem $\mathcal{A} \models \bar{\Phi}$ where \mathcal{A} is the TA described in Fig. 3 and $\bar{\Phi}$ is the formula: $\{t, x_1, \dots, x_n\} \dot{\sqsubseteq} \Psi$ with:

$$\Psi \stackrel{\text{def}}{=} \mathbb{W}(t=1 \Rightarrow \dots \dots \mathbb{W}\left(t=i \Rightarrow \left| \begin{array}{c} \langle a \rangle \mathbb{W}[b] \\ \mathbb{W}(t < i+1 \Rightarrow \dots \mathbb{W}(t=n+1 \Rightarrow \bar{\varphi})) \end{array} \right| x_i \dot{\sqsubseteq} \mathbb{W}(t=i+1 \Rightarrow \dots \mathbb{W}(t=n+1 \Rightarrow \bar{\varphi}))\right)).$$

The subformula $\langle a \rangle \mathbb{W}[b] \dots$ replaces $\exists(t < i+1 \wedge \dots)$, which was previously used for L_v^- . As for Lemma 13, we have

$$\bar{\varphi} \stackrel{\text{def}}{=} \varphi[p_i \leftarrow x_i = n+1-i, \bar{p}_i \leftarrow x_i < n+1-i]_{i=1}^n. \quad \square$$

Proof (of Proposition 23). Since $L_s + \vee$ is a sublanguage of L_v , it is sufficient to argue that the (specification) complexity of the model checking problem for $L_s + \vee$ is EXPTIME-hard. To this end, note that the formula Φ (cf. Eqs. (B.1)–(B.4) in Appendix B) used to prove that the *nil* model checking problem for L_v is EXPTIME-complete (Lemma 13) belongs to $L_s + \vee$. It follows that the model checking problem, and the *nil* model checking problem for $L_s + \vee$ are EXPTIME-complete. Since model checking for the *nil* process is EXPTIME-complete, so is the specification complexity of model checking.

Program complexity. We show that the program complexity of $L_s + \vee$ model checking is EXPTIME-hard. Let \mathcal{M} be a LBATM and w_0 be an input word. As for the proof of Proposition 19, we assume without loss of generality that for each control state q , there are 2 transitions ($\theta_{a,1}^q$ and $\theta_{a,2}^q$) of the form (q, a, \dots) and two transitions ($\theta_{b,1}^q$ and $\theta_{b,2}^q$) of the form (q, b, \dots) . We reduce the acceptance problem of w_0 by \mathcal{M} to a model checking problem $A'_{\mathcal{M}, w_0} \models \Phi$ where $A'_{\mathcal{M}, w_0}$ is built in the same way as in the proof of Theorem 9 except we have to replace the $\langle a \rangle$ operators: for any “and” state q and any position i of the tape head, we label by s_ϵ the transition from (q, i) which simulates $\theta_{\alpha, \epsilon}^q$ for $\epsilon \in \{1, 2\}$. And we use the following formula for Φ :

$$\Phi \stackrel{\text{def}}{=} \mathbb{W}[s_0]_{\max} \left(X, [\text{accept}]_{\text{ff}} \wedge \mathbb{W}[s] \mathbb{W}([s_1]X \vee [s_2]X) \right).$$

The idea is that after the initialization step, the configuration is not accepting and after an s -transition, any delay of duration d with $d \neq 1$ leads to a state from which no s_i is enabled (and then the subformula $[s_1]X \vee [s_2]X$ holds) and a delay of length 1 leads to a state from which there exists a transition (labeled by s_1 or s_2) leading to a non-accepting

configuration. Note that only one s_1 -transition and one s_2 -transition are enabled. Clearly the previous formula does not depend of \mathcal{M} and w_0 . \square

Proof (of Proposition 25). (Specification) complexity. Since $L_s + \Xi$ is a sublanguage of L_v , it is sufficient to argue that the (specification) complexity of the model checking problem for $L_s + \Xi$ is EXPTIME-hard. To this end, we begin by recalling that the formula Φ (cf. Eqs. (B.1)–(B.4) in Appendix B) used to prove that the *nil* model checking problem for L_v is EXPTIME-complete (Lemma 13) belongs to $L_s + \vee$. Nevertheless it is possible to mimic the effect of general disjunction by means of Ξ . The problem consists in writing, without using \vee , the following subformula:

$$\{z_j, c_i\} \underline{\text{in}} \bigvee_{\theta \in T(q_j)} \psi_{\theta}^i \quad (\text{C.1})$$

of (B.2), where $T(q_j)$ is a set of transitions, and each formula ψ_{θ}^i is defined as in (B.3). Let $T(q_j)$ be $\{\theta_1, \dots, \theta_m\}$ ($m \geq 0$). Note that the truth value of each formula ψ_{θ}^i is stable under delay transitions, if the clocks z_j and c_i are reset after the delay. This is because ψ_{θ}^i considers only differences between clocks, except for z_j and c_i which need to be less than 1.

Let t' be a fresh clock. In lieu of (C.1), we are going to use the following formula:

$$\begin{aligned} t' \underline{\text{in}} \Xi & \left(0 \leq t' < 1 \Rightarrow \{z_j, c_i\} \underline{\text{in}} \psi_{\theta_1}^i \wedge \dots \right. \\ & m - 2 \leq t' < m - 1 \Rightarrow \{z_j, c_i\} \underline{\text{in}} \psi_{\theta_{m-1}}^i \wedge \\ & \left. t' \geq m - 1 \Rightarrow \{z_j, c_i\} \underline{\text{in}} \psi_{\theta_m}^i \right). \end{aligned}$$

Again, \mathcal{M} accepts w iff *nil* does not satisfy the resulting modified version of the formula Φ (cf. Eqs. (B.1)–(B.4)). Therefore the *nil* model checking problem for $L_s + \Xi$ is EXPTIME-complete, and so are the model checking problem and the specification complexity.

Program complexity. We show that the program complexity of $L_s + \Xi$ model checking is EXPTIME-hard. We adapt the previous proof to avoid the use of \vee operator in Φ . First we place the guard $0 < t < 1$ in transitions (of $A'_{\mathcal{M}, w_0}$) labeled by s_1 and the guard $t \geq 1$ in transitions labeled by s_2 . In this timed automaton, the delay between two action transitions is just strictly greater than 0 (and not equal to 1 as before). The formula Φ' is the following:

$$\begin{aligned} \Phi = \mathbb{W}[s_0] \max & \left(X, [\text{accept}] \text{ff} \wedge \mathbb{W}[s] \left(t' \underline{\text{in}} \Xi \left(t' > 0 \wedge \right. \right. \right. \\ & \left. \left. \Xi((t' < 1 \Rightarrow [s_1]X) \wedge (t' \geq 1 \Rightarrow [s_2]X)) \right) \right) \right). \end{aligned}$$

The existential quantification over delay transition allows us to choose between the two enabled transitions s_1 and s_2 . \square

D. Complexity results for timed automata with restricted guards

The timed automata we considered in the main body of the paper may use clock constraints of the form $x - y \sim k$, where $\sim \in \{<, >, =\}$. This type of constraints was employed in the proof of Lemma 7 to model configurations of linear bounded Turing machines

by means of states of TA. In this section we shall show that the working of an LBTM on an input w can be simulated by a TA even if we restrict the guards to be boolean combinations of clock constraints of the form $x \sim k$. We also briefly discuss the complexity of model checking problems when timed automata used to describe systems and formulae used to express properties can only use this type of restricted guards.

Let $\mathcal{M} = \langle Q, \Sigma, q_0, q_F, T \rangle$ be a non-deterministic LBTM. We assume, without loss of generality, that $\Sigma = \{a, b\}$. Let w_0 be an input word over Σ^* . From \mathcal{M} and w_0 we are going to build, in polynomial time, a timed automaton $A_{\mathcal{M}, w_0}$ with restricted guards such that w_0 is accepted by \mathcal{M} iff a distinguished node is reachable in $A_{\mathcal{M}, w_0}$.

Let $n = |w_0|$. The timed automaton $A_{\mathcal{M}, w_0}$ is constructed as follows. The set of nodes of $A_{\mathcal{M}, w_0}$ is

$$\{(q, i) \mid q \in Q, 1 \leq i \leq n\} \cup \{init, end\} \cup \{(i, \theta, l) \mid 1 \leq i \leq n, \theta \in T, l \in \{1, \dots, i-1, i+1, \dots, n\}\}.$$

As in the encoding with full TA, a node (q, i) denotes the current state q of \mathcal{M} and the current position i of the tape head. The role played by the nodes (i, θ, l) will be explained below. Again the tape content of the configurations of \mathcal{M} is encoded by means of appropriate clock valuations. For each tape cell C_j ($1 \leq j \leq n$), we have one clock x_j whose value encodes the content of C_j as follows: if C_j contains an a (resp. b), we have $x_j \leq 1$ (resp. $x_j > 1$). In the encoding that we are about to present, the transitions of $A_{\mathcal{M}, w_0}$ which correspond to steps of \mathcal{M} occur precisely every 1 time unit. This will be ensured by means of a clock t that measures the time elapsing between these transitions in $A_{\mathcal{M}, w_0}$. Writing an a on cell j will consist in resetting x_j ; therefore when the next transition simulating a step of \mathcal{M} will be performed, the value of x_j will be precisely 1. Writing a b on cell j will consist in *not* resetting x_j ; therefore when the next transition simulating a step of \mathcal{M} will be performed, the value of x_j will be strictly greater than 1 (since at the previous step x_j was greater or equal to 1). Note that this encoding, unlike the one we employed in the proof of Lemma 7, is not preserved by delay transitions. This means that we will have to update every clock at every step. To this end, we will use the auxiliary states (i, θ, l) , whose intuitive meaning is that “the current tape head position is i , the transition θ is being performed and the content of cell l is being updated”.

The action set of $A_{\mathcal{M}, w_0}$ is $\{s_0, s, \text{upd}, \text{accept}\}$. (The new action symbol upd will label transitions that update the current cell contents.) For every transition $\theta = (q, \alpha, \alpha', \delta, q')$ of machine \mathcal{M} , we add, for every possible position of the tape head $i \in \{1, \dots, n\}$, transitions

$(q, i) \xrightarrow{g, s, r} (i, \theta, l_0)$ such that

- (1) g is $t = 1 \wedge x_i = 1$ (resp. $t = 1 \wedge x_i > 1$) if $\alpha = a$ (resp. $\alpha = b$),
- (2) $r = \{x_i, t\}$ (resp. $r = \{t\}$) if $\alpha' = a$ (resp. $\alpha' = b$), and
- (3) $l_0 = 1$ (resp. $l_0 = 2$) if $i > 1$ (resp. $i = 1$).

In order to preserve the encoding of the contents of all the tape cells that were unaffected by the transition θ in \mathcal{M} , for each $i \in \{1, \dots, n\}$ and θ , we add a set of $2(n-1)$ transitions whose role is to update the value of the clocks x_j with $j \neq i$. This is achieved by means of a sequence of transitions of length $n-1$ with a duration 0. The “updating transitions” we add are the following:

- (1) $(i, \theta, l) \xrightarrow{t=0 \wedge x_l=1, \text{upd}, \{x_l\}} (i, \theta, l+1)$ and $(i, \theta, l) \xrightarrow{t=0 \wedge x_l>1, \text{upd}, \emptyset} (i, \theta, l+1)$ if $l \in \{1, \dots, i-2, i+1, \dots, n-1\}$,
- (2) $(i, \theta, i-1) \xrightarrow{t=0 \wedge x_{i-1}=1, \text{upd}, \{x_{i-1}\}} (i, \theta, i+1)$ and $(i, \theta, i-1) \xrightarrow{t=0 \wedge x_{i-1}>1, \text{upd}, \emptyset} (i, \theta, i+1)$ if $i < n$,

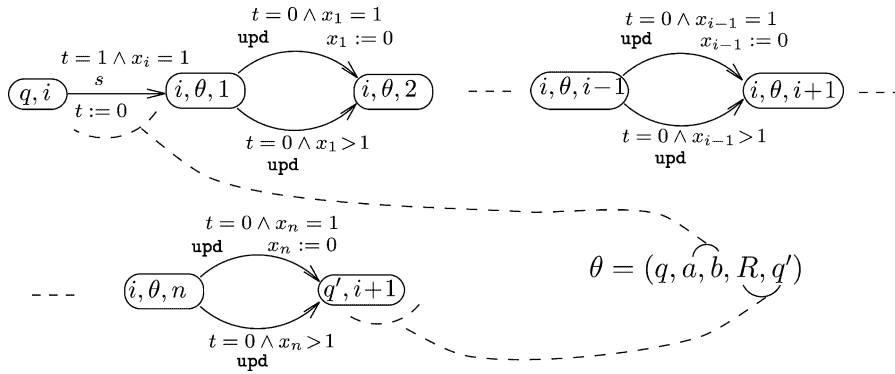


Fig. 4. The sequence of transitions to simulate the transition $\theta = (q, a, b, R, q')$.

- (3) $(i, \theta, n) \xrightarrow{t=0 \wedge x_n=1, \text{upd}, \{x_n\}} (q', i')$ and $(i, \theta, n) \xrightarrow{t=0 \wedge x_n > 1, \text{upd}, \emptyset} (q', i')$, where $i < n$, $\theta = (q, \alpha, \alpha', \delta, q')$ and $i' = i + 1$ (resp. $i' = i - 1$) if δ is R (resp. δ is L and $i > 1$),
- (4) $(n, \theta, n-1) \xrightarrow{t=0 \wedge x_{n-1}=1, \text{upd}, \{x_{n-1}\}} (q', n-1)$ and $(i, \theta, n-1) \xrightarrow{t=0 \wedge x_{n-1} > 1, \text{upd}, \emptyset} (q', n-1)$ if $\theta = (q, \alpha, \alpha', \delta, q')$, and δ is L .

Fig. 4 shows an example simulation for a transition $\theta = (q, a, b, R, q')$ when $1 < i < n$.

The initialization of the tape with the input word w_0 can be encoded by adding a transition $\text{init} \xrightarrow{t > 1, s_0, r_{w_0} \cup \{t\}} (q_0, 1)$, where $r_{w_0} = \{x_i \mid w_0(i) = a\}$. To detect the accepting run, we add transitions $(q_F, i) \xrightarrow{\text{accept}} \text{end}$ for every i . Clearly the node end is reachable in $A_{\mathcal{M}, w_0}$ iff \mathcal{M} accepts w_0 .

The number of states of $A_{\mathcal{M}, w_0}$ is in $O(|Q| \cdot n + |T| \cdot n^2)$. The number of transitions is in $O(|T| \cdot n^2)$.

Since $A_{\mathcal{M}, w_0}$ can obviously be built in polynomial time from \mathcal{M} and w_0 , we have thus proven that the acceptance problem for LBTMs can be reduced in polynomial time to the reachability problem in timed automata with restricted guards. Thus:⁵

Proposition D.1. *The reachability problem in timed automata with restricted guards is PSPACE-complete.*

This result has already been shown in [5,31], but our encoding will allow us to obtain results about model checking problems for the property languages we have studied in the main body of the paper without using constraints of the form $x - y \bowtie k$ and systems modelled by means of timed automata with restricted guards. Our results on this topic for the property languages with fixed point operators are summarized⁶ in Table 7. Sketches for the justifications for the claims may be found below:

A1 We offer a polynomial time reduction from the acceptance of an input w_0 by an LBTM \mathcal{M} to a model checking problem $A_{\mathcal{M}, w_0} \models \Phi$ where $A_{\mathcal{M}, w_0}$ is the timed automaton with restricted guards built as above, and Φ is an L_v formula used to encode

⁵ The PSPACE-membership comes from the PSPACE-membership of reachability for the more general timed automata considered in the main body of the paper.

⁶ Note that the complexity of the corresponding *nil* model checking problems remains open.

Table 7

Model checking w.r.t. restricted yimed automata: languages with fixed points

	L_v	$L_s + \langle a \rangle$	$L_s + \vee$	$L_s + \boxplus$	L_s
MC	EXPTIME-C	EXPTIME-C	EXPTIME-C	EXPTIME-C	PSPACE-C
	See A1	See A2	See A3	See A4	See A5

the behaviour of \mathcal{M} on input w_0 . We assume without loss of generality that we have a strict alternation of “or” and “and” states in \mathcal{M} ; moreover we remind the reader that we assume that the initial and final states of \mathcal{M} are “or” states.

The formula Φ describing the alternating behaviour of \mathcal{M} on input w_0 is

$$\mathbb{W}[s_0]\max\left(X, [\text{accept}]\text{ff} \wedge \underbrace{\mathbb{W}[s][\text{upd}] \cdots [\text{upd}]}_{(n-1)\text{times}} \boxplus \langle s \rangle \underbrace{\langle \text{upd} \rangle \cdots \langle \text{upd} \rangle}_{(n-1)\text{times}} X\right).$$

Note that this formula is appropriate because after any s -transition there exists a sequence of $n - 1$ upd-transitions. Now, \mathcal{M} accepts w_0 iff $A_{\mathcal{M}, w_0} \models \Phi$.

- A2** The following $L_s + \langle a \rangle$ formula can replace the previous one to express the alternating behavior of an LBTAM:

$$\mathbb{W}[s_0]\max\left(X, [\text{accept}]\text{ff} \wedge \underbrace{\mathbb{W}[s][\text{upd}] \cdots [\text{upd}]}_{(n-1)\text{times}} \left(z \text{ in } \mathbb{W}(z = 1 \Rightarrow \underbrace{\langle s \rangle \langle \text{upd} \rangle \cdots \langle \text{upd} \rangle}_{(n-1)\text{times}} X) \right) \right).$$

The sequence $\boxplus \langle s \rangle \cdots$ can be replaced by $z \text{ in } \mathbb{W}(z = 1 \Rightarrow \langle s \rangle \cdots$ since the delay between two s -transitions is 1 time unit and time is not progressing during the updating steps.

- A3** Given an LBTAM \mathcal{M} , we can assume (as for the proof of Proposition 19 in Appendix C) that for any control state q of \mathcal{M} , there are two transitions ($\theta_{a,1}^q$ and $\theta_{a,2}^q$) of the form (q, a, \dots) and two transitions ($\theta_{b,1}^q$ and $\theta_{b,2}^q$) of the form (q, b, \dots) . Let w_0 an input word and $n = |w_0|$. We reduce the acceptance problem of w_0 by \mathcal{M} to a model checking problem $A_{\mathcal{M}, w_0} \models \Phi$ where $A_{\mathcal{M}, w_0}$ is the timed automaton with restricted guards built as above except that the transitions of $A_{\mathcal{M}, w_0}$ which correspond to a $\theta_{a,j}^q$ transition of \mathcal{M} ($a \in \{a, b\}$ and $j \in \{1, 2\}$) with q being an “and” state are labeled by s_j . Then we can use the following $L_s + \vee$ formula:

$$\mathbb{W}[s_0]\max\left(X, [\text{accept}]\text{ff} \wedge \underbrace{\mathbb{W}[s][\text{upd}] \cdots [\text{upd}]}_{(n-1)\text{times}} \left(z \text{ in } \mathbb{W}(z = 1 \Rightarrow \underbrace{([s_1][\text{upd}] \cdots [\text{upd}] X \vee [s_2][\text{upd}] \cdots [\text{upd}] X)}_{(n-1)\text{times}}) \right) \right).$$

This is based on the fact that for any q and i , even if there are two transitions $(q, i) \xrightarrow{s_1}$, exactly one of them is enabled for a given configuration and then the universal modality $[s_1]$ can be used.

Moreover, during the “updating phase”, one and only one upd-transition is enabled and then $\langle \text{upd} \rangle$ can be replaced by $[\text{upd}]$.

Table 8

Model checking w.r.t. restricted timed automata: languages without fixed points

	L_v^-	$L_s^- + \langle a \rangle$	$L_s^- + \vee$	$L_s^- + \exists$	L_s^-
MC	PSPACE-C	PSPACE-C	coNP-C	PSPACE-C	coNP-C
$nil - MC$	PSPACE-C	coNP-C	coNP-C	PSPACE-C	coNP-C

A4 The previous proof can be adapted to the $L_s + \exists$ case. We use the same TA $A_{\mathcal{M}, w_0}$ to simulate the behavior of \mathcal{M} over w_0 . Now we use the following formula:

$$\mathbb{W}[s_0]_{\max} \left(X, [\text{accept}]_{\text{ff}} \wedge \mathbb{W}[s][\text{upd}] \cdots [\text{upd}] \left[z \text{ in } \exists (z < 1 \wedge (z = 0 \Rightarrow \exists (z = 1 \wedge [s_1][\text{upd}] \cdots [\text{upd}] X)) \wedge (z > 0 \Rightarrow \exists (z = 1 \wedge [s_2][\text{upd}] \cdots [\text{upd}] X))) \right] \right).$$

A5 The reachability problem in timed automata with restricted guards can be reduced in linear time to an L_s model checking problem w.r.t. timed automata with restricted guards (as in Theorem 16).

Table 8 presents the complexities of model checking for the fixed point free fragments of the property languages we considered in this appendix. In this case, the proofs of the lower bounds of the corresponding problems for languages with extended guards use only restricted ones, and can be used here. Moreover the upper bounds are obtained directly from complexities of the full formalisms.

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