

Report Lab 11

Nguyễn Khánh Nam

20225749

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Assignment 1

Code:

```
#-----
#   col 0x1 col 0x2 col 0x4 col 0x8
#
# row 0x1   0 1 2 3
#   0x11 0x21 0x41 0x81
#
# row 0x2   4 5 6 7
#   0x12 0x22 0x42 0x82
#
# row 0x4   8 9 a b
#   0x14 0x24 0x44 0x84
#
# row 0x8   c d e f
#   0x18 0x28 0x48 0x88
#
#-----
# command row number of hexadecimal keyboard (bit 0 to 3)
# Eg. assign 0x1, to get key button 0,1,2,3
# assign 0x2, to get key button 4,5,6,7
# NOTE must reassign value for this address before reading,
# eventhough you only want to scan 1 row
.eqv IN_ADDRESS_HEXa_KEYBOARD 0xFFFF0012
# receive row and column of the key pressed, 0 if not key pressed
# Eg. equal 0x11, means that key button 0 pressed.
# Eg. equal 0x28, means that key button D pressed.
.eqv OUT_ADDRESS_HEXa_KEYBOARD 0xFFFF0014
```

.data

n: .asciiz "\n"

.text

main:

```
li    $t1,    IN_ADDRESS_HEX_A_KEYBOARD
li    $t2,    OUT_ADDRESS_HEX_A_KEYBOARD
li    $t3,    0x01  # check row 4 with key 0, 1, 2, 3
li    $t4,    0x02  # check row 4 with key 4, 5, 6, 7
li    $t5,    0x04  # check row 4 with key 8, 9, A, B
li    $t6,    0x08  # check row 4 with key C, D, E, F
li    $t0, 0
```

polling:

beq \$t0, 100, exit

```
sb    $t3,    0($t1)          # must reassign expected row
lb    $a0,    0($t2)          # read scan code of key button
bne   $a0, $0, print
```

```
sb    $t4,    0($t1)          # must reassign expected row
lb    $a0,    0($t2)          # read scan code of key button
bne   $a0, $0, print
```

```
sb    $t5,    0($t1)          # must reassign expected row
lb    $a0,    0($t2)          # read scan code of key button
bne   $a0, $0, print
```

```
sb    $t6,    0($t1)          # must reassign expected row
lb    $a0,    0($t2)          # read scan code of key button
bne   $a0, $0, print
```

j continue

print:

```
li    $v0,    34              # print integer (hexa)
syscall
```

```
la    $a0, n
li    $v0, 4
syscall
```

continue:

add \$t0, \$t0, 1

sleep:

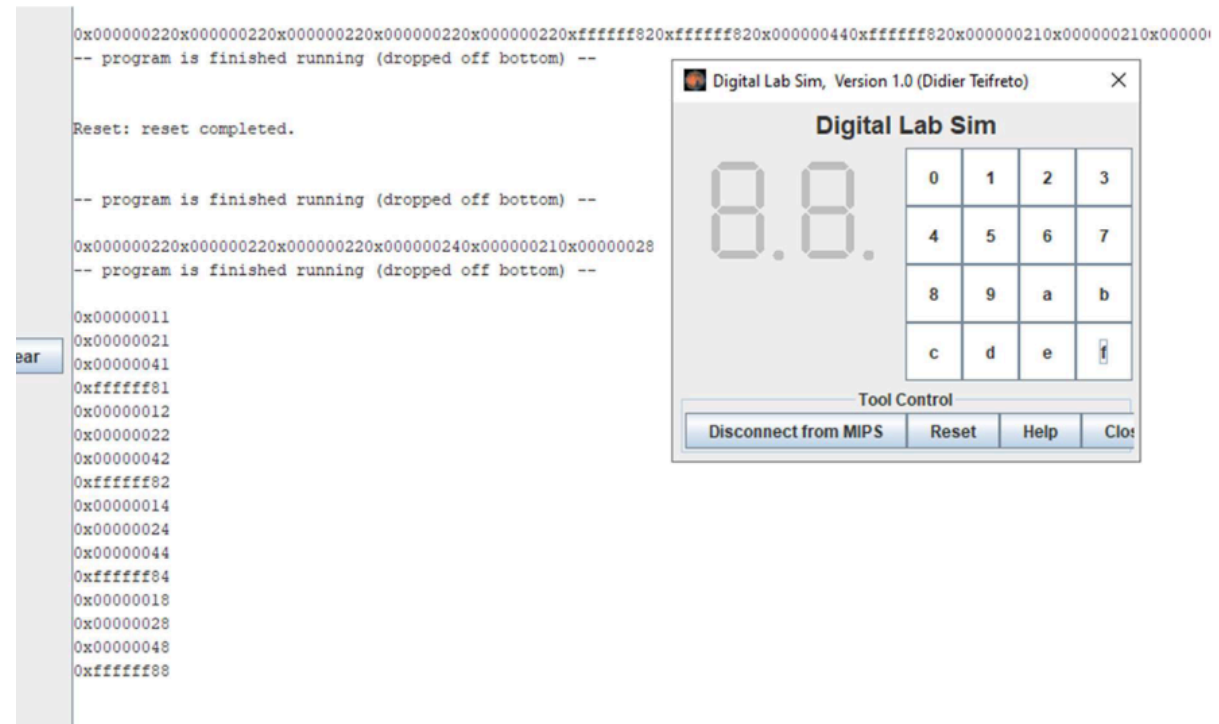
li \$a0, 5000 #5s nhận 1 giá trị nhập vào

```
li $v0, 32
syscall
```

```
back_to_polling:
    j    polling          # continue polling
```

```
exit:
```

Result:



Assignment 2

Code:

```
.eqv IN_ADDRESS_HEXKEYBOARD 0xFFFF0012
```

```
.data
```

```
Message: .asciiz "Oh my god. Someone's pressed a button.\n"
```

```
#~~~~~
```

```
# MAIN Procedure
```

```
#~~~~~
```

```
.text
```

```
main:
```

```
#-----
```

```
# Enable interrupts you expect
```

```
#-----
```

```
# Enable the interrupt of Keyboard matrix 4x4 of Digital Lab Sim
    li $t1, IN_ADDRESS_HEXA_KEYBOARD
    li $t3, 0x80 # bit 7 of = 1 to enable interrupt
    sb $t3, 0($t1)
```

```
#-----
```

```
# No-end loop, main program, to demo the effective of interrupt
```

```
#-----
```

```
Loop:  nop
```

```
        nop
```

```
        addi $v0, $zero, 32
```

```
        li $a0, 200
```

```
        syscall
```

```
        nop
```

```
        nop
```

```
        b Loop # Wait for interrupt
```

```
end_main:
```

```
#~~~~~
```

```
# GENERAL INTERRUPT SERVED ROUTINE for all interrupts
```

```
#~~~~~
```

```
.ktext 0x80000180
```

```
#-----
```

```
# Processing
```

```
#-----
```

```
IntSR:
```

```
    addi $v0, $zero, 4 # show message
```

```
    la $a0, Message
```

```
    syscall
```

```
#-----
```

```
# Evaluate the return address of main routine
```

```
# epc <= epc + 4
```

```
#-----
```

```
next_pc:
```

```
    mfc0 $at, $14 # $at <= Coproc0.$14 = Coproc0.epc
```

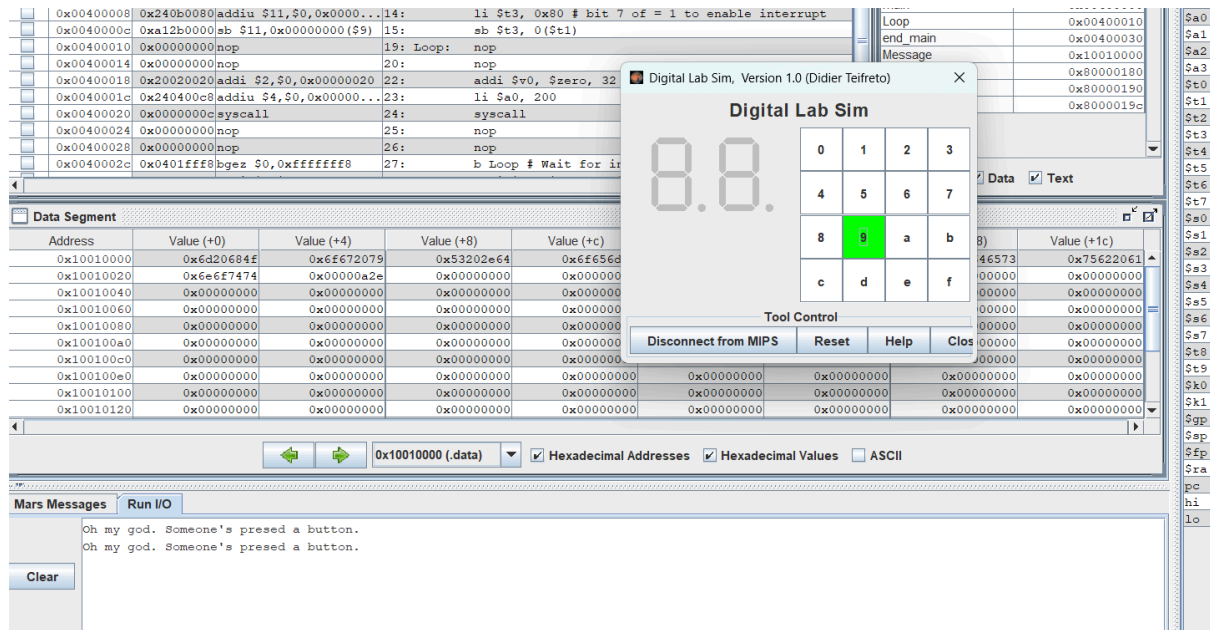
```
    addi $at, $at, 4 # $at = $at + 4 (next instruction)
```

```
    mtc0 $at, $14 # Coproc0.$14 = Coproc0.epc <= $at
```

```
return:
```

```
    eret # Return from exception
```

```
Result:
```



Assignment 3

Code:

```
.eqv IN_ADDRESS_HEXKEYBOARD 0xFFFF0012
.eqv OUT_ADDRESS_HEXKEYBOARD 0xFFFF0014
```

.data

Message: .asciiz "Key scan code "

```
#~~~~~
# MAIN Procedure
#~~~~~
```

.text

main:

```
#-----
# Enable interrupts you expect
#-----
# Enable the interrupt of Keyboard matrix 4x4 of Digital Lab Sim
li $t1, IN_ADDRESS_HEXKEYBOARD
li $t3, 0x80 # bit 7 = 1 to enable
sb $t3, 0($t1)
#-----
# Loop and print sequence numbers
#-----
```

```
xor $s0, $s0, $s0 # count = $s0 = 0
```

```
Loop: addi $s0, $s0, 1 # count = count + 1
```

```
prn_seq: addi $v0, $zero, 1
         add $a0, $s0, $zero # print auto sequence number
         syscall
```

```
prn_eol: addi $v0, $zero, 11
```

```

    li    $a0, '\n'    # print endofline
    syscall
sleep:    addi   $v0, $zero, 32
    li    $a0, 300      # sleep 300 ms
    syscall
    nop                    # WARNING: nop is mandatory here.
    b     Loop           # Loop
end_main:
#~~~~~
# GENERAL INTERRUPT SERVED ROUTINE for all interrupts
#~~~~~
    .ktext 0x80000180
#-----
# SAVE the current REG FILE to stack
#-----
IntSR:    addi   $sp, $sp, 4 # Save $at because we may change it later
    sw    $at, 0($sp)
    addi   $sp, $sp, 4      # Save $sp because we may change it later
    sw    $v0, 0($sp)
    addi   $sp, $sp, 4      # Save $a0 because we may change it later
    sw    $a0, 0($sp)
    addi   $sp, $sp, 4      # Save $t1 because we may change it later
    sw    $t1, 0($sp)
    addi   $sp, $sp, 4      # Save $t3 because we may change it later
    sw    $t3, 0($sp)
#-----
# Processing
#-----
prn_msg:  addi   $v0, $zero, 4
    la    $a0, Message
    syscall
get_cod:
    li    $t1, IN_ADDRESS_HEX_KEYBOARD
    li    $t3, 0x81          # check row 4 and re-enable bit 7
    sb    $t3, 0($t1)        # must reassign expected row
    li    $t1, OUT_ADDRESS_HEX_KEYBOARD
    lb    $a0, 0($t1)
    bne   $a0, $0, prn_cod #check

        li    $t1, IN_ADDRESS_HEX_KEYBOARD
    li    $t3, 0x82          # check row 4 and re-enable bit 7
    sb    $t3, 0($t1)        # must reassign expected row
    li    $t1, OUT_ADDRESS_HEX_KEYBOARD
    lb    $a0, 0($t1)
    bne   $a0, $0, prn_cod #check

        li    $t1, IN_ADDRESS_HEX_KEYBOARD
    li    $t3, 0x84          # check row 4 and re-enable bit 7

```

```

sb    $t3, 0($t1)                # must reassign expected row
li    $t1, OUT_ADDRESS_HEXA_KEYBOARD
lb    $a0, 0($t1)
bne $a0, $0, prn_cod #check

        li    $t1, IN_ADDRESS_HEXA_KEYBOARD
li    $t3, 0x88                    # check row 4 and re-enable bit 7
sb    $t3, 0($t1)                # must reassign expected row
li    $t1, OUT_ADDRESS_HEXA_KEYBOARD
lb    $a0, 0($t1)
bne $a0, $0, prn_cod #check

```

```

prn_cod:    li    $v0, 34
            syscall
            li    $v0, 11
            li    $a0, '\n'    # print end of line
            syscall
            #-----
            # Evaluate the return address of main routine
            # epc <= epc + 4
            #-----
next_pc:    mfc0    $at, $14    # $at <= Coproc0.$14 = Coproc0.epc
            addi    $at, $at, 4    # $at = $at + 4 (next instruction)
            mtc0    $at, $14    # Coproc0.$14 = Coproc0.epc <= $at
            #-----
            # RESTORE the REG FILE from STACK
            #-----
restore:    lw    $t3, 0($sp)    # Restore the registers from stack
            addi    $sp, $sp, -4
            lw    $t1, 0($sp)    # Restore the registers from stack
            addi    $sp, $sp, -4
            lw    $a0, 0($sp)    # Restore the registers from stack
            addi    $sp, $sp, -4
            lw    $v0, 0($sp)    # Restore the registers from stack

            addi    $sp, $sp, -4
            lw    $at, 0($sp)    # Restore the registers from stack
            addi    $sp, $sp, -4
return:    eret    # Return from exception

```

Result:

Assignment 4

Code:

Result: