# Parallel Algorithms for Kernel Density Estimation

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## Abstract

Keywords: Kernel Density Estimation, Parallel computing, Cuda

#### 1. Introduction

- Project Motivation
- Brief literary review KDE (See References Below)
- Brief literary review Parallized KDE (See References Below)

## 2. Multi-Core Programming Models

• MPI

Basic Review of MPI Architecture

• GPU

Basic Review of GPU Architecture

## 3. Serial vs. Vectorized vs. Multi-Core Kernel Density Estimation

- Serial Algorithm Pseudo Code
- SIMD Algorithm Pseudo Code
- MPI Algorithm Pseudo Code

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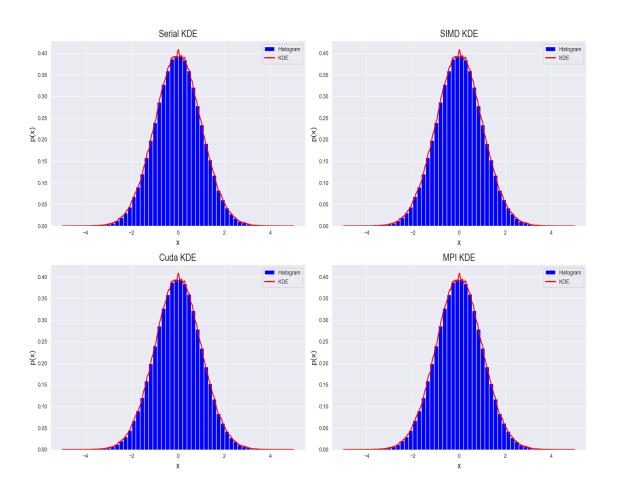


Figure 1: Kernel Density Estimates for all four proposed algorithms

## • Cuda Algorithm Pseudo Code

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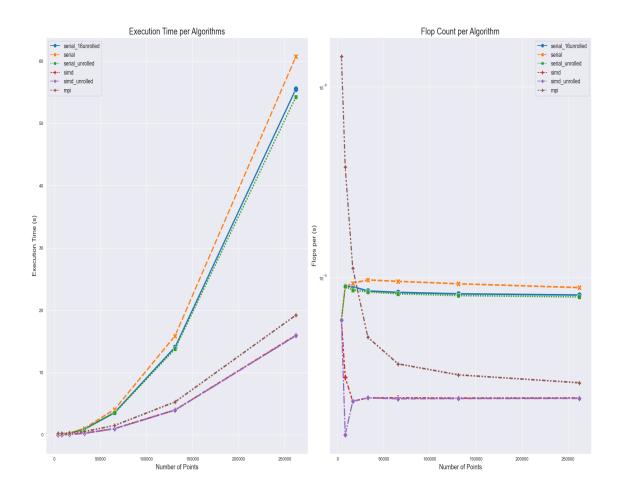


Figure 2: Execution times (in secs) of a kernel estimation as a function of the grid points.

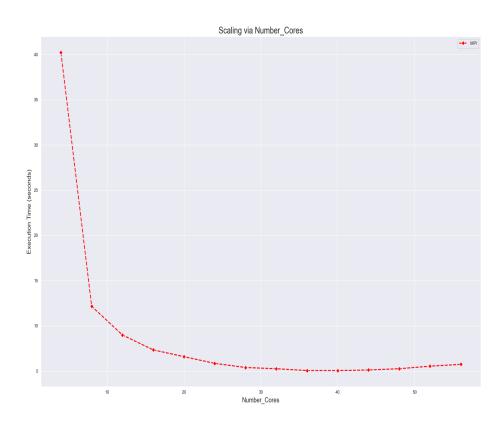


Figure 3: Execution times (in secs) of a kernel estimation as a function of the number of cores.

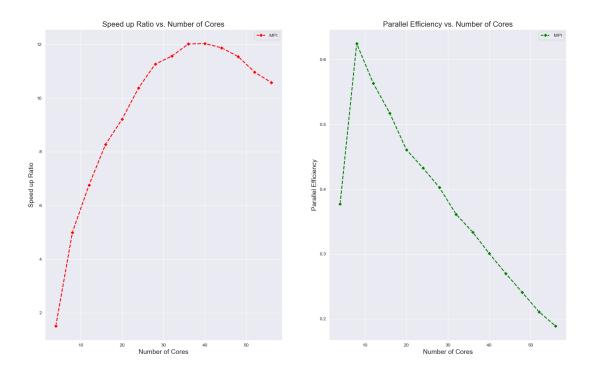


Figure 4: Performance of the MPI-parallel algorithm for KDE on the test data set (n=?, m=?): (a) speed up ratio and (b) parallel efficiency - defined as speedup ratio divided by number of CPU cores.

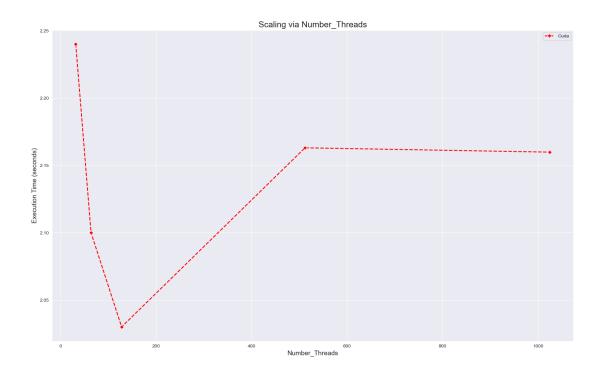


Figure 5: Scaling of the CUDA-parallel algorithm for KDE  $\,$ 

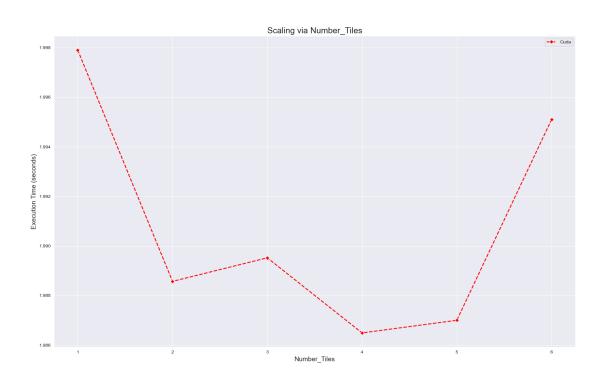


Figure 6: Scaling of the CUDA-parallel algorithm for KDE  $\,$ 

Kernel Density Algorithm	Serial	SIMD	MPI	Cuda
Lines of code	12	17	10	12

Table 1: Lines of code in Programming Models

### 4. Results

- 4.1. Quantititative Comparison
- 4.2. Qualitative Comparison

### 5. Conclusions

### References

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