Israr Sheikh

- Israr Sheikh is the Intel PSG Field Application Engineering Manager for India, ASEAN and ANZ Region, supporting FPGA Products.
- Israr lead the customer support, enablement and co-architect the Innovation products that target emerging FPGA usage models across the Embedded, Telecom, Network, Compute and Custom Designs. FPGA based Hardware Accelerators, Heterogeneous Co-processing, Edge and AI are the recent customer enablement programs which he is managing. He also participates in Intel India Site Technology office Innovation programs, startup-eco system enablement, Academia & Research for FPGA and System Design. Israr is frequent speaker and Intel and industry events.
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Padmanaban K

- Padmanaban has a post graduate degree in Applied Electronics from Anna University, Chennai. He has 14+ years of experience in digital design for both FPGA and ASIC.

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