

Designing CMOS-based XOR Gate

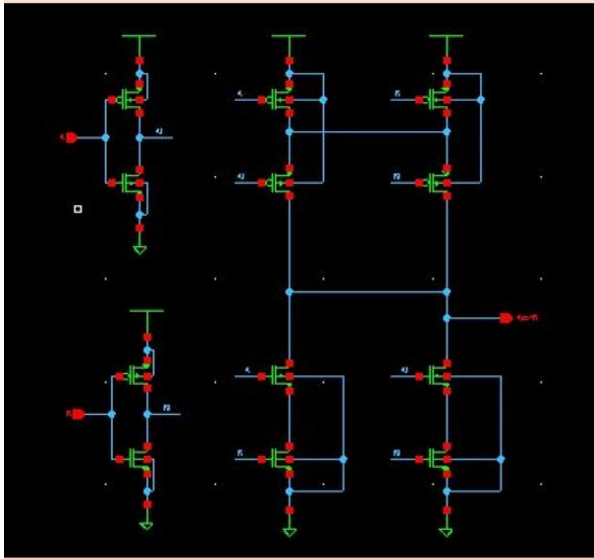


Fig 1 : Schematic Capture

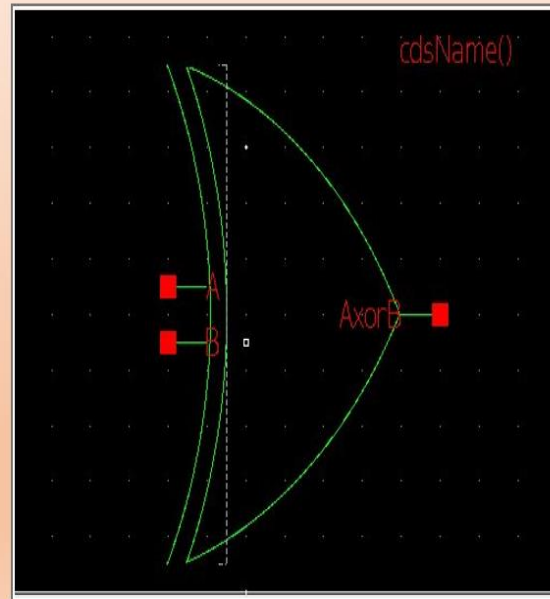


Fig 2 : Symbol creation of XOR gate

4.2 Designing Binary To Gray Code

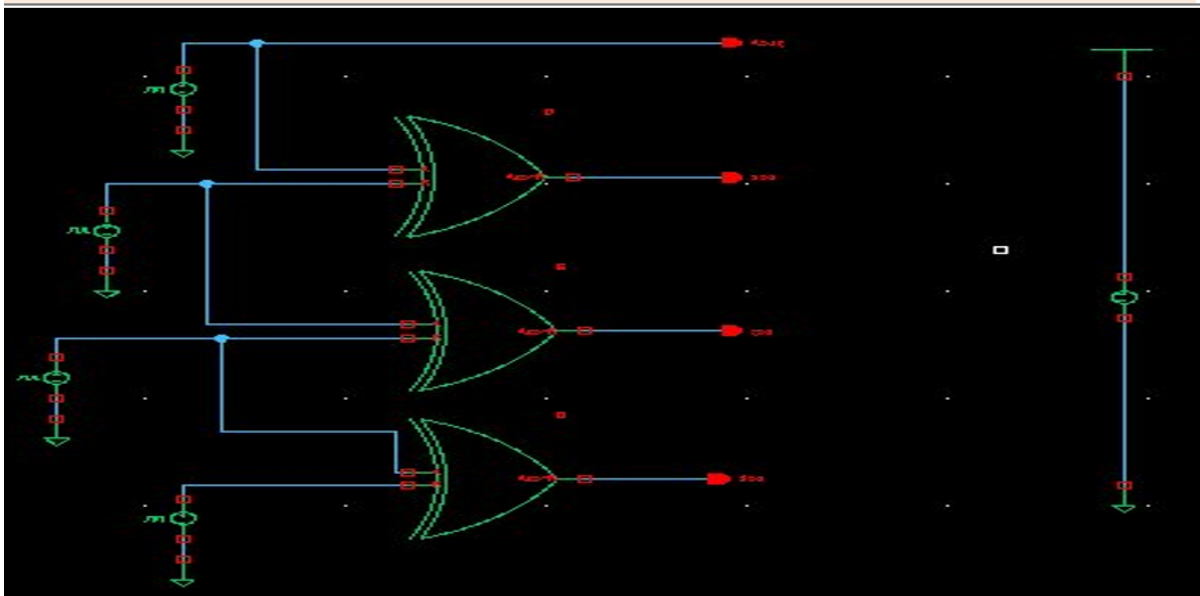


Fig 3 : Binary To Gray code Converter Logic diagram

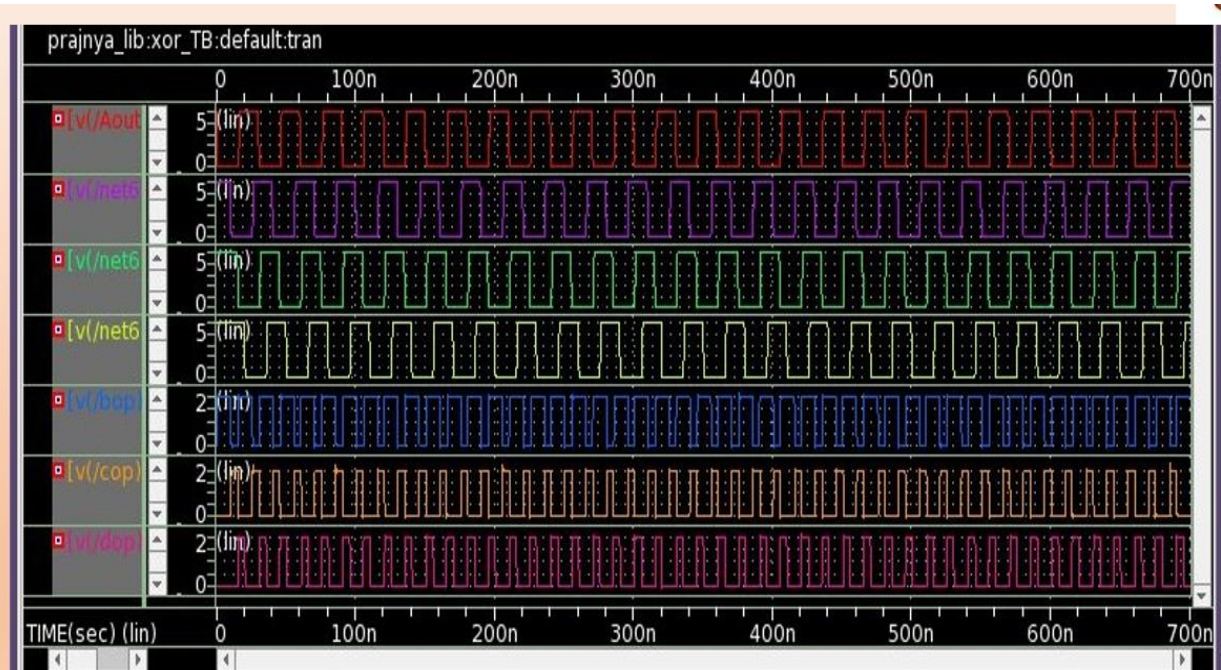


Fig 4 : output waveforms of Binary to gray code convertor