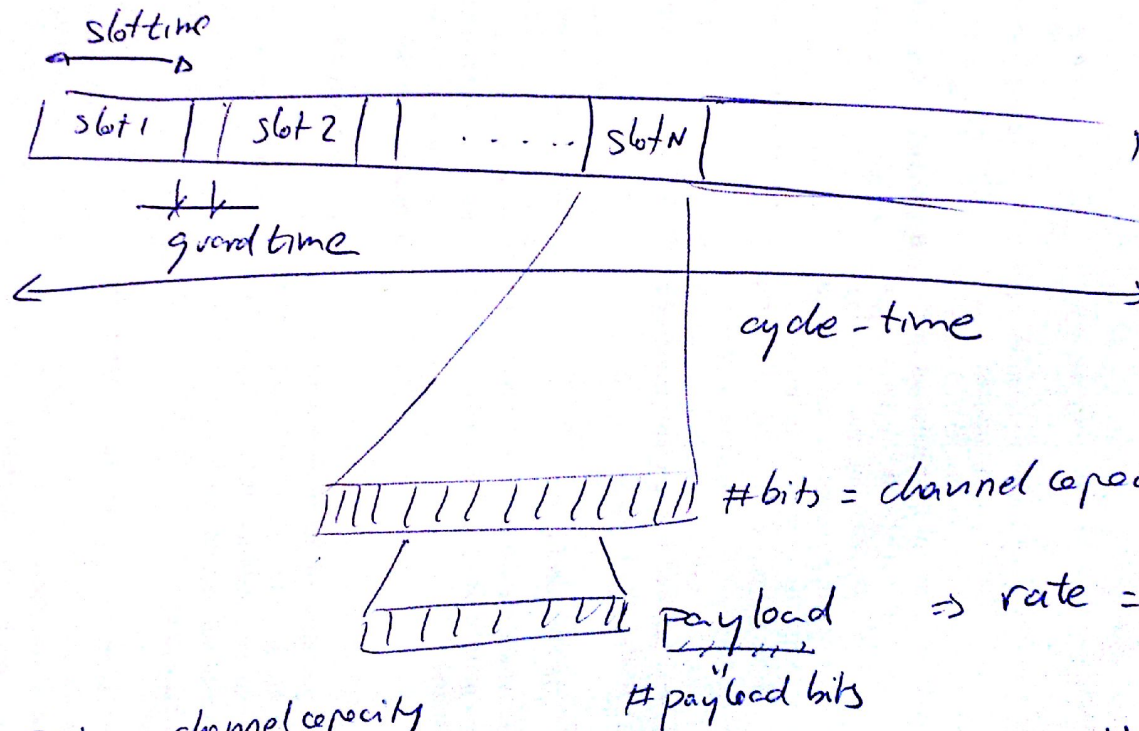


TPMS BUS monitor

checks or constraints.
 $\# \text{slots} \cdot \text{slot time} \leq \text{cycle time}$

slots



• slot BW = $\frac{\text{channel capacity}}{\text{cycle time}}$

• slot effective BW (eBW) = $\frac{\text{payload bits}}{\text{cycle time}}$

• bus effective BW (eBW) = $\frac{\# \text{slots} \cdot \text{payload bits}}{\text{cycle time}}$

Note: TX line speed is related but not the bus BW (when bus BW = $\# \text{slots} \cdot \text{slot capacity}$). Notice that several samples are to be used for each symbol at the physical layer.