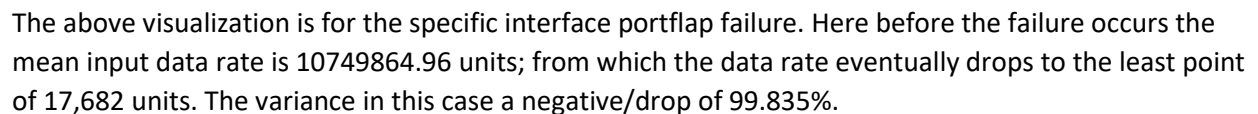


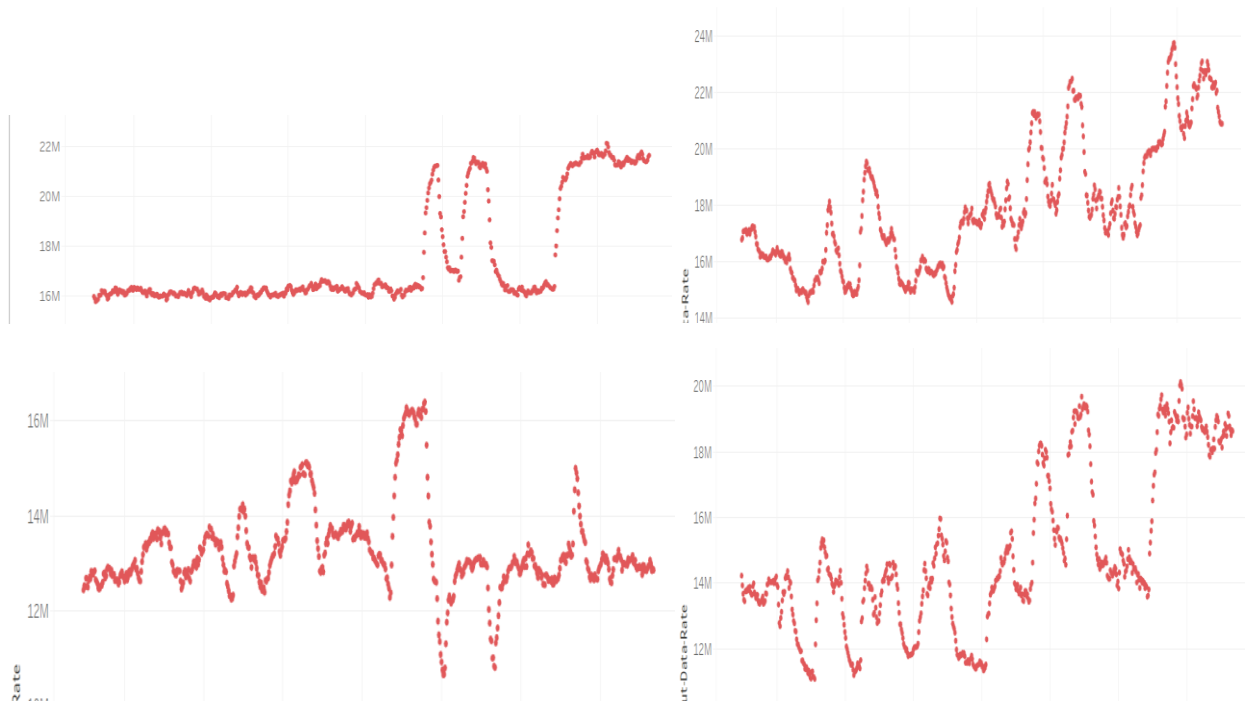
## EDA – Insight on Portflap Failure

Date of Submission: 2/21/18

To have the same previous semester model work for this semester as well, we will need to understand how the % changes (or variances) in the input data rates occur.



Looking at a couple of other ports(for the same spine2 producer) for the input data rate visualizations: -



We can calculate the variance in the negative/drop is anywhere in the range of 42.49%, 15.86%, 34.87%. Thus, we can (with a certain degree of confidence) conclude that any average percentage drop(variance) of about more than 50% over a time interval of more than 5 to 10 seconds will imply a port-flap failure with a high level of confidence.

This result will need to be cross-tallied with the results obtained by the other students and eventually tune the model developed in the previous semester.