Pseudo Code

Initialize the required register
and the relevent Pins using
THIS.x and LAT.x
↓
Have Pin RA7 set to ON at all
times
↓
Start a loop to start
the counting the 6-bit
numbers.
↓
Delay 250ms after each
count and reset counter
when counter reaches 6-bits.

Schematic

VDD

10K

100 Ω

0·1µF

MCLR/Vpp/RE3 □ 1

470 Ω RA0 □ 2

470 Ω RA1 □ 3

470 Ω RA2 □ 4

470 Ω RA3 □ 5

RA4 □ 6

470 Ω RA5 □ 7

470 Ω Vss □ 8

RA7 □ 9

470 Ω RA6 □ 10

RC0 □ 11

RC1 □ 12

RC2 □ 13

RC3 □ 14

28 □ RB7/ICSPDAT

27 □ RB6/ICSPCLK

26 □ RB5

25 □ RB4

24 □ RB3

23 □ RB2

22 □ RB1

21 □ RB0

20 □ VDD

19 □ Vss

18 □ RC7

17 □ RC6

16 □ RC5

15 □ RC4